

APPENDIX A

IMPLICIT MAPPING OF TECHNOLOGY INDEPENDENT NETWORK TO LIBRARY CELLS

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```
File: gnlutil.c

Version: 1.1

Modifications: -
/*
/*
/*
    Documentation: -
/*
/*
  Description:
/*-----
#include <stdio.h>
#include <sys/time.h>
#ifdef MEMORY
               /st If one wants memory statistics for SUN st/
#include <malloc.h>
#endif
#include "blist.h"
#include "gnl.h"
#include "gnlmint.h"
#include "bbdd.h"
#include "libc_mem.h"
#include "libc api.h"
#include "gnllibc.h"
#include "gnloption.h"
#include "blist.e"
/*----*/
/* EXTERN
/*----*/
extern GNL_ENV G_GnlEnv;
extern LIBC_PIN GnlGetPinCellWithName ();
/*----*/
/* GnlFreeVar
.
/*-----*/
/* Physical destruction of the Gnl Variable 'Var'. */
/*----*/
void GnlFreeVar (Var)
 GNL_VAR Var;
 free (GnlVarName (Var));
 if (GnlVarLocation (Var))
   free (GnlVarLocation (Var));
 free (Var);
}
/* SetGnlVarLineNumber
/st This procedure builds the string representing the line number where st/
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/* the var 'Var' is defined and stores it in the field 'GnlVarLocation' */
/* of the Var. This string can generally represent several line numbers */
/* of different source files.
/*----*/
GNL STATUS SetGnlVarLineNumber (Var, Line)
  GNL VAR Var;
             Line;
             *CompLocString;
  char
  char
             *LocString;
  if ((LocString = (char*)calloc (128, sizeof(char))) == NULL)
    return (GNL MEMORY FULL);
  sprintf (LocString, "%d ", Line);
  if ((CompLocString = (char*)
                calloc (strlen(LocString)+1, sizeof(char))) == NULL)
    return (GNL_MEMORY_FULL);
  sprintf (CompLocString, "%s", LocString);
  free (LocString);
  SetGnlVarLocation (Var, CompLocString);
  return (GNL OK);
/*----*/
/* GnlVarLineNumber
/*----*/
/* This procedure extracts the line number of the var 'Var' from the  */
/* location string stored in field 'GnlVarLocation'. By calling this */
/* procedure we assume that the string stores only one integer (should */
/* have been set by 'SetGnlVarLineNumber' above).
/*----*/
int GnlVarLineNumber (Var)
  GNL VAR Var;
  char LocString[128];
  int
             i;
  int
             Line;
  if (!GnlVarLocation (Var))
    return (0);
  i = 0;
  while ((i<strlen (GnlVarLocation (Var)) &&
        (GnlVarLocation (Var)[i] != ' ')))
       LocString[i] = GnlVarLocation (Var)[i];
       i++;
```

```
LocString[i] = '\0';
 Line = atoi (LocString);
 return (Line);
/* GnlResetVarFunction
/* Reset the field 'GnlVarFunction' of the variable 'Var'. */
void GnlResetVarFunction (Var)
 GNL VAR Var;
 SetGnlVarFunction (Var, NULL);
/* GnlFunctionFree
/* Procedure which physically free the structure pointed by Function */
/* which is of type GNL_FUNCTION_REC
void GnlFunctionFree (Function)
 GNL FUNCTION Function;
 free ((char*) Function);
/* GnlSystemGetTimeOfDay
                                         */
/* ------ */
void GnlSystemGetTimeOfDay (Seconds, MicroSeconds)
 long *Seconds; /* seconds since Jan. 1, 1970
 long *MicroSeconds;
                 /* and microseconds
 struct timeval tp;
 struct timezone tzp;
 (void) gettimeofday (&tp, &tzp);
 *Seconds = tp.tv_sec;
 *MicroSeconds = tp.tv_usec;
}
/* GnlTimeGetAbsoluteTime
/* ----- */
/* returns the elapsed time in seconds since January, 1, 1970
long GnlTimeGetAbsoluteTime ()
 long
     Seconds;
     MicroSeconds;
 long
```

```
GnlSystemGetTimeOfDay (&Seconds, &MicroSeconds);
 return (Seconds);
}
/* GnlSystemGetLocalTime
/* Seconds (0 - 59)
                                                 */
/* Minutes (0 - 59)
                                                 */
/* Hours (0 - 23)
                                                 */
/* Day : day of month (1 - 31)
                                                 */
/* Month : month of year (0 - 11)
                                                 */
/* Year : year - 1900
                                                 */
/* ----- */
void GnlSystemGetLocalTime (Year, Month, Day, Hours, Minutes, Seconds)
  int *Year;
  int *Month;
  int *Day;
  int *Hours;
  int *Minutes;
 int *Seconds;
  struct tm *CurDate;
  long
          Time;
 Time = GnlTimeGetAbsoluteTime ();
CurDate = localtime (&Time);
 *Year = CurDate->tm year;
 *Month = CurDate->tm_mon + 1;
 *Day = CurDate->tm_mday;
  *Hours = CurDate->tm hour;
 *Minutes = CurDate->tm min;
 *Seconds = CurDate->tm_sec;
}
/* GnlPrintDate
                                               */
/* Prints the date and time under format "04/20/99 @ 11:15:30" in the */
/* file 'File'.
                                        */
void GnlPrintDate (File)
           *File;
 FILE
 int Year;
 int Month;
 int Day;
 int Hours;
 int Minutes;
 int Seconds;
 GnlSystemGetLocalTime (&Year, &Month, &Day, &Hours, &Minutes, &Seconds);
```

```
fprintf (File, "%.2d/%.2d/%d @ %.2d:%.2d:%.2d", Month, Day, Year,
        Hours, Minutes, Seconds);
}
/*----*/
/* GnlVarIsIO
/*----*/
/st Returns 1 if the current var 'Var' is either a primary INPUT, or a st/
/* primary OUTPUT or a primary INOUT
int GnlVarIsIO (Var)
 GNL_VAR
        Var;
 if (GnlVarDir (Var) == GNL_VAR_INPUT)
   return (1);
 if (GnlVarDir (Var) == GNL_VAR_OUTPUT)
   return (1);
  if (GnlVarDir (Var) == GNL VAR INOUT)
   return (1);
 return (0);
/*-----/
/* GnlVarIsPrimary
/*----*/
/* Returns 1 if the current var 'Var' is a primary var that means which */
/* cannot be removed at anytime (except for GNL VAR LOCAL WIRING when
/* they are re-injected during a hierarchycal flattening). */
/*-----/
int GnlVarIsPrimary (Var)
 GNL VAR Var;
 if (GnlVarDir (Var) == GNL VAR INPUT)
   return (1);
 if (GnlVarDir (Var) == GNL_VAR_OUTPUT)
   return (1);
 if (GnlVarDir (Var) == GNL_VAR_INOUT)
   return (1);
 if (GnlVarDir (Var) == GNL VAR LOCAL WIRING)
   return (1);
 return (0);
}
/*-----*/
/* GnlVarRangeSize
/* Function returning the size of the range of the var 'Var'. A single */
/* bit var has a size range of 1 and for instance a bus (3 downto 0) */
/* has a range size of 4.
int GnlVarRangeSize (Var)
 GNL VAR Var;
```

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if (GnlVarMsb (Var) >= GnlVarLsb (Var))
   return (GnlVarMsb (Var) - GnlVarLsb (Var) + 1);
   return (GnlVarLsb (Var) - GnlVarMsb (Var) + 1);
/*----*/
/* GnlDirName
/*----*/
/* Returns a verilog-semantic string corresponding to the direction of */
/* the var 'Var'.
char *GnlDirName (Var)
 GNL_VAR Var;
 switch (GnlVarDir (Var)) {
   case GNL_VAR_INPUT: return ("input");
   case GNL_VAR_OUTPUT: return ("output");
   case GNL_VAR_INOUT: return ("inout");
   default:
        fprintf (stderr,
           " ERROR: cannot find the direction of variable '%s'\n",
           GnlVarName (Var));
        exit (1);
    }
/* GnlSegComponentIsDFF
/*----*/
/* Returns 1 if the segnetial component 'SegComponent' is a Flip-Flop */
int GnlSeqComponentIsDFF (SeqComponent)
 GNL SEQUENTIAL COMPONENT SegComponent;
 if ((GnlSequentialCompoOp (SeqComponent) == GNL DFF) | |
    (GnlSequentialCompoOp (SeqComponent) == GNL DFFX) |
    (GnlSequentialCompoOp (SeqComponent) == GNL_DFF0)
    (GnlSequentialCompoOp (SeqComponent) == GNL DFF1))
   return (1);
 return (0);
}
/* GnlSeqComponentIsLATCH
/*-----/
/* Returns 1 if the sequetial component 'SeqComponent' is a LATCH */
/* and 0 otherwise.
/*----*/
int GnlSegComponentIsLATCH (SegComponent)
 GNL SEQUENTIAL COMPONENT SeqComponent;
```

```
if ((GnlSequentialCompoOp (SeqComponent) == GNL LATCH) |
    (GnlSequentialCompoOp (SeqComponent) == GNL_LATCHX)
    (GnlSequentialCompoOp (SeqComponent) == GNL_LATCH1) |
    (GnlSequentialCompoOp (SeqComponent) == GNL_LATCH0))
    return (1);
 return (0);
/*----*/
/* GnlVarRangeUndefined
/* returns 1 if the range of the var 'Var' is not defined (e.g. its Msb */
int GnlVarRangeUndefined (Var)
 GNL VAR Var;
 if ((GnlVarMsb (Var) == -1) && (GnlVarLsb (Var) == -1))
   return (1);
 return (0);
/*----*/
/* GnlVarGetStrFromRange
/*---*/
/* Returns a string thru 'RangeStr' which is the verilog-format */
/* representation of the range of the variable 'Var'. */
/*----*/
GNL STATUS GnlVarGetStrFromRange (Var, RangeStr)
 GNL VAR Var;
           **RangeStr;
 char
  if (GnlVarRangeUndefined (Var))
    *RangeStr = NULL;
    return (GNL OK);
  if ((*RangeStr = (char*)GNL CALLOC (256, sizeof(char))) == NULL)
    return (GNL_MEMORY_FULL);
  sprintf ((*RangeStr), "[%d:%d]", GnlVarMsb (Var), GnlVarLsb (Var));
 return (GNL_OK);
/*----*/
/*----*/
/* This function creates physically a new name which is the concatanate */
/* of '[', the Index and ']'.
/* Example: Name = "bus" , Index = 0 then *NewStr = "bus[0]".
```

```
GNL_STATUS GnlNameIndexName (Str, Index, NewStr)
          *Str;
   int
               Index;
   char
               **NewStr;
   char *Strl;
   char *Str2;
   if (GnlEnvInputFormat() == GNL_INPUT VLG)
      if (GnlStrAppendStrCopy (Str, "[", &Str1))
         return (GNL MEMORY FULL);
   else
      if (GnlStrAppendStrCopy (Str, "(", &Str1))
        return (GNL_MEMORY_FULL);
   if (GnlStrAppendIntCopy (Str1, Index, &Str2))
     return (GNL_MEMORY_FULL);
   if (GnlEnvInputFormat() == GNL_INPUT VLG)
      if (GnlStrAppendStrCopy (Str2, "]", NewStr))
        return (GNL_MEMORY_FULL);
  else
     if (GnlStrAppendStrCopy (Str2, ")", NewStr))
        return (GNL_MEMORY_FULL);
  free (Str1);
  free (Str2);
  return (GNL_OK);
/*----*/
/* GnlVarIndexName
/*-----*/
/st This function creates physically a new name which is the concatanate st/
/* of '[', the Index and ']' or '(' ')' in fsm mode.
/* Example: Var = "bus" , Index = 0 then *NewStr = "bus[0]".
/*----*/
GNL_STATUS GnlVarIndexName (Var, Index, NewStr)
  GNL_VAR Var;
  int
              Index;
  char
              **NewStr;
  if (GnlNameIndexName (GnlVarName (Var), Index, NewStr))
    return (GNL_MEMORY FULL);
  return (GNL_OK);
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}
/*----*/
/* GnlVarStrNameStrIndex
/*----*/
/\star Extracts Base name and Index from the name of an Index Var. The name \star/
/* of an index Var should of the forme: BASE_NAME[Index] */
/*----*/
GNL_STATUS GnlVarStrNameStrIndex (Str, VarName, Index)
      *Str;
  char
  char
         **VarName;
  int
         *Index;
  int
        LengthStr;
  char
int
         *IndexName;
         i;
  int
         j;
  LengthStr = strlen (Str);
  if (Str[LengthStr-1] != ']')
                          /* Possibly an index Var
     if (GnlStrCopy (Str, VarName))
        return (GNL_MEMORY FULL);
     *Index = -1;
     return (GNL_OK);
 if ((*VarName = (char*)GNL_CALLOC (LengthStr, sizeof(char))) == NULL)
   return (GNL MEMORY FULL);
 if ((IndexName = (char*)GNL_CALLOC (LengthStr, sizeof(char))) == NULL)
   return (GNL_MEMORY_FULL);
 i = 0;
 while (i < LengthStr-1)
     if (Str[i] == '[')
       break;
     (*VarName)[i] = Str[i];
     i++;
 (*VarName)[i] = '\setminus 0';
 j = 0;
while (i < LengthStr-1)
     if (Str[i] == ']')
       break;
     IndexName[j] = Str[i];
     j++;
     i++;
IndexName[j] = ' \ 0';
*Index = atoi (IndexName);
free (IndexName);
```

```
return (GNL OK);
/*----*/
/* GnlStrIndexName
/*----*/
/* This returns a string thru 'NewStr' which is the concatenation of the*/
/* signal name 'Str' '[' 'Index' ']'.
/* Example: Str = "x", Index = 2 ---> \text{NewStr} = "x[2]"
                                                 */
/*----*/
GNL_STATUS GnlStrIndexName (Str, Index, NewStr)
  char
        *Str;
  int
           Index;
           **NewStr;
  char
  char *Strl;
  char *Str2;
  if (GnlStrAppendStrCopy (Str, "[", &Str1))
    return (GNL_MEMORY_FULL);
  if (GnlStrAppendIntCopy (Str1, Index, &Str2))
    return (GNL_MEMORY_FULL);
  if (GnlStrAppendStrCopy (Str2, "]", NewStr))
    return (GNL_MEMORY_FULL);
  free (Str1);
  free (Str2);
  return (GNL OK);
/*-----*/
/* GnlGetVarFromName
/*----*/
/* We look for the object GNL_VAR whose name is 'Name' in the Hash Table*/
/* names. GNL_OK is returned if the object is found and it is assecible */
/* thru 'Var'. If does not exist then GNL_VAR_NOT_EXISTS is returned. */
/*-----*/
GNL_STATUS GnlGetVarFromName (Gnl, Name, Var)
  GNL
           Gnl;
  char
             *Name;
  GNL VAR *Var;
 BLIST
 BLIST NewLi GNL_VAR VarI;
           NewList;
 unsigned int Key;
 int
 BLIST
          HashTableNames;
 HashTableNames = GnlHashNames (Gnl);
```

```
/* The list GnlHashNames (Gnl) is of size HASH_TABLE_NAMES_SIZE
   Key = KeyOfName (Name, BListSize (HashTableNames));
   if (HashTableNames->Adress[Key] == (int)NULL)
     return (GNL VAR NOT EXISTS);
   NewList = (BLIST) (HashTableNames->Adress[Key]);
   for (i=0; i<BListSize (NewList); i++)</pre>
       VarI = (GNL_VAR)BListElt (NewList, i);
       if (!strcmp (GnlVarName (VarI), Name))
          *Var = VarI;
          return (GNL OK);
      }
   return (GNL_VAR_NOT_EXISTS);
}
/* GnlResetVarHook
/*----*/
/* Reset the field 'GnlVarHook' of all the Var of 'Gnl'.
void GnlResetVarHook (Gnl)
  GNL Gnl;
  int
            i;
  int
 BLIST
            BucketI;
 GNL VAR
            VarJ;
 BLIST
            HashTableNames;
  HashTableNames = GnlHashNames (Gnl);
  for (i=0; i<BListSize (HashTableNames); i++)</pre>
      BucketI = (BLIST)BListElt (HashTableNames, i);
      for (j=0; j<BListSize (BucketI); j++)</pre>
          VarJ = (GNL VAR)BListElt (BucketI, j);
           SetGnlVarHook (VarJ, NULL);
      }
}
/* GnlResetVarTag
/*----*/
/* Reset the field 'GnlVarTag' of all the Var of 'Gnl'.
/*----*/
void GnlResetVarTag (Gnl)
  GNL Gnl;
```

```
int
   int
               j;
   BLIST
               BucketI;
   GNL VAR
               VarJ;
   BLIST
               HashTableNames;
   HashTableNames = GnlHashNames (Gnl);
   for (i=0; i<BListSize (HashTableNames); i++)</pre>
       BucketI = (BLIST)BListElt (HashTableNames, i);
       for (j=0; j<BListSize (BucketI); j++)</pre>
            VarJ = (GNL_VAR)BListElt (BucketI, j);
            SetGnlVarTag (VarJ, 0);
}
                                                                   */
/* Reset the field 'GnlVarDads' of all the Var of 'Gnl'.
/*-----
void GnlResetVarDads (Gnl)
   GNL Gnl;
   int
              i;
  int
              j;
  BLIST
              BucketI;
  GNL VAR
              VarJ;
  BLIST
              HashTableNames;
  HashTableNames = GnlHashNames (Gnl);
   for (i=0; i<BListSize (HashTableNames); i++)</pre>
       BucketI = (BLIST)BListElt (HashTableNames, i);
       for (j=0; j<BListSize (BucketI); j++)</pre>
            VarJ = (GNL_VAR)BListElt (BucketI, j);
            SetGnlVarDads (VarJ, NULL);
      }
}
/*----*/
/* GnlResetGnlNetworkTagInGnl
void GnlResetGnlNetworkTagInGnl (Gnl)
  GNL
                Gnl;
  BLIST
                Components;
                      i;
  GNL_COMPONENT ComponentI;
  GNL_USER_COMPONENT UserCompol;
  GNL
                      GnlCompoI;
```

```
SetGnlTag (Gnl, 0);
  Components = GnlComponents (Gnl);
  for (i=0; i<BListSize (Components); i++)</pre>
      ComponentI = (GNL_COMPONENT)BListElt (Components, i);
      if (GnlComponentType (ComponentI) != GNL_USER_COMPO)
        continue;
      UserCompoI = (GNL_USER_COMPONENT) ComponentI;
      GnlCompoI = GnlUserComponentGnlDef (UserCompoI);
      if (!GnlCompoI)
        continue;
      GnlResetGnlNetworkTagInGnl (GnlCompoI);
}
/*----*/
/* GnlResetGnlNetworkTag
/*----*/
/* Resets the field tag of each Gnl of the current network 'Nw'. */
/*----*/
void GnlResetGnlNetworkTag (Nw)
  GNL NETWORK Nw;
  GNL
        TopGnl;
  TopGnl = GnlNetworkTopGnl (Nw);
  GnlResetGnlNetworkTagInGnl (TopGnl);
/*----*/
/* GnlAddVarInHashTablenames
/*----*/
/* We add the GNL_VAR 'Var' in the HashTableNames of 'Gnl' according to */
/* the Variable Name. It returns:
       - GNL_MEMORY_FULL if no more memory to extend the
/*
         Hash Table Names.
/*
        - GNL_VAR_EXISTS if the 'Var' is already present in
/*
         the Hash Table Names.
/*
        - GNL_OK if the 'Var' was not present and has been
/*-----/
GNL_STATUS GnlAddVarInHashTablenames (Gnl, Var)
  GNL
            Gnl;
  GNL VAR Var;
  BLIST
           NewList;
  GNL VAR
           VarI;
  unsigned int Key;
  int
           i;
```

```
BLIST
            HashTableNames;
   GNL STATUS GnlStatus;
  HashTableNames = GnlHashNames (Gnl);
   /* The list GnlHashNames (Gnl) is of size HASH_TABLE_NAMES SIZE
  Key = KeyOfName (GnlVarName (Var), BListSize (HashTableNames));
   if (HashTableNames->Adress[Key] == (int)NULL)
      if (BListCreateWithSize (1, &NewList))
        return (GNL MEMORY FULL);
      HashTableNames->Adress[Key] = (int)NewList;
  NewList = (BLIST) (HashTableNames->Adress[Key]);
  for (i=0; i<BListSize (NewList); i++)</pre>
       VarI = (GNL_VAR)BListElt (NewList, i);
       if (!strcmp (GnlVarName (VarI), GnlVarName (Var)))
          return (GNL_VAR_EXISTS);
  if (BListAddElt (NewList, (int)Var))
     return (GNL MEMORY FULL);
  return (GNL OK);
/*----*/
/* GnlAddCompoNameInHashTablenames
/*----*/
/* We add the string 'CompoName' in the HashTableCompoNames of 'Gnl'
/* if it does not exist. If not '*CompoName' takes the value of the
/* already existing compo name.
/*----*/
GNL_STATUS GnlAddCompoNameInHashTablenames (Gnl, CompoName)
  GNL
              Gnl;
  char
              **CompoName;
  BLIST
            NewList;
  char
              *CompoNameI;
  unsigned int Key;
  i;
BLIST Han
            HashTableCompoNames;
  GNL STATUS GnlStatus;
  HashTableCompoNames = GnlHashCompoNames (Gnl);
  /* The list GnlHashCompoNames (Gnl) is of size
  /* HASH_TABLE_COMPO NAMES SIZE.
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Key = KeyOfName ((*CompoName), HASH_TABLE_COMPO_NAMES_SIZE);
   if (HashTableCompoNames->Adress[Key] == (int)NULL)
      if (BListCreateWithSize (1, &NewList))
         return (GNL_MEMORY FULL);
      HashTableCompoNames->Adress[Key] = (int)NewList;
   NewList = (BLIST) (HashTableCompoNames->Adress[Key]);
   for (i=0; i<BListSize (NewList); i++)</pre>
       CompoNameI = (char*)BListElt (NewList, i);
       if (!strcmp (CompoNameI, (*CompoName)))
          free ((*CompoName));
         *CompoName = CompoNameI;
          return (GNL OK);
   if (BListAddElt (NewList, (int)(*CompoName)))
     return (GNL_MEMORY_FULL);
  return (GNL OK);
/*----*/
/* GnlCreateUniqueVarWithNoTest
/*----*/
/* This procedure creates a GNL_VAR object and stores it in the Hash
/* table names of 'Gnl'. The Var name is based on the name 'BaseName' */
/* but may be not unique since the unicity test is by-passed. This
                                                               */
/* function must be invoked if the caller knows that the base name he */
/* is giving is unique by construction.
/*----*/
static char G_NewUniqueName[528];
GNL_STATUS GnlCreateUniqueVarWithNoTest (Gnl, BaseName, NewVar)
  GNL
               Gnl;
  char
               *BaseName;
  GNL_VAR *NewVar;
  GNL STATUS
               GnlStatus;
  char
               *NewName;
  SetGnlUniqueId (Gnl, GnlUniqueId (Gnl)+1);
  sprintf (G_NewUniqueName, "\\%s_\%d", BaseName, GnlUniqueId (Gnl));
  if ((GnlStatus = GnlVarCreateAndAddInHashTableWithNoTest (Gnl,
                        G_NewUniqueName, NewVar)))
     return (GNL MEMORY FULL);
```

```
return (GNL_OK);
/*----*/
/* GnlCreateUniqueVarWithNoTestGnlId
/*----*/
/* This procedure creates a GNL_VAR object and stores it in the Hash
/* table names of 'Gnl'. The Var name is based on the name 'BaseName'
/* but may be not unique since the unicity test is by-passed. This
/* function must be invoked if the caller knows that the base name he */
/* is giving is unique by construction.
                                                          */
/* The difference with 'GnlCreateUniqueVarWithNoTest' is that the Gnl
/* Id used is the one given by 'Gnlld' and not the one from 'Gnl'. */
/*----*/
GNL STATUS GnlCreateUniqueVarWithNoTestGnlId (GnlId, Gnl, BaseName,
  GNL
           GnlId;
  GNL
           Gnl;
  char
           *BaseName:
  GNL_VAR *NewVar;
  GNL_STATUS GnlStatus;
  char
            *NewName;
  SetGnlUniqueId (GnlId, GnlUniqueId (GnlId)+1);
  sprintf (G_NewUniqueName, "\\%s_%d", BaseName, GnlUniqueId (GnlId));
  if ((GnlStatus = GnlVarCreateAndAddInHashTableWithNoTest (Gnl,
                         G NewUniqueName, NewVar)))
    return (GNL_MEMORY_FULL);
  return (GNL OK);
/*----*/
/* GnlCreateUniqueVar
/*----*/
/* This procedure creates a Unique GNL_VAR object and stores it in the */
/* Hash table names of 'Gnl'. The Var name is based on the name */
/* 'BaseName' and is unique. It can be either 'BaseName' if this one is st/
/* unique in 'Gn' or a derivated name. A derivated name is the
/* 'BaseName' concatanate with a unique Id.
/*----*/
GNL_STATUS GnlCreateUniqueVar (Gnl, BaseName, NewVar)
  GNL
              *BaseName;
  GNL VAR *NewVar;
  int
             ConflictName;
           GnlStatus;
  GNL STATUS
  /* If no based name specified then we take the Gnl one
                                                   */
  if (!BaseName)
    BaseName = GnlName (Gnl);
```

```
if ((GnlStatus = GnlVarCreateAndAddInHashTable (Gnl, BaseName,
                                      NewVar)))
     if (GnlStatus == GNL MEMORY FULL)
        return (GNL MEMORY FULL);
     ConflictName = 1;
  else
    return (GNL_OK);
  while (ConflictName)
    ConflictName = 0;
    SetGnlUniqueId (Gnl, GnlUniqueId (Gnl)+1);
    sprintf (G_NewUniqueName, "\\%s_%d", BaseName, GnlUniqueId (Gnl));
    if ((GnlStatus = GnlVarCreateAndAddInHashTable (Gnl,
                        G_NewUniqueName, NewVar)))
         if (GnlStatus == GNL MEMORY FULL)
            return (GNL MEMORY FULL);
        ConflictName = 1;
  return (GNL OK);
/*----*/
/* GnlVarNameExistsInGnl
/* This function returns 1 if the name 'VarName' is already defined
/* among the name in the gnl 'Gnl'.
int GnlVarNameExistsInGnl (Gnl, VarName)
  GNL
       Gnl;
  char
             *VarName;
  BLIST
           NewList;
  GNL_VAR
            VarI;
  unsigned int Key;
  int
            i;
  BLIST
           HashTableNames;
  GNL_STATUS GnlStatus;
  HashTableNames = GnlHashNames (Gnl);
  /* The list GnlHashNames (Gnl) is of size HASH TABLE NAMES SIZE
  Key = KeyOfName (VarName, BListSize (HashTableNames));
  if (HashTableNames->Adress[Key] == (int)NULL)
     return (0);
```

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```
NewList = (BLIST) (HashTableNames->Adress[Key]);
  for (i=0; i<BListSize (NewList); i++)</pre>
      VarI = (GNL_VAR)BListElt (NewList, i);
      if (!strcmp (GnlVarName (VarI), VarName))
         return (1);
  return (0);
/* GnlCreateUniqueVarName
/*----*/
GNL_STATUS GnlCreateUniqueVarName (Gnl, BaseName, NewName)
            Gnl;
  char
            *BaseName;
  char
            **NewName;
  if (!GnlVarNameExistsInGnl (Gnl, BaseName))
     *NewName = BaseName;
     return (GNL_OK);
  while (1)
      SetGnlUniqueId (Gnl, GnlUniqueId (Gnl)+1);
      sprintf (G_NewUniqueName, "%s_%d", BaseName, GnlUniqueId (Gnl));
      if (!GnlVarNameExistsInGnl (Gnl, G_NewUniqueName))
         free (BaseName);
         if (GnlStrCopy (G_NewUniqueName, NewName))
           return (GNL_MEMORY_FULL);
         return (GNL_OK);
     }
  return (GNL_OK);
}
/* GnlRemoveVarFromGnlLocals
/*----*/
/* Removes the variable 'Var' from the list of locals of 'Gnl'.
/*----*/
void GnlRemoveVarFromGnlLocals (Gnl, Var)
  GNL
           Gnl;
  GNL_VAR
            Var;
```

```
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{
   int
             i;
   GNL VAR
             VarI;
   for (i=0; i<BListSize (GnlLocals (Gnl)); i++)</pre>
      VarI = (GNL_VAR)BListElt (GnlLocals (Gnl), i);
      if (Var == VarI)
         BListDelInsert (GnlLocals (Gnl), i+1);
      }
}
/*----*/
/* GnlRemoveVarFromGnlHashTableName
/*----*/
/* This procedure removes the var 'Var' from the hash table names of
/* 'Gnl'.
/*----*/
void GnlRemoveVarFromGnlHashTableName (Gnl, Var)
              Gnl;
  GNL_VAR Var;
  BLIST
         HashTableNames;
  BLIST
         Bucket;
  int
  GNL_VAR VarI;
  unsigned int Key;
  HashTableNames = GnlHashNames (Gnl);
  Key = KeyOfName (GnlVarName (Var), BListSize (HashTableNames));
  /* This var does not appear since no bucket.
  if (HashTableNames->Adress[Key] == (int)NULL)
    return;
  Bucket = (BLIST)HashTableNames->Adress[Key];
  for (i=0; i<BListSize (Bucket); i++)</pre>
    VarI = (GNL_VAR)BListElt (Bucket, i);
    if (VarI == Var)
         BListDelInsert (Bucket, i+1);
       return;
     }
}
/*----*/
```

```
/* GnlNameFromOp
/*-----*/
char *GnlNameFromOp (Op)
  GNL_OP Op;
  switch (Op) {
    case GNL AND: return (".");
    case GNL_OR: return ("+");
    case GNL_NOT: return ("!");
    case GNL XOR: return ("^");
    case GNL NAND: return ("*");
    case GNL NOR: return ("#");
    case GNL XNOR: return ("@");
    default: return ("??");
}
/* GnlVarIsVss
/*----*/
/* returns 1 if the var is a the predefined var VSS (0).
*/
/*----*/
int GnlVarIsVss (Var)
  GNL_VAR Var;
  if (GnlVarName (Var)[0] == '0')
   return (1);
 return (0);
/*----*/
/* GnlVarIsVdd
/*----*/
/* returns 1 if the var is a the predefined var VDD (1). */
/*----*/
int GnlVarIsVdd (Var)
 GNL_VAR Var;
  if (GnlVarName (Var)[0] == '1')
   return (1);
 return (0);
}
/* GnlNodeIsVss
/*----*/
/* A node is a Vdd in two cases:
/* - either it is a GNL_CONSTANTE operator with GnlNodeSons = 0,

    or a GNL_VARIABLE operator and the variable is a GnlVarIsVss
```

```
/*----*/
int GnlNodeIsVss (Node)
  GNL NODE Node;
  if (GnlNodeOp (Node) == GNL_VARIABLE)
     return (GnlVarIsVss ((GNL_VAR)GnlNodeSons (Node)));
  if (GnlNodeOp (Node) == GNL_CONSTANTE)
    return (GnlNodeSons (Node) == (BLIST)0);
  return (0);
}
/*----*/
/* GnlNodeIsVdd
/* A node is a Vdd in two cases:
                                                 */
/* - either it is a GNL_CONSTANTE operator with GnlNodeSons = 1,
                                                */
     - or a GNL_VARIABLE operator and the variable is a GnlVarIsVdd */
/*----*/
int GnlNodeIsVdd (Node)
  GNL_NODE Node;
  if (GnlNodeOp (Node) == GNL VARIABLE)
    return (GnlVarIsVdd ((GNL_VAR)GnlNodeSons (Node)));
  if (GnlNodeOp (Node) == GNL CONSTANTE)
    return (GnlNodeSons (Node) == (BLIST)1);
  return (0);
}
/*----*/
/* GnlVarIsSignal
/*-----*/
int GnlVarIsSignal (Var)
 GNL_VAR Var;
  if (GnlVarIsVdd (Var) || GnlVarIsVss (Var))
   return (0);
 return (1);
}
/*-----*/
```

```
/* GnlNodeCopy
/*------/
/* Copy the Node 'Node' at only one level, i.e. the structure and */
/* eventually the list of sons. The sons elements are not physically
/* duplicated and are simply shared.
/*----*/
GNL_STATUS GnlNodeCopy (Gnl, Node, NodeCopy)
               Gnl;
   GNL NODE Node;
   GNL_NODE *NodeCopy;
   int
   BLIST NewSons;
   if (GnlCreateNode (Gnl, GnlNodeOp (Node), NodeCopy))
     return (GNL MEMORY FULL);
   SetGnlNodeLineNumber (*NodeCopy, GnlNodeLineNumber (Node));
   if ((GnlNodeOp (*NodeCopy) == GNL_VARIABLE) | |
      (GnlNodeOp (*NodeCopy) == GNL_CONSTANTE)
      (GnlNodeOp (*NodeCopy) == GNL_WIRE))
      /* Only the Value of 'GnlNodeSons (Node)' is relevant.
                                                             */
      SetGnlNodeSons (*NodeCopy, GnlNodeSons (Node));
      return (GNL_OK);
     }
  /* Otherwise we need to duplicate the list of sons.
  ^{\prime}/* The list is duplicated but the pointed elements are physically the*/
  /* same.
  if (BListCreateWithSize (BListSize (GnlNodeSons (Node)), &NewSons))
     return (GNL MEMORY FULL);
  for (i=0; i<BListSize (GnlNodeSons (Node)); i++)</pre>
       if (BListAddElt (NewSons, BListElt (GnlNodeSons (Node), i)))
         return (GNL_MEMORY FULL);
  SetGnlNodeSons (*NodeCopy, NewSons);
  return (GNL OK);
}
/*-----/
/* GnlNodeCopyRec
/* Copy the Node 'Node' and its sub-tree recursively.
/*----*/
GNL STATUS GnlNodeCopyRec (Gnl, Node, NodeCopy)
  GNL Gnl;
GNL_NODE Node;
GNL_NODE *NodeCopy;
  int
  BLIST Sons;
```

```
NewList;
   BLIST
   GNL NODE SonI;
   GNL NODE NodeCopyI;
   switch (GnlNodeOp (Node)) {
     case GNL CONSTANTE:
       case GNL VARIABLE:
       case GNL WIRE:
            return (GnlNodeCopy (Gnl, Node, NodeCopy));
       default:
            Sons = GnlNodeSons (Node);
            if (BListCreateWithSize (BListSize (Sons), &NewList))
               return (GNL_MEMORY FULL);
            if (GnlCreateNode (Gnl, GnlNodeOp (Node), NodeCopy))
               return (GNL MEMORY FULL);
            SetGnlNodeLineNumber (*NodeCopy, GnlNodeLineNumber (Node));
            SetGnlNodeSons (*NodeCopy, NewList);
            for (i=0; i<BListSize (Sons); i++)</pre>
                SonI = (GNL_NODE)BListElt (Sons, i);
                if (GnlNodeCopyRec (Gnl, SonI, &NodeCopyI))
                   return (GNL_MEMORY_FULL);
                if (BListAddElt (NewList, (int)NodeCopyI))
                   return (GNL_MEMORY FULL);
           return (GNL_OK);
     }
/*----*/
/* PrintNodeRec
/*----*/
void PrintNodeRec (Node, Bind)
  GNL NODE Node;
  int
                Bind;
  GNL NODE Son;
  GNL_VAR Var;
  int
                i;
  switch (GnlNodeOp (Node)) {
     case GNL VARIABLE:
           Var = (GNL_VAR)GnlNodeSons (Node);
           if (GnlVarIsVss (Var))
              fprintf (stderr, "{0}");
           else if (GnlVarIsVdd (Var))
              fprintf (stderr, "{1}");
           else
             if (Bind && GnlVarBindVar (Var))
              Var = GnlVarBindVar (Var);
               fprintf (stderr, "%s", GnlVarName (Var));
```

```
return;
      case GNL_NOT:
          Son = (GNL_NODE)BListElt (GnlNodeSons (Node), 0);
            fprintf (stderr, "!(");
            PrintNodeRec (Son, Bind);
            fprintf (stderr, ")");
            return;
      case GNL_AND:
      case GNL OR:
      case GNL NOR:
      case GNL NAND:
      case GNL XOR:
      case GNL_XNOR:
            fprintf (stderr, "(");
            for (i=0; i<BListSize (GnlNodeSons (Node))-1; i++)</pre>
                Son = (GNL NODE)BListElt (GnlNodeSons (Node), i);
                PrintNodeRec (Son, Bind);
                fprintf (stderr, "%s", GnlNameFromOp (GnlNodeOp (Node)));
            Son = (GNL NODE)BListElt (GnlNodeSons (Node), i);
            PrintNodeRec (Son, Bind);
            fprintf (stderr, ")");
            return;
     case GNL CONSTANTE:
          if (GnlNodeSons (Node) == (BLIST)1)
           fprintf (stderr, "{1}");
          else
           fprintf (stderr, "{0}");
          return;
     case GNL WIRE:
          Son = (GNL_NODE)GnlNodeSons (Node);
          PrintNodeRec (Son, Bind);
          return;
     default:
           GnlError (9 /* Unknown node */);
            return;
      }
}
/*------*/
/* GnlPrintNodeRec
                                                      */
/*----*/
void GnlPrintNodeRec (File, Node, Bind)
  FILE
               *File;
  GNL NODE Node;
  int
                Bind;
  GNL NODE Son;
  GNL_VAR Var;
```

```
int
              i;
switch (GnlNodeOp (Node)) {
   case GNL VARIABLE:
          Var = (GNL_VAR)GnlNodeSons (Node);
          if (GnlVarIsVss (Var))
             fprintf (File, "{0}");
          else if (GnlVarIsVdd (Var))
             fprintf (File, "{1}");
          else
         {
            if (Bind && GnlVarBindVar (Var))
             Var = GnlVarBindVar (Var);
              fprintf (File, "%s", GnlVarName (Var));
          return;
  case GNL NOT:
       Son = (GNL_NODE)BListElt (GnlNodeSons (Node), 0);
          fprintf (File, "!(");
          GnlPrintNodeRec (File, Son, Bind);
          fprintf (File, ")");
         return;
  case GNL AND:
  case GNL OR:
  case GNL NOR:
  case GNL_NAND:
  case GNL XOR:
  case GNL XNOR:
          fprintf (File, "(");
         for (i=0; i<BListSize (GnlNodeSons (Node))-1; i++)</pre>
              Son = (GNL_NODE)BListElt (GnlNodeSons (Node), i);
              GnlPrintNodeRec (File, Son, Bind);
               fprintf (File, "%s", GnlNameFromOp (GnlNodeOp (Node)));
         Son = (GNL_NODE)BListElt (GnlNodeSons (Node), i);
         GnlPrintNodeRec (File, Son, Bind);
         fprintf (File, ")");
         return;
  case GNL_CONSTANTE:
       if (GnlNodeSons (Node) == (BLIST)1)
        fprintf (File, "{1}");
        fprintf (File, "{0}");
       return;
  case GNL WIRE:
       Son = (GNL NODE)GnlNodeSons (Node);
       GnlPrintNodeRec (File, Son, Bind);
       return;
  default:
         GnlError (9 /* Unknown node */);
```

```
return;
     }
}
/*----*/
/* GnlPrintNode
/*----*/
void GnlPrintNode (File, Node)
             *File;
  GNL_NODE Node;
  GnlPrintNodeRec (File, Node, 0);
/* GnlEqualNode
/*----*/
int GnlEqualNode (Node1, Node2)
  GNL NODE Node1;
  GNL_NODE Node2;
  int
                  i;
  GNL NODE
             Son1;
  GNL_NODE
             Son2;
  if (GnlNodeOp (Node1) != GnlNodeOp (Node2))
    return (0);
  if (GnlNodeOp (Node1) == GNL_VARIABLE)
     return (GnlNodeSons (Node1) == GnlNodeSons (Node2));
  if (GnlNodeOp (Node1) == GNL_CONSTANTE)
     return (GnlNodeSons (Node1) == GnlNodeSons (Node2));
  if (BListSize (GnlNodeSons (Node1)) !=
     BListSize (GnlNodeSons (Node2)))
    return (0);
  for (i=0; i<BListSize (GnlNodeSons (Node1)); i++)</pre>
     Son1 = (GNL NODE)BListElt (GnlNodeSons (Node1), i);
     Son2 = (GNL_NODE)BListElt (GnlNodeSons (Node2), i);
     if (!GnlEqualNode (Son1, Son2))
        return (0);
  return (1);
}
/*----*/
/* GnlSeqName
/*----*/
char *GnlSeqName (SeqName)
```

```
GNL SEQUENTIAL OP
                   SeqName;
  switch (SeqName) {
     case GNL DFF: return ("DFF");
     case GNL_DFFX: return ("DFFX");
     case GNL_DFF0: return ("DFF0");
     case GNL DFF1: return ("DFF1");
     case GNL_LATCH: return ("LATCH");
     case GNL_LATCHX: return ("LATCHX");
     case GNL LATCH0: return ("LATCH0");
     case GNL_LATCH1: return ("LATCH1");
      default: return ("DFF ??");
}
/*-----
/* GnlPrintTriStateBox
/*----*/
void GnlPrintTriStateBox (File, PBox)
                         *File:
  GNL_TRISTATE_COMPONENT
                         PBox;
{
  fprintf (File,
          "TRISTATE %s (q=%s, d=%s, d pol=%d, en=%s, en pol=%d) n",
          GnlTriStateInstName (PBox),
          GnlVarName (GnlTriStateOutput (PBox)),
          GnlVarName (GnlTriStateInput (PBox)),
          GnlTriStateInputPol (PBox),
          GnlVarName (GnlTriStateSelect (PBox)),
          GnlTriStateSelectPol (PBox));
}
/*----*/
/* GnlPrintPredefinedBox
/*----*/
void GnlPrintPredefinedBox (File, PBox)
                         *File:
  GNL_SEQUENTIAL_COMPONENT
                         PBox;
  char
               *SetName;
  char
              *ResetName;
  if (!GnlSequentialCompoSet (PBox))
    SetName = "";
     SetName = GnlVarName (GnlSequentialCompoSet (PBox));
  if (!GnlSequentialCompoReset (PBox))
    ResetName = "";
    ResetName = GnlVarName (GnlSequentialCompoReset (PBox));
  if (GnlSeqComponentIsDFF (PBox))
     fprintf (File,
```

```
"[%s] %s (q=%s, d=%s, clk=%s, clk_pol=%d, s=%s, s_pol=%d, r=%s, r_pol=%d)\n",
               GnlSequentialCompoInstName (PBox),
               GnlSeqName (GnlSequentialCompoOp(PBox)),
               GnlVarName (GnlSequentialCompoOutput (PBox)),
               GnlVarName (GnlSequentialCompoInput (PBox)),
               GnlVarName (GnlSequentialCompoClock (PBox)),
               GnlSequentialCompoClockPol (PBox),
               SetName,
               GnlSequentialCompoClockPol (PBox),
               ResetName,
               GnlSequentialCompoResetPol (PBox));
   else
      fprintf (File,
"[%s] %s (q=%s, d=%s, clk=%s, clk_pol=%d, s=%s, s_pol=%d, r=%s, r_pol=%d)n",
               GnlSequentialCompoInstName (PBox),
               GnlSeqName (GnlSequentialCompoOp(PBox)),
               GnlVarName (GnlSequentialCompoOutput (PBox)),
               GnlVarName (GnlSequentialCompoInput (PBox)),
               GnlVarName (GnlSequentialCompoClock (PBox)),
               GnlSequentialCompoClockPol (PBox),
               SetName,
               GnlSequentialCompoClockPol (PBox),
              ResetName,
              GnlSequentialCompoResetPol (PBox));
     }
/*----*/
/* GnlPrintUserBox
/*----*/
void GnlPrintUserBox (File, UserCompo)
                      *File;
  GNL_USER_COMPONENT UserCompo;
  int
                i;
  GNL ASSOC
              AssocI;
  char
                *FormalName;
  char
                *ActualName;
  fprintf (File, "[%s] %s (",
          GnlUserComponentName (UserCompo),
          GnlUserComponentInstName (UserCompo));
  for (i=0; i<BListSize (GnlUserComponentInterface (UserCompo))-1; i++)</pre>
       AssocI = (GNL ASSOC)
             BListElt (GnlUserComponentInterface (UserCompo), i);
       if (GnlUserComponentFormalType (UserCompo) == GNL_FORMAL_CHAR)
          FormalName = GnlAssocFormalPort (AssocI);
       else
         FormalName = GnlVarName ((GNL_VAR)GnlAssocFormalPort (AssocI));
       ActualName = GnlVarName (GnlAssocActualPort (AssocI));
       fprintf (File, ".%s(%s), ", FormalName, ActualName);
```

```
}
  AssocI = (GNL ASSOC)
           BListElt (GnlUserComponentInterface (UserCompo), i);
  if (GnlUserComponentFormalType (UserCompo) == GNL FORMAL CHAR)
     FormalName = GnlAssocFormalPort (AssocI);
  else
     FormalName = GnlVarName ((GNL_VAR)GnlAssocFormalPort (AssocI));
  ActualName = GnlVarName (GnlAssocActualPort (AssocI));
  fprintf (File, ".%s(%s))\n", FormalName, ActualName);
/*----*/
/* GnlPrintGnl
/*----*/
void GnlPrintGnl (File, Gnl)
  FILE
         *File;
  GNL
          Gnl;
  int
  GNL VAR
               VarI;
  GNL_VAR
               VarJ;
  BLIST
               BucketI;
  int
                   j;
  BLIST
               HashTableNames;
  GNL_FUNCTION
                    FunctionI;
  BLIST
               Components;
  GNL COMPONENT ComponentI;
  fprintf (File, "-----
\n");
  fprintf (File, "Name = s\n\n", GnlName (Gnl));
  fprintf (File, "Inputs = ");
  for (i=0; i<BListSize (GnlInputs (Gnl)); i++)</pre>
      VarI = (GNL_VAR)BListElt (GnlInputs (Gnl), i);
       fprintf (File, "%s ", GnlVarName (VarI));
  fprintf (File, ";\n");
  fprintf (File, "Outputs = ");
  for (i=0; i<BListSize (GnlOutputs (Gnl)); i++)</pre>
      VarI = (GNL_VAR)BListElt (GnlOutputs (Gnl), i);
      fprintf (File, "%s ", GnlVarName (VarI));
  fprintf (File, ";\n");
  fprintf (File, "Locals = ");
  /* Removing the unused GNL VAR LOCAL variables
                                                                */
  HashTableNames = GnlHashNames (Gnl);
  for (i=0; i<BListSize (HashTableNames); i++)</pre>
```

```
BucketI = (BLIST)BListElt (HashTableNames, i);
        for (j=0; j<BListSize (BucketI); j++)</pre>
             VarJ = (GNL_VAR)BListElt (BucketI, j);
             if ((GnlVarDir (VarJ) == GNL_VAR_LOCAL) | |
                 (GnlVarDir (VarJ) == GNL_VAR_LOCAL_WIRING))
                 fprintf (File, "%s ", GnlVarName (VarJ));
            }
*/
   for (i=0; i<BListSize (GnlLocals (Gnl)); i++)</pre>
       VarJ = (GNL VAR)BListElt (GnlLocals (Gnl), i);
        if (!VarJ)
           fprintf (File, "NULL ");
       else
           fprintf (File, "%s ", GnlVarName (VarJ));
   fprintf (File, ";\n\n");
   /* Printing the Boolean functions ...
                                                            */
  for (i=0; i<BListSize (GnlFunctions (Gnl)); i++)</pre>
       VarI = (GNL_VAR)BListElt (GnlFunctions (Gnl), i);
       FunctionI = GnlVarFunction (VarI);
       fprintf (File, "%s = ", GnlVarName (VarI));
       GnlPrintNode (File, GnlFunctionOnSet (FunctionI));
       fprintf (File, ";\n");
       if (GnlFunctionDCSet (FunctionI))
            fprintf (File, "DCSET %s = ", GnlVarName (VarI));
           GnlPrintNode (File, GnlFunctionDCSet (FunctionI));
           fprintf (File, ";\n");
      }
  return;
  Components = GnlComponents (Gnl);
  if (Components && BListSize (Components))
      fprintf (File, "-----\n");
      fprintf (File, "WIRING:\n\n");
  for (i=0; i<BListSize (Components); i++)</pre>
       ComponentI = (GNL_COMPONENT)BListElt (Components, i);
       switch (GnlComponentType (ComponentI)) {
           case GNL_SEQUENTIAL COMPO:
                    GnlPrintPredefinedBox (File,
                                 (GNL_SEQUENTIAL COMPONENT) ComponentI);
                    break;
```

```
case GNL TRISTATE COMPO:
                 GnlPrintTriStateBox (File,
                          (GNL_TRISTATE_COMPONENT)ComponentI);
                 break;
         case GNL_USER COMPO:
                 GnlPrintUserBox (File,
                          (GNL_USER_COMPONENT) ComponentI);
                break;
         default:
                break;
      }
  fprintf (File, "-----
\n");
/* GnlGetGnlNbLiterals
/*----*/
/* Returns the number of Literals used in the GNL 'Gnl'.
/*----*/
int GnlGetGnlNbLiterals (Gnl)
  GNL
         Gnl;
  int
              i;
  int
              NbLitt;
  GNL_FUNCTION
              FunctionI;
  GNL_VAR VarI;
  NbLitt = 0;
  for (i=0; i<BListSize (GnlFunctions (Gnl)); i++)
      VarI = (GNL_VAR)BListElt (GnlFunctions (Gnl), i);
      FunctionI = GnlVarFunction (VarI);
      NbLitt += GnlNodeNbLitt (GnlFunctionOnSet (FunctionI));
  return (NbLitt);
}
/*----*/
/* GnlNodeNbEquiGates
float GnlNodeNbEquiGates (Node)
  GNL NODE
          Node;
  int
            i;
  float
           NbEquiGates;
  GNL NODE
            SonI;
  if (GnlNodeOp (Node) == GNL_CONSTANTE)
    return (0);
```

```
if (GnlNodeOp (Node) == GNL VARIABLE)
     return (0);
   if (GnlNodeOp (Node) == GNL_WIRE)
      SonI = (GNL_NODE)GnlNodeSons (Node);
      return (GnlNodeNbEquiGates (SonI));
   if (GnlNodeOp (Node) == GNL NOT)
      NbEquiGates = 0.5;
     NbEquiGates = BListSize (GnlNodeSons(Node))-1;
   for (i=0; i<BListSize (GnlNodeSons (Node)); i++)</pre>
       SonI = (GNL_NODE)BListElt (GnlNodeSons (Node), i);
       NbEquiGates += GnlNodeNbEquiGates (SonI);
   return (NbEquiGates);
/*----*/
/* GnlGetNbEquiGatesInPredefinedBox
/*----*/
float GnlGetNbEquiGatesInPredefinedBox (PBox)
  GNL_SEQUENTIAL_COMPONENT PBox;
  switch (GnlSequentialCompoOp (PBox)) {
    case GNL DFF:
    case GNL DFFX:
    case GNL DFF1:
     case GNL DFF0:
         return (2.5); /* Two master-slave latches + Inv. */
     case GNL LATCH:
     case GNL LATCHX:
     case GNL_LATCH1:
     case GNL LATCHO:
         return (1); /* Two inverters
                                           */
     }
}
/* GnlGetGnlNbEquiGates
/*----*/
/* Returns the number of Equivalent gates in the GNL 'Gnl'.
/*----*/
void GnlGetGnlNbEquiGates (Gnl, CombEquiGates, SeqEquiGates)
  GNL
  float
         *CombEquiGates;
  float *SeqEquiGates;
```

```
int
                i;
   float
                NbEquiGates;
   GNL_FUNCTION FunctionI;
   GNL VAR
                 VarI;
   GNL_COMPONENT ComponentI;
   BLIST
                 Components;
   *CombEquiGates = *SeqEquiGates = 0;
   for (i=0; i<BListSize (GnlFunctions (Gnl)); i++)</pre>
      VarI = (GNL_VAR)BListElt (GnlFunctions (Gnl), i);
      FunctionI = GnlVarFunction (VarI);
      *CombEquiGates += GnlNodeNbEquiGates (GnlFunctionOnSet (FunctionI));
   Components = GnlComponents (Gnl);
   if (!Components)
     return ;
   for (i=0; i<BListSize (Components); i++)</pre>
       ComponentI = (GNL_COMPONENT)BListElt (Components, i);
       if (GnlComponentType (ComponentI) == GNL_SEQUENTIAL_COMPO)
           *SeqEquiGates += GnlGetNbEquiGatesInPredefinedBox (
            (GNL_SEQUENTIAL_COMPONENT) ComponentI);
      }
/* GnlGetNbFlipsFlops
   -----*/
/* Returns the number of Flips Flops in the GNL 'Gnl'.
/*-----
int GnlGetNbFlipsFlops (Gnl)
  GNL Gnl;
  int
                           NbFlipsFlops;
                           i;
  GNL_SEQUENTIAL COMPONENT
                           PBox;
  GNL_COMPONENT ComponentI;
  BLIST
                     Components;
  NbFlipsFlops = 0;
  Components = GnlComponents (Gnl);
  if (!Components)
     return (0);
  for (i=0; i<BListSize (Components); i++)</pre>
     ComponentI = (GNL_COMPONENT)BListElt (Components, i);
```

```
if (GnlComponentType (ComponentI) == GNL_SEQUENTIAL_COMPO)
        PBox = (GNL_SEQUENTIAL COMPONENT) ComponentI;
        if ((GnlSequentialCompoOp (PBox) == GNL_DFF) | |
           (GnlSequentialCompoOp (PBox) == GNL DFFX) ||
           (GnlSequentialCompoOp (PBox) == GNL_DFF0)
           (GnlSequentialCompoOp (PBox) == GNL DFF1))
          NbFlipsFlops++;
  return (NbFlipsFlops);
}
/*----*/
/* GnlGetNbLatches
/*----*/
/* Returns the number of Latches in the GNL 'Gnl'.
/*----*/
int GnlGetNbLatches (Gnl)
  GNL Gnl;
  int
                       NbLatches;
  int
  GNL_SEQUENTIAL COMPONENT
                           PBox;
  GNL COMPONENT
                       ComponentI;
  BLIST
                  Components;
  NbLatches = 0;
  Components = GnlComponents (Gnl);
  if (!Components)
    return (0);
  for (i=0; i<BListSize (Components); i++)</pre>
    ComponentI = (GNL_COMPONENT)BListElt (Components, i);
    if (GnlComponentType (ComponentI) == GNL_SEQUENTIAL_COMPO)
       PBox = (GNL_SEQUENTIAL_COMPONENT) ComponentI;
       if ((GnlSequentialCompoOp (PBox) == GNL_LATCH) | |
           (GnlSequentialCompoOp (PBox) == GNL_LATCHX) ||
           (GnlSequentialCompoOp (PBox) == GNL_LATCH0) ||
           (GnlSequentialCompoOp (PBox) == GNL_LATCH1))
         NbLatches++;
      }
   }
  return (NbLatches);
}
/*-----*/
/* GnlGetNbTriSTates
/*----*/
/* Returns the number of TriStates in the GNL 'Gnl'.
/*----*/
```

```
int GnlGetNbTriStates (Gnl)
   GNL Gnl;
   int
                          NbTriStates;
   int
   GNL_TRISTATE_COMPONENT
                          PBox;
   GNL COMPONENT
                          ComponentI;
   BLIST
                     Components;
   NbTriStates = 0;
   Components = GnlComponents (Gnl);
   if (!Components)
     return (0);
   for (i=0; i<BListSize (Components); i++)</pre>
     ComponentI = (GNL_COMPONENT)BListElt (Components, i);
     if (GnlComponentType (ComponentI) == GNL_TRISTATE_COMPO)
        PBox = (GNL_TRISTATE_COMPONENT)ComponentI;
        NbTriStates++;
    }
  return (NbTriStates);
/*----*/
/* GnlGetNbBuffers
/*----*/
/* Returns the number of Buffers in the GNL 'Gnl'.
/*------//
int GnlGetNbBuffers (Gnl)
  GNL Gnl;
  int
                           NbBuffers;
  int
                           i;
  GNL BUF COMPONENT
                        PBox;
  GNL COMPONENT
                           ComponentI;
  BLIST
                           Components;
  NbBuffers = 0;
  Components = GnlComponents (Gnl);
  if (!Components)
    return (0);
  for (i=0; i<BListSize (Components); i++)</pre>
    ComponentI = (GNL_COMPONENT)BListElt (Components, i);
    if (GnlComponentType (ComponentI) == GNL_BUF_COMPO)
        PBox = (GNL_BUF_COMPONENT)ComponentI;
        NbBuffers++;
```

```
return (NbBuffers);
/*----*/
/* GnlResetNodeHook
/*----*/
/*----//* Reset 'Hook' field of 'Node' and recursively on the Sons. If the */
/*----*/
void GnlResetNodeHook (Node)
  GNL NODE Node;
  int
  GNL NODE SonI;
  if (Node == NULL)
    return;
  if ((GnlNodeOp (Node) == GNL_CONSTANTE) ||
     (GnlNodeOp (Node) == GNL_VARIABLE))
    return;
  if (GnlNodeHook (Node) == 0)
                             /* Already processed previously
    */
    return;
  if (GnlNodeOp (Node) == GNL_WIRE)
    SonI = (GNL_NODE)GnlNodeSons (Node);
    GnlResetNodeHook (SonI);
    return;
  for (i=0; i<BListSize (GnlNodeSons(Node)); i++)</pre>
     SonI = (GNL NODE)BListElt (GnlNodeSons(Node), i);
     GnlResetNodeHook (SonI);
  SetGnlNodeHook (Node, 0);
}
/*----*/
/* GnlResetNodeHookRec
/*----*/
/* Reset 'Hook' field of 'Node' and recursively on the Sons. If the
                                                   */
/* 'Node' has a 0 Hook then we assume it is the same for its sons.
/*----*/
void GnlResetNodeHookRec (Node)
  GNL NODE Node;
  int
            i;
 GNL NODE
            SonI;
```

```
GNL NODE
             NewNode;
   GNL_VAR
              Var;
   GNL FUNCTION
                   Function;
   if (Node == NULL)
     return;
   if (GnlNodeOp (Node) == GNL_CONSTANTE)
     return;
  if (GnlNodeOp (Node) == GNL_VARIABLE)
      Var = (GNL VAR)GnlNodeSons (Node);
      Function = GnlVarFunction (Var);
      if (!Function)
        return;
      NewNode = GnlFunctionOnSet (Function);
      GnlResetNodeHook (NewNode);
      return;
  SetGnlNodeHook (Node, NULL);
  for (i=0; i<BListSize (GnlNodeSons(Node)); i++)</pre>
      SonI = (GNL_NODE)BListElt (GnlNodeSons(Node), i);
      GnlResetNodeHookRec (SonI);
}
/*----*/
/* GnlResetGnlNodeHook
/*----*/
/* This procedure resets all the 'Hook' fields of each GNL_NODE object */
/*----*/
void GnlResetGnlNodeHook (Gnl)
  GNL
        Gnl:
  int
              i;
  GNL FUNCTION
              FunctionI;
  GNL_VAR VarI;
  for (i=0; i<BListSize (GnlFunctions (Gnl)); i++)</pre>
      VarI = (GNL_VAR)BListElt (GnlFunctions (Gnl), i);
      FunctionI = GnlVarFunction (VarI);
      GnlResetNodeHook (GnlFunctionOnSet (FunctionI));
      GnlResetNodeHook (GnlFunctionDCSet (FunctionI));
}
/*----*/
```

```
/* GnlNodeMaxDepth
/*----*/
/* Computes the Max depth from the node 'Node'. The Field 'Hook' is used*/
/st so it must be equals to 0 at the beginning if not a wrong max depth st/
/* can be returned.
/*----*/
int GnlNodeMaxDepth (Node)
  GNL NODE Node;
  int
               i;
  int
              MaxDepth;
  int
              DepthI;
  GNL NODE SonI;
  GNL_NODE NodeFunction;
  GNL_VAR Var;
  if (GnlNodeOp (Node) == GNL_CONSTANTE)
     return (0);
  if (GnlNodeOp (Node) == GNL_WIRE)
     SonI = (GNL_NODE)GnlNodeSons (Node);
     return (GnlNodeMaxDepth (SonI));
  if (GnlNodeOp (Node) == GNL_VARIABLE)
     Var = (GNL_VAR)GnlNodeSons (Node);
     if (GnlVarDir (Var) != GNL_VAR_LOCAL)
       return (0);
     if (GnlVarIsVss (Var))
        return (0);
     if (GnlVarIsVdd (Var))
        return (0);
     /* Continuing on the corresponding function.
                                                          */
     if (GnlVarFunction (Var) == NULL)
         return (0);
     /* we continue on the On set of the function.
                                                          */
     NodeFunction = GnlFunctionOnSet (GnlVarFunction (Var));
     return (GnlNodeMaxDepth (NodeFunction));
 if (GnlNodeHook (Node))
                         /* different of NULL so we already got
                    /* the depth of this node previously. */
    return ((int)GnlNodeHook (Node));
 MaxDepth = 0;
 for (i=0; i<BListSize (GnlNodeSons(Node)); i++)</pre>
```

```
SonI = (GNL_NODE)BListElt (GnlNodeSons(Node), i);
      DepthI = GnlNodeMaxDepth (SonI);
      if (DepthI > MaxDepth)
         MaxDepth = DepthI;
  /* we store the result if we pass later on this node
                                                         */
  SetGnlNodeHook (Node, (int*)(MaxDepth+1));
  return (MaxDepth+1);
}
/*----*/
/* GnlGetGnlMaxDepth
/*----*/
/* This procedures will use the field 'Hook' of each GNL_NODE so all the*/
int GnlGetGnlMaxDepth (Gnl)
  GNL Gnl;
  int
  int
             MaxDepth;
             DepthI;
  GNL_FUNCTION FunctionI;
  GNL_NODE NodeI;
  GNL_VAR VarI;
  /* First we reset all the 'Hook' fields of each GNL NODE object
  GnlResetGnlNodeHook (Gnl);
  MaxDepth = 0;
  for (i=0; i<BListSize (GnlFunctions (Gnl)); i++)</pre>
      VarI = (GNL_VAR)BListElt (GnlFunctions (Gnl), i);
      FunctionI = GnlVarFunction (VarI);
    NodeI = GnlFunctionOnSet (FunctionI);
      DepthI = GnlNodeMaxDepth (NodeI);
      if (DepthI > MaxDepth)
        MaxDepth = DepthI;
  /* First we reset all the 'Hook' fields of each GNL NODE object
                                                       */
  /* to be clean at the output of this procedure
  GnlResetGnlNodeHook (Gnl);
  return (MaxDepth);
}
/*----*/
/* GnlDepthComp
```

```
/*----*/
static int GnlDepthComp (Var1, Var2)
   GNL VAR *Var1;
   GNL_VAR *Var2;
{
   if ((*Var1)->Hook < (*Var2)->Hook)
     return (-1);
   if ((*Var1) \rightarrow Hook == (*Var2) \rightarrow Hook)
     return (0);
   return (1);
}
/*----*/
/* GnlDepthInvComp
/*----*/
static int GnlDepthInvComp (Var1, Var2)
   GNL_VAR *Var1;
   GNL_VAR *Var2;
   if ((*Var1)->Hook < (*Var2)->Hook)
     return (1);
   if ((*Var1) \rightarrow Hook == (*Var2) \rightarrow Hook)
     return (0);
  return (-1);
/* GnlNodeLevel
/*----*/
void GnlNodeLevel (Node, Level)
  GNL NODE Node;
  int
              Level;
  int
             i;
  int
             MaxDepth;
  int
              DepthI;
  GNL_NODE SonI;
  GNL_NODE NodeFunction;
  GNL_VAR Var;
  if (GnlNodeOp (Node) == GNL_VARIABLE)
     Var = (GNL_VAR)GnlNodeSons (Node);
     if (GnlVarDir (Var) == GNL_VAR_INPUT)
     if (GnlVarDir (Var) == GNL_VAR_INOUT)
       return ;
     /st If we passed previously with a higher level then it is not st/
```

```
/* necessary to recurse.
                                                           */
      if (GnlVarHook (Var) >= (void*)Level)
       return;
      SetGnlVarHook (Var, (int*)Level);
      if (GnlVarIsVss (Var))
         return ;
      if (GnlVarIsVdd (Var))
         return ;
      /* Continuing on the corresponding function.
                                                           */
      if (GnlVarFunction (Var) == NULL)
       return;
      /* we continue on the On set of the function.
                                                           */
      NodeFunction = GnlFunctionOnSet (GnlVarFunction (Var));
      GnlNodeLevel (NodeFunction, Level+1);
      return;
   if (GnlNodeOp (Node) == GNL_CONSTANTE)
     return ;
   for (i=0; i<BListSize (GnlNodeSons(Node)); i++)</pre>
      SonI = (GNL_NODE)BListElt (GnlNodeSons(Node), i);
       GnlNodeLevel (SonI, Level);
}
/*----*/
/* GnlSortFunctionsOnLevel
/* GHISOTER MICE TO MISOTIME VET // /*----*/
/* This procedure sorts the Functions (GNL_VAR) according to their */
/* Level in the netlist. The first function is the highest one and the */
/* last is the deepest one.
/*----*/
void GnlSortFunctionsOnLevel (Gnl)
  GNL
               Gnl;
  int
               i;
  int
               MaxDepth;
  int
               DepthI;
  GNL FUNCTION FunctionI;
  GNL VAR VarI;
  BLIST Functions;
  /* First we reset all the 'Hook' fields of each GNL_VAR object */
  GnlResetVarHook (Gnl);
  for (i=0; i<BListSize (GnlFunctions (Gnl)); i++)</pre>
```

```
VarI = (GNL_VAR)BListElt (GnlFunctions (Gnl), i);
      FunctionI = GnlVarFunction (VarI);
      GnlNodeLevel (GnlFunctionOnSet (FunctionI), 1);
   Functions = GnlFunctions (Gnl);
   qsort (BListAdress (Functions), BListSize (Functions),
        sizeof(GNL_VAR), GnlDepthComp);
   /* Finally we reset all the 'Hook' fields of each GNL_VAR object
  GnlResetVarHook (Gnl);
/*----*/
/* This procedure sorts the Functions (GNL_VAR) according to their */
/* Level in the netlist. The first function is the deepest one and the */
/*----*/
void GnlSortFunctionsOnInvertLevel (Gnl)
  GNL
             Gnl:
  int
             i;
  int
             MaxDepth;
  int
             DepthI;
  GNL FUNCTION FunctionI;
  GNL_VAR VarI;
  BLIST Functions;
  /* First we reset all the 'Hook' fields of each GNL_VAR object
                                                   */
  GnlResetVarHook (Gnl);
  for (i=0; i<BListSize (GnlFunctions (Gnl)); i++)</pre>
      VarI = (GNL_VAR)BListElt (GnlFunctions (Gnl), i);
      FunctionI = GnlVarFunction (VarI);
      GnlNodeLevel (GnlFunctionOnSet (FunctionI), 1);
  Functions = GnlFunctions (Gnl);
  qsort (BListAdress (Functions), BListSize (Functions),
       sizeof(GNL_VAR), GnlDepthInvComp);
  /* Finally we reset all the 'Hook' fields of each GNL_VAR object */
  GnlResetVarHook (Gnl);
}
/* GnlSortFunctionsOnInvertLevelStoreLevel
/*----*/
/* This procedure sorts the Functions (GNL_VAR) according to their
/* Level in the netlist. The first function is the deepest one and the */
/* last is the highest one.
/*----*/
void GnlSortFunctionsOnInvertLevelStoreLevel (Gnl)
```

```
GNL
               Gnl;
   int
               i;
   int
               MaxDepth;
   int
               DepthI;
               FunctionI;
   GNL_FUNCTION
  GNL VAR VarI;
  BLIST Functions;
  /* First we reset all the 'Hook' fields of each GNL_VAR object
  GnlResetVarHook (Gnl);
  for (i=0; i<BListSize (GnlFunctions (Gnl)); i++)</pre>
       VarI = (GNL_VAR)BListElt (GnlFunctions (Gnl), i);
       FunctionI = GnlVarFunction (VarI);
       GnlNodeLevel (GnlFunctionOnSet (FunctionI), 1);
  Functions = GnlFunctions (Gnl);
  qsort (BListAdress (Functions), BListSize (Functions),
        sizeof(GNL_VAR), GnlDepthInvComp);
}
/*----*/
/* GnlGetGnlInfo
/*----*/
/* This procedure computes different kind of information about the \star/
/* current Gnl like: Number Flip-Flops, Number Latches, Number Literals */
/* Maximum combinatorial critcal path, ...
/* If returns a structure thru 'GnlInfo' storing all these information */
/*----*/
GNL_STATUS GnlGetGnlInfo (Gnl, GnlInfo)
  GNL
  GNL INFO *GnlInfo;
  int
               NbFlipsFlops;
  int
              NbLatches;
  int
              NbTriStates;
  int
               NbBuffers;
  int
               NbLiterals;
  float NbCombEquiGates;
float NbSeqEquiGates;
  float NbTotalEquiGates;
  int
              MaxDepth;
  int
               i;
  int
              NbInputs;
  int
               NbOutputs;
  int
               NbInOuts;
  GNL_VAR PortI;
  if ((*GnlInfo = (GNL_INFO)calloc (1, sizeof (GNL_INFO_REC))) == NULL)
    return (GNL_MEMORY_FULL);
```

```
NbFlipsFlops = GnlGetNbFlipsFlops (Gnl);
   NbLatches = GnlGetNbLatches (Gnl);
   NbTriStates = GnlGetNbTriStates (Gnl);
   NbBuffers = GnlGetNbBuffers (Gnl);
   NbLiterals = GnlGetGnlNbLiterals (Gnl);
   GnlGetGnlNbEquiGates (Gnl, &NbCombEquiGates, &NbSeqEquiGates);
   NbTotalEquiGates = NbCombEquiGates+NbSeqEquiGates;
   MaxDepth = GnlGetGnlMaxDepth (Gnl);
   NbInputs = NbOutputs = NbInOuts = 0;
   for (i=0; i<BListSize (GnlListPorts (Gnl)); i++)</pre>
        PortI = (GNL_VAR)BListElt (GnlListPorts (Gnl), i);
        switch (GnlVarDir (PortI)) {
            case GNL VAR INPUT:
                    NbInputs++;
                    break:
            case GNL VAR OUTPUT:
                    NbOutputs++;
                    break;
            case GNL VAR INOUT:
                    NbInOuts++;
                    break;
               }
       }
   SetGnlInfoNbInputs (*GnlInfo, NbInputs);
   SetGnlInfoNbOutputs (*GnlInfo, NbOutputs):
   SetGnlInfoNbInOuts (*GnlInfo, NbInOuts);
   SetGnlInfoNbClocks (*GnlInfo, BListSize (GnlClocks (Gnl)));
   SetGnlInfoNbFlipFlops (*GnlInfo, NbFlipsFlops);
   SetGnlInfoNbLatches (*GnlInfo, NbLatches);
   SetGnlInfoNbTriStates (*GnlInfo, NbTriStates);
   SetGnlInfoNbBuffers (*GnlInfo, NbBuffers);
  SetGnlInfoNbLit (*GnlInfo, NbLiterals);
  SetGnlInfoNbCombEquiGates (*GnlInfo, NbCombEquiGates);
  SetGnlInfoNbSeqEquiGates (*GnlInfo, NbSeqEquiGates);
  SetGnlInfoNbTotalEquiGates (*GnlInfo, NbTotalEquiGates);
  SetGnlInfoMaxDepth (*GnlInfo, MaxDepth);
  return (GNL OK);
/* GnlPrintGnlInfo
/*----*/
GNL_STATUS GnlPrintGnlInfo (File, Gnl)
  FILE
                *File:
  GNL
                 Gnl:
  GNL INFO GnlInfo;
```

```
if (GnlGetGnlInfo (Gnl, &GnlInfo))
     return (GNL MEMORY FULL);
   fprintf (File, "# -----\n");
   fprintf (File, "# | INPUTS
                            =%9d \\n",
          GnlNbIn (Gnl));
   fprintf (File, "# | OUTPUTS
                                =%9d \n",
          GnlNbOut (Gnl));
   fprintf (File, "# | LOCALS
                                  =%9d |\n",
          GnlNbLocal (Gnl));
   fprintf (File, "# | CLOCKS
                                   =%9d |\n",
          GnlInfoNbClocks (GnlInfo));
   fprintf (File, "# | FLIP-FLOPS
                                   =%9d \n",
          GnlInfoNbFlipFlops (GnlInfo));
   fprintf (File, "# | LATCHES
                                   =%9d | n",
          GnlInfoNbLatches (GnlInfo));
   fprintf (File, "# | TRISTATES
                                   =%9d \\n",
          GnlInfoNbTriStates (GnlInfo));
   fprintf (File, "# | BUFFERS
                                   =%9d \n",
          GnlInfoNbBuffers (GnlInfo));
   fprintf (File, "# | LITERALS
                                   =%9d |\n",
          GnlInfoNbLit (GnlInfo));
   /*
   fprintf (File, "# | COMB.EQUIV.GATES =%9.1f |\n",
        GnlInfoNbCombEquiGates (GnlInfo));
  fprintf (File, "# | SEQ.EQUIV.GATES =%9.1f |\n",
        GnlInfoNbSeqEquiGates (GnlInfo));
  fprintf (File, "# | TOTAL EQUIV.GATES =%9.1f |\n",
        GnlInfoNbTotalEquiGates (GnlInfo));
        */
  fprintf (File, "# | COMB.DEPTH
                                   =%9d |\n",
          GnlInfoMaxDepth (GnlInfo));
  fprintf (File, "# -----\n");
  free ((char*)GnlInfo);
  return (GNL OK);
/*----*/
/* GnlGnlInfoPrint
/*----*/
void GnlGnlInfoPrint (File, Gnl, GnlInfo)
  FILE
               *File;
  GNL
  GNL INFO Gnlinfo;
  fprintf (File, "# -----\n");
  fprintf (File, "# | INPUTS
                                  =%9d |\n",
          GnlNbIn (Gnl));
  fprintf (File, "# | OUTPUTS
                                  =%9d |\n",
          GnlNbOut (Gnl));
  fprintf (File, "# | LOCALS
                                  =%9d |\n",
          GnlNbLocal (Gnl));
  fprintf (File, "# | CLOCKS
                                  =%9d \\n",
          GnlInfoNbClocks (GnlInfo));
```

```
fprintf (File, "# | FLIP-FLOPS
                                    =%9d \n",
          GnlInfoNbFlipFlops (GnlInfo));
   fprintf (File, "# | LATCHES
                                     =%9d \\n",
          GnlInfoNbLatches (GnlInfo));
   fprintf (File, "# | TRISTATES
                                     =%9d |\n",
          GnlInfoNbTriStates (GnlInfo));
   fprintf (File, "# | BUFFERS
                                    =%9d \n",
          GnlInfoNbBuffers (GnlInfo));
   fprintf (File, "# | LITERALS
                                    =%9d \n",
          GnlInfoNbLit (GnlInfo));
   /*
  fprintf (File, "# | COMB.EQUIV.GATES =%9.1f |\n",
         GnlInfoNbCombEquiGates (GnlInfo));
  fprintf (File, "# | SEQ.EQUIV.GATES =%9.1f |\n",
         GnlInfoNbSeqEquiGates (GnlInfo));
  fprintf (File, "# | TOTAL EQUIV.GATES =%9.1f |\n",
         GnlInfoNbTotalEquiGates (GnlInfo));
  fprintf (File, "# | COMB.DEPTH
                                     =%9d \mid n",
          GnlInfoMaxDepth (GnlInfo));
  fprintf (File, "# -----\n");
  free ((char*)GnlInfo);
}
/*----*/
/* GnlFreeNodeSons
/*----*/
void GnlFreeNodeSons (Node)
  GNL NODE Node;
  GNL NODE SonI;
  if (Node == NULL)
     return;
  if (GnlNodeOp (Node) == GNL_CONSTANTE)
     return;
  if (GnlNodeOp (Node) == GNL_VARIABLE)
     return;
  if (GnlNodeOp (Node) == GNL WIRE)
     return;
  if (GnlNodeSons (Node) == NULL)
     return;
  for (i=0; i <BListSize (GnlNodeSons (Node)); i++)</pre>
     SonI = (GNL_NODE)BListElt (GnlNodeSons (Node), i);
     GnlFreeNodeSons (SonI);
  BListQuickDelete (&GnlNodeSons (Node));
```

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```
}
/*----*/
/* GnlFreeHashNames
/*----*/
/* Physical destruction the Hash Table of Gnl Variables in the Gnl 'Gnl'*/
/*----*/
void GnlFreeHashNames (Gnl)
  GNL
        Gnl;
  BLIST HashTableNames;
        i;
  BLIST
        BucketI;
  int
            j;
  GNL VAR VarJ;
  HashTableNames = GnlHashNames (Gnl);
  for (i=0; i<BListSize (HashTableNames); i++)</pre>
    BucketI = (BLIST)BListElt (HashTableNames, i);
    for (j=0; j<BListSize (BucketI); j++)</pre>
         VarJ = (GNL_VAR)BListElt (BucketI, j);
       if (GnlVarIsVdd (VarJ) | GnlVarIsVss (VarJ))
        continue;
       GnlFreeVar (VarJ);
    if (BucketI)
      BListQuickDelete (&BucketI);
  BListQuickDelete (&HashTableNames);
  SetGnlHashNames (Gnl, NULL);
/*----*/
/* GnlFreeNodesSegments
/*----*/
/st Physical destruction of the segments storing all the Gnl Nodes which st/
/* have been created in the gnl 'Gnl'.
/*----*/
void GnlFreeNodesSegments (Gnl)
 GNL
      Gnl;
 int
 int
 GNL_NODE Node;
 BLIST ListNodesSegments;
 GNL_NODE SegmentI;
 GNL VAR VarI;
 GNL_FUNCTION FunctionI;
```

```
for (i=0; i<BListSize (GnlFunctions (Gnl)); i++)</pre>
        VarI = (GNL_VAR)BListElt (GnlFunctions (Gnl), i);
        FunctionI = GnlVarFunction (VarI);
      GnlFreeNodeSons (GnlFunctionOnSet (FunctionI));
      GnlFreeNodeSons (GnlFunctionDCSet (FunctionI));
   ListNodesSegments = GnlNodesSegments (Gnl);
   for (i=0; i<BListSize (ListNodesSegments)-1; i++)</pre>
      SegmentI = (GNL_NODE)BListElt (ListNodesSegments, i);
        Node = SegmentI;
        for (j=0; j<SEGMENT NODE SIZE; j++)
             if (GnlNodeOp (Node) == GNL_CONSTANTE)
                continue;
             if (GnlNodeOp (Node) == GNL_VARIABLE)
                continue;
             BListQuickDelete (&GnlNodeSons (Node));
             SetGnlNodeSons (Node, NULL);
            Node++;
      free ((char*)SegmentI);
   SegmentI = (GNL_NODE)BListElt (ListNodesSegments, i);
  Node = SegmentI;
  while (Node < GnlLastNode (Gnl))</pre>
          if (GnlNodeOp (Node) == GNL_CONSTANTE)
             Node++;
             continue;
         if (GnlNodeOp (Node) == GNL VARIABLE)
             Node++;
             continue;
         BListQuickDelete (&GnlNodeSons (Node));
         SetGnlNodeSons (Node, NULL);
         Node++;
  free ((char*)SegmentI);
  SetGnlFirstNode (Gnl, NULL);
  SetGnlLastNode (Gnl, NULL);
/* GnlPropagateInvInNode
/*----*/
GNL_STATUS GnlPropagateInvInNode (Gnl, Node, DoNegation, NewNode)
```

```
GNL
                Gnl;
GNL_NODE Node;
int
               DoNegation;
GNL_NODE *NewNode;
int
BLIST
         SonList;
GNL NODE SonI;
BLIST
         Sons;
GNL NODE SNode;
GNL_NODE VarNode;
if (Node == NULL)
    *NewNode = NULL;
    return (GNL OK);
if (GnlCreateNode (Gnl, NULL, NewNode))
   return (GNL_MEMORY_FULL);
switch (GnlNodeOp (Node)) {
   case GNL_AND:
        if (BListCreateWithSize (1, &SonList))
             return (GNL_MEMORY_FULL);
          if (!DoNegation)
          SetGnlNodeOp (*NewNode, GNL_AND);
          Sons = GnlNodeSons (Node);
          for (i=0; i<BListSize (Sons); i++)</pre>
                   SonI = (GNL_NODE)BListElt (Sons, i);
               if (GnlPropagateInvInNode (Gnl, SonI, 0, &SNode))
                      return (GNL_MEMORY_FULL);
               if (BListAddElt (SonList, (int)SNode))
                      return (GNL MEMORY FULL);
          SetGnlNodeSons (*NewNode, SonList);
          return (GNL_OK);
       else
         SetGnlNodeOp (*NewNode, GNL OR);
          Sons = GnlNodeSons (Node);
          for (i=0; i<BListSize (Sons); i++)</pre>
               SonI = (GNL_NODE)BListElt (Sons, i);
               if (GnlPropagateInvInNode (Gnl, SonI, 1, &SNode))
                  return (GNL_MEMORY_FULL);
                 if (BListAddElt (SonList, (int)SNode))
                    return (GNL_MEMORY FULL);
            SetGnlNodeSons (*NewNode, SonList);
            return (GNL_OK);
       break;
```

```
case GNL OR:
     if (BListCreateWithSize (1, &SonList))
          return (GNL_MEMORY_FULL);
       if (!DoNegation)
       SetGnlNodeOp (*NewNode, GNL_OR);
       Sons = GnlNodeSons (Node);
       for (i=0; i<BListSize (Sons); i++)</pre>
                SonI = (GNL_NODE)BListElt (Sons, i);
            if (GnlPropagateInvInNode (Gnl, SonI, 0, &SNode))
                   return (GNL_MEMORY_FULL);
            if (BListAddElt (SonList, (int)SNode))
                   return (GNL MEMORY FULL);
       SetGnlNodeSons (*NewNode, SonList);
       return (GNL_OK);
     else
       SetGnlNodeOp (*NewNode, GNL AND);
       Sons = GnlNodeSons (Node);
       for (i=0; i<BListSize (Sons); i++)</pre>
            SonI = (GNL_NODE)BListElt (Sons, i);
            if (GnlPropagateInvInNode (Gnl, SonI, 1, &SNode))
               return (GNL MEMORY FULL);
              if (BListAddElt (SonList, (int)SNode))
                 return (GNL MEMORY FULL);
         SetGnlNodeSons (*NewNode, SonList);
         return (GNL OK);
     break;
case GNL NOT:
       Sons = GnlNodeSons (Node);
       SonI = (GNL_NODE)BListElt (Sons, 0);
     if (DoNegation)
        return (GnlPropagateInvInNode (Gnl, SonI, 0, NewNode));
     else
        return (GnlPropagateInvInNode (Gnl, SonI, 1, NewNode));
       break;
case GNL VARIABLE:
       if (!DoNegation)
           SetGnlNodeOp (*NewNode, GNL VARIABLE);
       SetGnlNodeSons (*NewNode, GnlNodeSons (Node));
       return (GNL_OK);
     else
      {
```

```
SetGnlNodeOp (*NewNode, GNL NOT);
           if (BListCreateWithSize (1, &SonList))
              return (GNL_MEMORY_FULL);
               if (GnlCreateNode (Gnl, GNL_VARIABLE, &VarNode))
                 return (GNL MEMORY FULL);
           SetGnlNodeSons (VarNode, GnlNodeSons (Node));
           if (BListAddElt (SonList, (int)VarNode))
                 return (GNL_MEMORY FULL);
           SetGnlNodeSons (*NewNode, SonList);
           return (GNL_OK);
         break;
     case GNL CONSTANTE:
         SetGnlNodeOp (*NewNode, GNL_CONSTANTE);
         if (DoNegation)
          SetGnlNodeSons (*NewNode, (BLIST)!GnlNodeSons(Node));
          SetGnlNodeSons (*NewNode, (BLIST)GnlNodeSons(Node));
         return (GNL OK);
     }
/*----*/
/* GnlPropagateInv
/*----*/
/* This procedure propagates the GNL_NOT node operator down to the */
/* leaves. Nodes trees are physically duplicated and new segments of
                                                              */
/* nodes are created in 'Gnl'. The original segments of nodes of 'Gnl' */
/* are freed.
/*----*/
GNL_STATUS GnlPropagateInv (Gnl)
               Gnl;
  int
               i;
  char
               *CopyName;
  GNL_VAR VarI;
  GNL FUNCTION
              FunctionI;
  GNL NODE NewOnSet;
  GNL NODE NewDCSet;
  BLIST NewListFunctions;
  int
              j;
  BLIST NodesSegments;
  GNL
               NewGnl;
  /* We use an intermediate GNL to do the propagation and duplication */
  if ((NewGnl = (GNL)calloc (1, sizeof(GNL_REC))) == NULL)
     return (GNL MEMORY FULL);
  SetGnlNbIn (NewGnl, GnlNbIn (Gnl));
  SetGnlNbOut (NewGnl, GnlNbOut (Gnl));
  SetGnlNbLocal (NewGnl, GnlNbLocal (Gnl));
```

```
SetGnlInputs (NewGnl, GnlInputs (Gnl));
SetGnlOutputs (NewGnl, GnlOutputs(Gnl));
SetGnlLocals (NewGnl, GnlLocals (Gnl));
SetGnlHashNames (NewGnl, GnlHashNames (Gnl));
if (BListCreate (&NodesSegments))
   return (GNL MEMORY FULL);
SetGnlNodesSegments (NewGnl, NodesSegments);
SetGnlFirstNode (NewGnl, NULL);
SetGnlLastNode (NewGnl, NULL);
SetGnlComponents (NewGnl, GnlComponents (Gnl));
if (BListCreate (&NewListFunctions))
   return (GNL MEMORY FULL);
for (i=0; i<BListSize (GnlFunctions (Gnl)); i++)</pre>
     VarI = (GNL_VAR)BListElt (GnlFunctions (Gnl), i);
   FunctionI = GnlVarFunction (VarI);
   if (GnlPropagateInvInNode (NewGnl, GnlFunctionOnSet (FunctionI),
                           0, &NewOnSet))
      return (GNL_MEMORY FULL);
   if (GnlPropagateInvInNode (NewGnl, GnlFunctionDCSet (FunctionI),
                           0, &NewDCSet))
      return (GNL_MEMORY FULL);
   /* 'NewListFunctions' is a list of couples: NewOnSet node and
                                                                       */
   /* 'NewDCSet' node.
     if (BListAddElt (NewListFunctions, (int)NewOnSet))
      return (GNL_MEMORY_FULL);
     if (BListAddElt (NewListFunctions, (int)NewDCSet))
      return (GNL MEMORY FULL);
GnlFreeNodesSegments (Gnl);
for (j=0; j<BListSize (NewListFunctions)-1; j += 2)</pre>
     VarI = (GNL VAR)BListElt (GnlFunctions (Gnl), i);
   FunctionI = GnlVarFunction (VarI);
   NewOnSet = (GNL_NODE)BListElt (NewListFunctions, j);
  NewDCSet = (GNL_NODE)BListElt (NewListFunctions, j+1);
   /* we update the function 'FunctionI' with the new Nodes. */
   SetGnlFunctionOnSet (FunctionI, NewOnSet);
   SetGnlFunctionDCSet (FunctionI, NewDCSet);
   i++;
    }
```

```
BListQuickDelete (&NewListFunctions);
  /* We move the segments of nodes from 'NewGnl' to 'Gnl'.
  SetGnlNodesSegments (Gnl, GnlNodesSegments (NewGnl));
  SetGnlFirstNode (Gnl, GnlFirstNode (NewGnl));
  SetGnlLastNode (Gnl, GnlLastNode (NewGnl));
  free ((char*)NewGnl);
  return (GNL OK);
/* GnlFreeConcatNode
void GnlFreeConcatNode (ConcatNode)
  GNL_NODE ConcatNode;
  BListQuickDelete (&(GnlNodeSons (ConcatNode)));
  free (ConcatNode);
/*----*/
/* GnlFreeAssoc
/*----*/
void GnlFreeAssoc (Assoc)
  GNL_ASSOC Assoc;
  GNL_VAR Actual;
  /* Formal is a GNL_VAR and has been freed in the Hash names freeing */
  Actual = GnlAssocActualPort (Assoc);
  if (!Actual | GnlVarIsVar (Actual))
     free (Assoc);
     return;
  /* Actual is a GNL_NODE (GNL_CONCAT) that we can free
  GnlFreeConcatNode (Actual);
  free (Assoc);
/*----
/* GnlFreeUserComponent
/*-----
void GnlFreeUserComponent (UserCompo)
  GNL_USER_COMPONENT UserCompo;
  BLIST Interface;
  int
            i;
  GNL ASSOC
            AssocI;
```

```
Interface = GnlUserComponentInterface (UserCompo);
   for (i=0; i<BListSize (Interface); i++)</pre>
     AssocI = (GNL_ASSOC)BListElt (Interface, i);
     GnlFreeAssoc (AssocI);
  BListQuickDelete (&Interface);
  free (GnlUserComponentInstName (UserCompo));
  free (UserCompo);
/* GnlFreeBufComponent
/*----*/
void GnlFreeBufComponent (BufCompo)
  GNL_BUF_COMPONENT BufCompo;
  BLIST
             Interface;
  int
             i;
  GNL ASSOC
             AssocI;
  Interface = GnlBufInterface (BufCompo);
  for (i=0; i<BListSize (Interface); i++)</pre>
      AssocI = (GNL_ASSOC)BListElt (Interface, i);
      GnlFreeAssoc (AssocI);
  BListQuickDelete (&Interface);
  if (GnlBufInstName (BufCompo))
     free (GnlBufInstName (BufCompo));
  free (BufCompo);
/*----*/
/* GnlFreeComponent
/*----*/
void GnlFreeComponent (Component)
  GNL_COMPONENT Component;
  GNL_USER_COMPONENT
                   UserCompo;
  switch (GnlComponentType (Component)) {
     case GNL_SEQUENTIAL COMPO:
     case GNL_TRISTATE COMPO:
    case GNL BUF COMPO:
    case GNL MACRO COMPO:
         return;
    case GNL_USER COMPO:
```

```
UserCompo = (GNL_USER_COMPONENT)Component;
          GnlFreeUserComponent (UserCompo);
          return;
      }
}
/* GnlUpdateComponentGnlDefOfGnl
/*----*/
void GnlUpdateComponentGnlDefOfGnl (Gnl, ListGnls)
               Gnl;
   BLIST
              ListGnls;
   int
   GNL COMPONENT ComponentI;
   GNL_USER_COMPONENT
                     UserCompo;
   int
                      j;
   GNL
                      GnlJ;
   for (i=0; i<BListSize (GnlComponents (Gnl)); i++)</pre>
       ComponentI = (GNL_COMPONENT)BListElt (GnlComponents (Gnl), i);
       if (GnlComponentType (ComponentI) != GNL_USER_COMPO)
          continue;
       UserCompo = (GNL_USER_COMPONENT)ComponentI;
       for (j=0; j<BListSize (ListGnls); j++)</pre>
            GnlJ = (GNL)BListElt (ListGnls, j);
            if (!strcmp (GnlName (GnlJ),
                       GnlUserComponentName (UserCompo)))
               SetGnlUserComponentGnlDef (UserCompo, GnlJ);
               break;
           }
      }
}
/* GnlUpdateComponentGnlDef
/*----*/
void GnlUpdateComponentGnlDef (ListGnls)
        ListGnls;
  BLIST
  int
                i;
  GNL
                GnlI;
  for (i=0; i<BListSize (ListGnls); i++)</pre>
       GnlI = (GNL)BListElt (ListGnls, i);
       GnlUpdateComponentGnlDefOfGnl (GnlI, ListGnls);
      }
}
```

```
/*----*/
/* GnlUpdateFanoutOfVar
/*----*/
void GnlUpdateFanoutOfVar (Var)
  GNL VAR Var;
  GNL NODE Node;
  GNL_VAR NewVar;
  if (GnlVarIsVss (Var) || GnlVarIsVdd (Var))
    return;
  /* 'Var' is then used one more time
                                            */
  SetGnlVarHook (Var, (int)GnlVarHook (Var)+1);
}
/*----*/
/* GnlUpdateFanoutOfActual
/*-----
void GnlUpdateFanoutOfActual (Actual)
  GNL_VAR Actual;
  int
  BLIST
            Sons;
  GNL NODE
            NodeI;
  GNL_VAR
            VarI;
  if (GnlVarIsVar (Actual))
    GnlUpdateFanoutOfVar (Actual);
    return;
  /* nodes.
  Sons = GnlNodeSons ((GNL NODE)Actual);
  for (i=0; i<BListSize (Sons); i++)</pre>
     VarI = (GNL_VAR)BListElt (Sons, i);
     GnlUpdateFanoutOfVar (VarI);
}
/* GnlUpdateFanoutFromUserCompo
/*----*/
void GnlUpdateFanoutFromUserCompo (UserCompo)
 GNL USER COMPONENT
               UserCompo;
{
 int
            i;
```

```
GNL ASSOC AssocI;
   GNL_VAR Actual;
   GNL_VAR Formal;
   BLIST Interface;
   Interface = GnlUserComponentInterface (UserCompo);
   for (i=0; i<BListSize (Interface); i++)</pre>
       AssocI = (GNL_ASSOC)BListElt (Interface, i);
       Actual = GnlAssocActualPort (AssocI);
       if (!Actual)
          continue;
       Formal = GnlAssocFormalPort (AssocI);
       if (GnlVarDir (Formal) == GNL_VAR_OUTPUT)
          continue;
       GnlUpdateFanoutOfActual (Actual);
/* GnlUpdateFanoutFromUserCompoCell
void GnlUpdateFanoutFromUserCompoCell (UserCompo)
  GNL USER COMPONENT UserCompo;
  int
  GNL ASSOC
                       AssocI;
  GNL VAR
                       Actual;
  char
                       *Formal;
  BLIST
                       Interface;
  LIBC PIN
                PinFormal;
  LIBC CELL
                     Cell;
  LIBC NAME LIST ListPinName;
  Interface = GnlUserComponentInterface (UserCompo);
  Cell = GnlUserComponentCellDef (UserCompo);
  /* If it is a real black box we do not count the ports
  if (!Cell)
     return ;
  for (i=0; i<BListSize (Interface); i++)</pre>
       AssocI = (GNL_ASSOC)BListElt (Interface, i);
       Actual = GnlAssocActualPort (AssocI);
       if (!Actual)
          continue;
       Formal = GnlAssocFormalPort (AssocI);
```

```
PinFormal = GnlGetPinCellWithName (Cell, Formal);
       if (!PinFormal)
           fprintf (stderr,
                   " ERROR: cannot find pin name <%s> in cell [%s]\n",
                   Formal, LibCellName (Cell));
          exit (1);
       if (LibPinDirection (PinFormal) == OUTPUT E)
         continue;
       GnlUpdateFanoutOfActual (Actual);
}
/*----*/
/* GnlUpdateFanoutFromUserCompoKeepVar
/*----*/
void GnlUpdateFanoutFromUserCompoKeepVar (UserCompo)
  GNL_USER_COMPONENT UserCompo;
  int
             i;
  GNL_ASSOC AssocI;
  GNL_VAR
           Actual;
  char
             *Formal;
  BLIST
             Interface;
  LIBC_PIN PinFormal;
  LIBC_CELL
            Cell;
  Interface = GnlUserComponentInterface (UserCompo);
  Cell = GnlUserComponentCellDef (UserCompo);
  /* If it is a real black box we do not count the ports
                                                           */
  if (!Cell)
     return;
  for (i=0; i<BListSize (Interface); i++)</pre>
      AssocI = (GNL_ASSOC)BListElt (Interface, i);
      Actual = GnlAssocActualPort (AssocI);
      if (!Actual)
         continue;
      Formal = GnlAssocFormalPort (AssocI);
      PinFormal = GnlGetPinCellWithName (Cell, Formal);
       if (!PinFormal)
    fprintf (stderr, " ERROR: cannot find pin name <%s> in cell [%s]\n",
                  Formal, LibCellName (Cell));
          exit (1);
```

```
if (LibPinDirection (PinFormal) == OUTPUT E)
         continue;
      GnlUpdateFanoutOfActual (Actual);
}
/* GnlUpdateFanoutFromSeqCompo
                                                            */
/*----*/
void GnlUpdateFanoutFromSeqCompo (SeqCompo)
  GNL_SEQUENTIAL_COMPONENT SeqCompo;
  GNL_VAR Signal;
  Signal = GnlSequentialCompoInput (SeqCompo);
  GnlUpdateFanoutOfActual (Signal);
  Signal = GnlSequentialCompoClock (SeqCompo);
  GnlUpdateFanoutOfActual (Signal);
  Signal = GnlSequentialCompoReset (SeqCompo);
  if (Signal)
     GnlUpdateFanoutOfActual (Signal);
  Signal = GnlSequentialCompoSet (SeqCompo);
  if (Signal)
     GnlUpdateFanoutOfActual (Signal);
/* GnlUpdateFanoutFromTriStateCompo
/*----*/
void GnlUpdateFanoutFromTriStateCompo (TriStateCompo)
  GNL_TRISTATE_COMPONENT TriStateCompo;
  GNL_VAR
            Signal;
  Signal = GnlTriStateInput (TriStateCompo);
  GnlUpdateFanoutOfActual (Signal);
  Signal = GnlTriStateSelect (TriStateCompo);
  GnlUpdateFanoutOfActual (Signal);
}
/* GnlUpdateFanoutFromBufCompo
/*----*/
void GnlUpdateFanoutFromBufCompo (BufCompo)
  GNL_BUF_COMPONENT BufCompo;
  GNL VAR
           Signal;
```

```
Signal = GnlBufInput (BufCompo);
  GnlUpdateFanoutOfActual (Signal);
/*----*/
/* GnlUpdateFanoutOnVarRec
/*----*/
void GnlUpdateFanoutOnVarRec (Var)
  GNL_VAR Var;
  GNL NODE Node;
  GNL VAR RecVar;
  GnlUpdateFanoutOfVar (Var);
  if (!GnlVarFunction (Var))
    return:
  Node = GnlFunctionOnSet (GnlVarFunction (Var));
  if (GnlNodeOp (Node) != GNL_VARIABLE)
    return;
  RecVar = (GNL_VAR)GnlNodeSons (Node);
  GnlUpdateFanoutOnVarRec (RecVar);
/*----*/
/* GnlComputeMaxFanoutInGnl
/*----*/
/* This procedure computes the MaxFanout found in the qnl 'Gnl' and
/* stores it in the field 'GnlMaxFanout (Gnl)'.
                                           */
/* We use the field 'GnlVarHook' of each var to store the number of
/* times this variable is used.
/*----*/
void GnlComputeMaxFanoutInGnl (Gnl)
  GNL
            Gnl;
                     i;
  GNL_COMPONENT
                ComponentI;
  int
                     MaxFanout;
  BLIST
                BucketI;
                      j;
                VarJ;
  GNL VAR
  BLIST
                HashTableNames;
 BLIST Interface;
GNL_USER_COMPONENT UserCompo;
GNL_TRISTATE_COMPONENT TristateCompo;
GNL_BUF_COMPONENT BufCompo;
  GNL_SEQUENTIAL_COMPONENT SeqCompo;
  GNL_VAR
                 VarI;
```

```
/* We reset the Hook filed of all the Vars of the Gnl.
                                                                  */
GnlResetVarHook (Gnl);
for (i=0; i<BListSize (GnlComponents (Gnl)); i++)</pre>
      ComponentI = (GNL_COMPONENT)BListElt (GnlComponents (Gnl), i);
     switch (GnlComponentType (ComponentI)) {
          case GNL USER COMPO:
                   UserCompo = (GNL_USER_COMPONENT)ComponentI;
                   if (GnlUserComponentFormalType (UserCompo) ==
                       GNL FORMAL_CHAR)
                       GnlUpdateFanoutFromUserCompoCell (UserCompo);
                   else
                      GnlUpdateFanoutFromUserCompo (UserCompo);
                   break;
         case GNL TRISTATE COMPO:
                   TristateCompo = (GNL_TRISTATE_COMPONENT)ComponentI;
                   GnlUpdateFanoutFromTriStateCompo (TristateCompo);
                   break;
              case GNL_BUF_COMPO:
                   BufCompo = (GNL_BUF_COMPONENT) ComponentI;
                   GnlUpdateFanoutFromBufCompo (BufCompo);
                   break;
         case GNL_SEQUENTIAL COMPO:
                   SeqCompo = (GNL_SEQUENTIAL_COMPONENT) Component1;
                   GnlUpdateFanoutFromSeqCompo (SeqCompo);
                  break;
         default:
                   fprintf (stderr, " ERROR: unknow component\n");
                   exit (1);
            }
for (i=0; i<BListSize (GnlOutputs (Gnl)); i++)</pre>
     VarI = (GNL_VAR)BListElt (GnlOutputs (Gnl), i);
     GnlUpdateFanoutOnVarRec (VarI);
    }
MaxFanout = 0;
/* We scan all the variables and pick up the max fanout value */
HashTableNames = GnlHashNames (Gnl);
for (i=0; i<BListSize (HashTableNames); i++)</pre>
     BucketI = (BLIST)BListElt (HashTableNames, i);
     for (j=0; j<BListSize (BucketI); j++)</pre>
          VarJ = (GNL_VAR)BListElt (BucketI, j);
          if ((int)GnlVarHook (VarJ) > MaxFanout)
              MaxFanout = (int)GnlVarHook (VarJ);
```

```
}
  SetGnlMaxFanout (Gnl, MaxFanout);
  /* We reset the Hook field of all the Vars of the Gnl.
  GnlResetVarHook (Gnl);
/*----*/
/* GnlComputeMaxFanoutInGnlKeepVarHook
/*----*/
/* This procedure computes the MaxFanout found in the gnl 'Gnl' and
/* stores it in the field 'GnlMaxFanout (Gnl)'.
/* We use the field 'GnlVarHook' of each var to store the number of
/* times this variable is used.
/st The GNL must be composed only of components and simple assignements st/
/*----*/
void GnlComputeMaxFanoutInGnlKeepVarHook (Gnl)
  GNL
              Gnl;
  int
                         i;
  GNL COMPONENT
                  ComponentI;
  int
                        MaxFanout;
  BLIST
                   BucketI;
  int
  GNL VAR
                   VarJ;
  BLIST
                    HashTableNames;
  BLIST
                    Interface;
  GNL_TRISTATE_COMPONENT
  GNL_TRISTATE_COMPONENT TristateCompo;
GNL_BUF_COMPONENT BufCompo;
  GNL SEQUENTIAL COMPONENT SeqCompo;
  GNL VAR
                    VarI;
  /* We reset the Hook filed of all the Vars of the Gnl.
  GnlResetVarHook (Gnl);
  for (i=0; i<BListSize (GnlComponents (Gnl)); i++)</pre>
      ComponentI = (GNL COMPONENT)BListElt (GnlComponents (Gnl), i);
      switch (GnlComponentType (ComponentI)) {
          case GNL_USER_COMPO:
                 UserCompo = (GNL_USER COMPONENT)ComponentI;
                 GnlUpdateFanoutFromUserCompoKeepVar (UserCompo);
                 break;
         case GNL TRISTATE COMPO:
                 TristateCompo = (GNL_TRISTATE_COMPONENT) ComponentI;
                 GnlUpdateFanoutFromTriStateCompo (TristateCompo);
                 break:
         case GNL_SEQUENTIAL COMPO:
                 SeqCompo = (GNL_SEQUENTIAL COMPONENT)ComponentI;
```

```
GnlUpdateFanoutFromSeqCompo (SeqCompo);
                    break;
                case GNL_BUF_COMPO:
                    BufCompo = (GNL_BUF_COMPONENT)Component1;
                    GnlUpdateFanoutFromBufCompo (BufCompo);
            default:
                    fprintf (stderr, " ERROR: unknow component\n");
                    exit (1);
   for (i=0; i<BListSize (GnlOutputs (Gnl)); i++)
       VarI = (GNL_VAR)BListElt (GnlOutputs (Gnl), i);
       GnlUpdateFanoutOnVarRec (VarI);
   MaxFanout = 0;
   /st We scan all the variables and pick up the max fanout value st/
   HashTableNames = GnlHashNames (Gnl);
   for (i=0; i<BListSize (HashTableNames); i++)</pre>
       BucketI = (BLIST)BListElt (HashTableNames, i);
       for (j=0; j<BListSize (BucketI); j++)</pre>
            VarJ = (GNL_VAR)BListElt (BucketI, j);
            if ((int)GnlVarHook (VarJ) > MaxFanout)
                MaxFanout = (int)GnlVarHook (VarJ);
           }
      }
  SetGnlMaxFanout (Gnl, MaxFanout);
/* GnlComputeMaxFanoutInNetworkInGnlRec
/*----*/
void GnlComputeMaxFanoutInNetworkInGnlRec (Nw, Gnl)
  GNL NETWORK
              Nw;
  GNL
                Gnl;
                       i;
  GNL_COMPONENT Component1;
  GNL_USER_COMPONENT
                      UserCompoI;
  GNL
                      GnlCompoI;
  if (GnlTag (Gnl) == GnlNetworkTag (Nw))
     return;
```

```
SetGnlTag (Gnl, GnlNetworkTag (Nw));
  GnlComputeMaxFanoutInGnlKeepVarHook (Gnl);
  for (i=0; i<BListSize (GnlComponents (Gnl)); i++)</pre>
      ComponentI = (GNL_COMPONENT)BListElt (GnlComponents (Gnl), i);
      if (GnlComponentType (ComponentI) != GNL_USER_COMPO)
        continue;
      UserCompoI = (GNL_USER_COMPONENT) ComponentI;
      GnlCompoI = GnlUserComponentGnlDef (UserCompoI);
      if (!GnlCompoI)
        continue;
      GnlComputeMaxFanoutInNetworkInGnlRec (Nw, GnlCompoI);
}
/*----*/
/* GnlComputeMaxFanoutInNetwork
/*----*/
/* This procedure computes the Fanout of each variable
/*-----*/
void GnlComputeMaxFanoutInNetwork (Nw)
  GNL_NETWORK Nw;
  GNL
             TopGnl;
  SetGnlNetworkTag (Nw, GnlNetworkTag (Nw)+1);
  TopGnl = GnlNetworkTopGnl (Nw);
  GnlComputeMaxFanoutInNetworkInGnlRec (Nw, TopGnl);
}
/*----*/
/* GnlUpdateComponentGnlDef
/*----*/
/* This procedure rebuild the list of functions of the gnl 'Gnl' by
/* scanning all the variables of the Hash Tables of 'Gnl' and adding
/* them if their 'Function' field is not NULL.
/*----*/
GNL_STATUS GnlUpdateGnlFunction (Gnl)
  GNL
             Gnl;
 BLIST
        HashTableNames;
 int
 BLIST
           BucketI;
  int
                 j;
 GNL VAR
            VarJ;
```

```
BSize (GnlFunctions (Gnl)) = 0;
   HashTableNames = GnlHashNames (Gnl);
   for (i=0; i<BListSize (HashTableNames); i++)</pre>
       BucketI = (BLIST)BListElt (HashTableNames, i);
       for (j=0; j<BListSize (BucketI); j++)</pre>
            VarJ = (GNL_VAR)BListElt (BucketI, j);
            if (GnlVarFunction (VarJ))
                if (BListAddElt (GnlFunctions (Gnl), (int)
                               VarJ))
                  return (GNL_MEMORY_FULL);
           }
      }
  return (GNL_OK);
/*----*/
/* GnlAddOutputsCoupleInverter
/*----*/
GNL_STATUS GnlAddOutputsCoupleInverter (Gnl)
  GNL
                Gnl;
  int
                      i;
  GNL_VAR
                VarI;
  GNL_FUNCTION
                      FunctionI;
  GNL NODE
                NodeI;
  GNL NODE
                NodeNot;
  GNL NODE
                NodeNotNot;
  for (i=0; i<BListSize (GnlFunctions (Gnl)); i++)</pre>
       VarI = (GNL_VAR)BListElt (GnlFunctions (Gnl), i);
       FunctionI = GnlVarFunction (VarI);
       if (!FunctionI)
         continue;
       NodeI = GnlFunctionOnSet (FunctionI);
       if (GnlNodeOp (NodeI) == GNL_CONSTANTE)
          continue;
       if (GnlNodeOp (NodeI) == GNL_VARIABLE)
         continue;
       if (GnlCreateNodeNot (Gnl, NodeI, &NodeNot))
         return (GNL MEMORY FULL);
       if (GnlCreateNodeNot (Gnl, NodeNot, &NodeNotNot))
         return (GNL_MEMORY_FULL);
      SetGnlFunctionOnSet (FunctionI, NodeNotNot);
```

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```
}
  return (GNL_OK);
/*-----/
/* GnlPrintFormatSignalName
/* Procedure which prints out a signal name in file 'File' with a */
/* limited size of 'Length'. The name is then truncated and only the end*/
/* of the name is printed out prefixed by '..'.
/*-----/
void GnlPrintFormatSignalName (File, Name, Length)
     *File;
  FILE
  char
           *Name;
  int
           Length;
  int
            i;
  if (strlen (Name) >= Length)
    fprintf (File, "..");
    for (i=strlen (Name)-Length+2; i<strlen (Name); i++)
       fprintf (File, "%c", Name[i]);
  else
    for (i=0; i<strlen (Name); i++)
       fprintf (File, "%c", Name[i]);
    while (i<Length)</pre>
         fprintf (File, " ");
         i++;
    }
/* GnlRemoveFunctionFromGnl
/*-----/
/* This procedure removes the GNL_VAR 'Func' from the list of functions */
/* of the gnl 'Gnl'.
void GnlRemoveFunctionFromGnl (Gnl, Func)
           Gnl;
  GNL VAR Func;
  int
  GNL_VAR FuncI;
  for (i=0; i<BListSize (GnlFunctions (Gnl)); i++)</pre>
     FuncI = (GNL_VAR)BListElt (GnlFunctions (Gnl), i);
```

```
if (FuncI == Func)
             BListDelInsert (GnlFunctions (Gnl), i+1);
             return;
        }
 }
void GnlTrackVarInGnl (Gnl, VarName)
                  Gnl;
    char
                   *VarName;
   int
                   i;
   int
                   j;
   BLIST
                 BucketI;
   {\tt GNL\_VAR}
                 VarJ;
   BLIST
                 HashTableNames;
   GNL_FUNCTION Function;
   GNL_NODE
                 Node;
for (i=0; i<BListSize (GnlFunctions (Gnl)); i++)</pre>
     VarJ = (GNL_VAR) BListElt (GnlFunctions (Gnl), i);
     if (!strcmp (GnlVarName (VarJ), VarName))
         fprintf (stderr, " Oui dans les functions !\n");
         break;
    }
   HashTableNames = GnlHashNames (Gnl);
   fprintf (stderr, "hashtable = %d\n", HashTableNames);
   for (i=0; i<BListSize (HashTableNames); i++)</pre>
        BucketI = (BLIST)BListElt (HashTableNames, i);
        for (j=0; j<BListSize (BucketI); j++)</pre>
             VarJ = (GNL_VAR)BListElt (BucketI, j);
             if (!strcmp (GnlVarName (VarJ), VarName))
                  fprintf (stderr, "Var = %d\n", VarJ);
                 Function = GnlVarFunction (VarJ);
                 Node = GnlFunctionOnSet (Function);
                 GnlPrintNode (stderr, Node);
                 fprintf (stderr, "\n");
       }
}
```

```
/* PrintListVar
/*----*/
/* Prints out the list of all the GNL VAR present in the list 'List' */
/*----*/
void PrintListVar (List)
  BLIST List;
  int
  GNL_VAR VarI;
  for (i=0; i<BListSize (List); i++)</pre>
      VarI = (GNL_VAR)BListElt (List, i);
      if (!VarI)
        fprintf (stderr, "NULL ");
      fprintf (stderr, "%s ", GnlVarName (VarI));
  fprintf (stderr, "\n");
/*----*/
void PrintGnl (Gnl)
  GNL
     Gnl;
  GnlPrintGnl (stderr, Gnl);
/*----*/
/* GnlTrackVarRec
,
/*----*/
void GnlTrackVarRec (Nw, Gnl, GnlName, VarName)
  GNL_NETWORK Nw;
            Gnl;
  char
            *GnlName;
  char
            *VarName;
  int
  GNL COMPONENT ComponentI;
  GNL_USER_COMPONENT UserCompol;
                GnlCompoI;
  BLIST
             Components;
  if (!strcmp (GnlName (Gnl), GnlName))
    GnlTrackVarInGnl (Gnl, VarName);
  Components = GnlComponents (Gnl);
  for (i=0; i<BListSize (Components); i++)</pre>
     ComponentI = (GNL_COMPONENT) BListElt (Components, i);
     if (GnlComponentType (ComponentI) != GNL_USER_COMPO)
       continue;
```

```
UserCompoI = (GNL_USER_COMPONENT) ComponentI;
      GnlCompoI = GnlUserComponentGnlDef (UserCompoI);
      if (!GnlCompoI)
        continue;
      GnlTrackVarRec (Nw, GnlCompoI, GnlName, VarName);
}
/*----*/
/* GnlTrackVar
/*----*/
/* Procedure used to debug a specific variable in the network in a
^{\prime} specific Gnl. You have to specify the Network, the name of the Gnl ^{*\prime}
/* and the name of the variable.
/* Modify the core of 'GnlTrackVarInGnl' for the fields of the var you */
/* want to track.
/*----*/
void GnlTrackVar (Nw, GnlName, VarName)
  GNL NETWORK
             Nw;
  GNL
             TopGnl;
  fprintf (stderr, " !!! TRACKING VAR <%s> in [%s]\n",
        VarName, GnlName);
  TopGnl = GnlNetworkTopGnl (Nw);
  GnlTrackVarRec (Nw, TopGnl, GnlName, VarName);
  fprintf (stderr, " !!! TRACKING VAR DONE\n");
/*----*/
```

```
/*----*/
   File: libc_api.c
Version: 1.0
/*
                                   */
/*
                                   */
    Modifications: -
/*
                                   */
/*
    Documentation: -
/*
   -----
#include <stdio.h>
#include <malloc.h>
#include "blist.h"
#include "gnl.h"
#include "libc_mem.h"
#include "libc_api.h"
/*----*/
/* EXTERN
/*----*/
extern int libc_error_count; extern LIBC_LIB tech_lib;
/*----*/
/* LibRead
/*----*/
LIB_STATUS LibRead (char *FileName, LIBC_LIB *Library)
 int code;
 code = 0;
 libc_error_count = 0;
 if ((code = parse_a_tlib_file (FileName)) || libc_error_count)
   return (LIB_READ_ERROR);
 *Library = tech_lib;
 return (LIB OK);
}
/*----*/
```

```
-----*/
 /*
 /*
       File:
                     libc api.e
                                                          */
 /*
       Version:
                     1.0
                                                          */
 /*
       Modifications: -
                                                          */
 /*
       Documentation: -
                                                          */
 /*
 /*-----
                                                         ----*/
 extern LIBC_OPER_COND LibGetOperatingConditionsFromName (LIBC_LIB Lib, char
extern void LibInitializeDeltaOperCondAndNomOperCond (LIBC_LIB Lib, char
 *Name);
extern float LibScalValue (float
                                        Value,
                    float
                                  KProcess,
                    float
                                  KTemp,
                    float
                                  KVoltage);
extern LIBC_PIN LibGetPinFromNameAndCell (LIBC_CELL Cell, char *PinName);
extern LIBC_TIMING LibGetArcTiming (LIBC_PIN PinIn, LIBC_PIN PinOut);
extern float LibGetScaledValFromTransCapaAndMatrix (float Trans, float Capa,
                                  LIBC_TABLE VAL Matrix,
                                  LIBC CELL Cell,
                                  LIB_MATRIX TYPE Type);
extern LIBC_WIRE_LOAD_SELECT LibGetWireLoadSelectFromName (LIBC_LIB Lib, char
*Name);
extern float LibGetWireLengthFromFanout (LIBC_WIRE_LOAD WireL, int Fanout);
extern float LibGetWireLengthFromCapa (LIBC_WIRE_LOAD WireL, float Capa);
extern float LibGetWireScaledCapaFromLength (LIBC_WIRE_LOAD WireL,
                                  float Length, LIBC_LIB Lib);
extern float LibGetWireScaledResiFromLength (LIBC_WIRE_LOAD WireL,
                                  float Length, LIBC LIB Lib);
extern float LibGetWireAreaFromLength (LIBC_WIRE_LOAD WireL, float Length,
                            LIBC LIB Lib);
extern void LibDelayArcCellRise (float InTransR, float InTransF, float
OutCapa,
                 LIBC_CELL Cell, char *PinInName, char *PinOutName,
                  float *DelayR, float *OutTransR);
extern void LibDelayArcCellFall (float InTransR, float InTransF, float
OutCapa,
                  LIBC CELL Cell, char *PinInName, char *PinOutName,
                  float *DelayF, float *OutTransF);
extern void LibDelayArcCell (float InTransR, float InTransF, float OutCapa,
                 float WireResistance, int Fanout,
```

libc_api.e

```
LIBC_CELL Cell, LIBC_PIN PinInName, LIBC_PIN PinOutName, float *DelayR, float *OutTransR, float *DelayF, float *OutTransF);

extern void LibDelayArcCellRiseSetup (float InTransR, float InTransF, float OutCapa, LIBC_CELL Cell, LIBC_PIN PinIn, LIBC_PIN PinClock, float *DelayR);

extern void LibDelayArcCellFallSetup (float InTransR, float InTransF, float OutCapa, LIBC_CELL Cell, LIBC_PIN PinIn, LIBC_PIN PinClock, float *DelayF);
```

```
/*----*/
  File: libc_api.h
Version: 1.0
Modifications: -
/*
                                          */
/*
                                          */
/*
/*
    Documentation: -
/*
/*----*/
/* LIB STATUS
typedef enum {
    LIB OK,
    LIB READ ERROR,
    LIB MEMORY_FULL
} LIB_STATUS;
/*----*/
/*----*/
typedef enum {
    MATRIX CELL RISE,
    MATRIX CELL FALL,
    MATRIX RISE PROPAGATION,
    MATRIX_FALL PROPAGATION,
    MATRIX_RISE_TRANSITION,
    MATRIX_FALL_TRANSITION,
    MATRIX_SETUP_RISE,
    MATRIX SETUP FALL
} LIB_MATRIX_TYPE;
/*----*/
/* API type definitions
/*----*/
/* Defintion of pointer-based types related to the original types
/* defined in file "libcds.h".
/*----*/
typedef struct libc_operating_condition_rec *LIBC_OPER_COND;
typedef struct libc_oc_power_rail_rec
typedef struct libc_fanout_length_rec
*LIBC_OC_POWER_RAIL;
*LIBC_FAN_LEN;
typedef struct libc_wire_load_rec *LIBC_WIRE_LOAD;
typedef struct libc_wire_load_selection_rec
typedef struct libc_wire_load_from_area_rec
*LIBC_WIRE_LOAD_SELECT;
*LIBC_WIRE_LOAD_FROM_AREA;
typedef struct libc_ff_latch_rec     *LIBC_FF_LATCH;
typedef struct libc_table_val_rec     *LIBC_TABLE_VAL;
typedef struct libc_timing_rec
                             *LIBC_TIMING;
```

```
/*----*/
 /* LIBC NAME LIST
/*----*/
#define LibNameListName(1) ((1)->name)
#define SetLibNameListName(1,n) ((1)->name = n)
#define LibNameListNext(1) ((1)->next)
#define SetLibNameListNext(1,n) ((1)->
                          ((1) - \text{next} = n)
/*----*/
/* LIBC BOOL OPR
/*----*/
/* Selectors related to the LIBC_BOOL_OPR group. */
/*----*/
#define LibBoolOprType(m) ((m)->type)
#define SetLibBoolOprType(m,c) ((m)->type = c)
#define SetLibBoolOprValue(m,c) ((m)->u.value)
                          ((m)->u.value = c)
#define LibBoolOprLeftSon(m)
                          ((m)->L)
#define SetLibBoolOprLeftSon(m,c)
                          ((m) -> L = C)
#define LibBoolOprRightSon(m)
                          ((m) ->R)
\#define SetLibBoolOprRightSon(m,c) ((m)->R = c)
/*----*/
/* LIBC LIB
.
/*----*/
((text_buffer*)(m)->lib_name)
((m)->lib_name = (text_buffer*)c)
#define LibName(m)
#define SetLibName(m,c)
#define LibCells(m)
                         ((m)->cells)
#define SetLibCells(m,c)
                         ((m)->cells = c)
#define LibHook(m)
                          ((m) -> hook)
#define SetLibHook(m,c)
                          ((m) - > hook = c)
#define LibDelayModel(Lib)
                        ((Lib)->delay model)
#define LibTimeUnit(Lib)
                         ((Lib)->time unit)
#define LibNomProcess(Lib)
                         ((Lib)->nom process)
#define LibNomTemp(Lib)
                          ((Lib)->nom_temperature)
#define LibNomVolt(Lib)
                          ((Lib)->nom_voltage)
/* ---- lib_group_stmt */
#define LibLutTemplate(Lib)
                         ((Lib)->lut_template)
#define LibPlutTemplate(Lib)
                          ((Lib)->plut template)
#define LibOperatingCond(Lib)
                          ((Lib)->operating_cond)
#define LibWireLoad(Lib)
                          ((Lib)->wire_load)
#define LibWireLoadSelect(Lib)
                              ((Lib) ->wire_load_selection)
```

libc_api.h

```
/* ---- default attr */
#define LibDefaultCellLeakagePower(Lib) ((Lib)->default_cell_leakage_power)
#define LibDefaultCellPower(Lib) ((Lib)->default cell power)
#define LibDefaultFallWireResistance(Lib) \
     ((Lib)->default fall wire resistance)
#define LibDefaultWireLoad(Lib) ((Lib)->default_wire_load)
#define LibDefaultWireLoadArea(Lib) ((Lib)->default_wire_load_area)
#define DefaultWireLoadCapa(Lib) ((Lib)->default wire load capacitance)
#define LibDefaultWireLoadResistance(Lib) \
     ((Lib) ->default_wire_load_resistance)
#define LibDefaultWireLoadSelect(Lib) ((Lib) -
>default wire load selection)
#define LibDefaultFanoutLoad(Lib) ((Lib)->default_fanout_load)
#define LibDefaultInoutPinCap(Lib) ((Lib)->default_inout_pin_cap)
#define LibDefaultInputPinCap(Lib) ((Lib)->default_input_pin_cap)
#define LibDefaultMaxTransition(Lib) ((Lib)->default_max_transition)
#define LibDefaultOperatingCond(Lib)
                                    ((Lib)-
>default operating conditions)
#define LibDefaultOutputPinCap(Lib) ((Lib)->default output pin cap)
#define LibKFactor(Lib)
                               ((Lib)->k factor)
                          ((Lib)->sc_k_factor)
#define LibScKFactor(Lib)
/*----*/
/* LIBC CELL
/*----*/
/* Selectors related to the CELL group.
/*----*/
#define LibCellArea(c) ((c)->area)
#define SetLibCellArea(c,n) ((c)->area = n)
#define LibCellName(c)
                             ((c)->cell_name)
((c)->cell_name = n)
#define SetLibCellName(c,n)
#define LibCellPins(c)
                               ((c)->pins)
#define SetLibCellPins(c,n)
                                ((c) - pins = n)
#define LibCellNext(c)
                               ((c)->next)
                             ((c) - next = n)
#define SetLibCellNext(c,n)
#define LibCellHook(1)
                              ((l)->hook)
#define SetLibCellHook(1,c) ((1) ->hook = c)
#define LibCellPower(Cell)
                                     ((Cell)->cell_power)
#define LibCellCellLeakagePower(Cell)
                                          ((Cell) ->cell_leakage_power)
#define LibCellDontTouch(Cell)
                                          ((Cell)->dont_touch)
#define LibCellDontUse(Cell)
                                    ((Cell)->dont use)
#define LibCellPad(Cell)
                                    ((Cell)->pad cell)
#define LibCellPad(Cell)
#define LibCellPadType(Cell)
                                    ((Cell)->pad type)
#define LibCellScalingFactors(Cell)
                                  ((Cell)->_scaling_factors)
#define LibCellScanGroup(Cell)
                                          ((Cell)->scan group)
```

```
#define LibCellPinEqual(Cell)
#define LibCellPinOpposite(Cell)
                                  ((Cell)->pin equal)
                                   ((Cell)->pin_opposite)
#define LibCellInternalPowerC(Cell)
                                   ((Cell)->internal power c)
#define LibCellLeakagePower(Cell)
                                   ((Cell)->leakage power)
#define LibCellFFLatch(Cell)
                                   ((Cell)->ff latch)
/*----*/
/* LIBC PIN
/*----*/
/* Selectors related to the PIN group.
#define SetLibDirection(p,d) ((p)->direction) ((p)->direction)
                              ((p) - > direction = d)
#define LibPinFunction(p)
                             ((p)->function)
#define SetLibPinFunction(p,f)
                                ((p) - stunction = f)
#define LibPinNext(p)
                              ((p)->next)
#define SetLibPinNext(p,n)
                              ((p) - next = n)
#define LibPinHook(1)
                              ((1)->hook)
#define SetLibPinHook(1,c)
                              ((1) - > hook = c)
>clock gate enable pin)
#define LibPinFanoutLoad(Pin)
                                   ((Pin)->fanout load)
#define LibPinInvertedOutput(Pin) ((Pin) ->inverted_output)
#define LibPinIsPad(Pin)
#define LibPinMaxFanout(Pin)
                                   ((Pin)->is pad)
                                   ((Pin)->max_fanout)
                                ((Pin)->max_fanout)
((Pin)->max_transition)
#define LibPinMaxTransition(Pin)
#define LibPinMaxCapa(Pin)
#define LibPinMinFanout(Pin)
                                   ((Pin)->max capacitance)
                                   ((Pin)->min fanout)
#define LibPinMinTransition(Pin)
                                   ((Pin)->min transition)
#define LibPinMinCapa(Pin)
                                  ((Pin)->min capacitance)
#define LibPinNextstateType(Pin)
                                  ((Pin)->nextstate type)
#define LibPin3State(Pin)
                                   ((Pin)->three state)
#define LibPinStateFunction(Pin)
                                   ((Pin)->state_function)
#define LibPinTiming(Pin)
                                  ((Pin)->timing)
                                ((Pin)->min_pulse_width)
((Pin)->minimum_period)
((Pin)->internal_power)
#define LibPinMinPulseWidth(Pin)
#define LibPinMinimumPeriod(Pin)
#define LibPinInternalPower(Pin)
#define LibPinType(Pin)
                                   ((Pin)->pin_type)
/*-----/
/* LIBC KFATOR
/*----*/
/* Selectors related to the KFATOR group.
```

```
/*----*/
 #define LibKFactorCellRise(KF)
                                     ((KF)->k_cell_rise)
((KF)->k_cell_fall)
((KF)->k_cell_leakage_power)
((KF)->k_cell_power)
 #define LibKFactorCellFall(KF)
 #define LibKFactorCellLeakagePower(KF)
 #define LibKFactorCellPower(KF)
 #define LibKFactorFallPropagation(KF)
                                     ((KF)->k_fall_propagation)
 #define LibKFactorFallTransition(KF)
                                       ((KF)->k_fall_transition)
 #define LibKFactorRisePropagation(KF)
                                      ((KF)->k_rise_propagation)
 #define LibKFactorRiseTransition(KF)
                                       ((KF)->k_rise transition)
 #define LibKFactorFallWireResistance(KF) ((KF)->k_fall_wire_resistance)
 #define LibKFactorHoldFall(KF)
                                       ((KF)->k hold fall)
 #define LibKFactorHoldRise(KF)
                                       ((KF) ->k hold rise)
 #define LibKFactorPinCap(KF)
                                   ((KF)->k_pin_cap)
 #define LibKFactorPinPower(KF)
                                       ((KF)->k_pin_power)
 #define LibKFactorRiseWireResistance(KF)
                                  ((KF)->k_rise_wire_resistance)
 #define LibKFactorSetupFall(KF)
                                      ((KF)->k_setup_fall)
 #define LibKFactorSetupRise(KF)
                                       ((KF)->k_setup rise)
#define LibKFactorSkewFall(KF)
                                       ((KF)->k skew fall)
#define LibKFactorSkewRise(KF)
                                       ((KF)->k skew rise)
#define LibKFactorSlopeFall(KF)
                                       ((KF)->k_slope fall)
#define LibKFactorSlopeRise(KF)
                                       ((KF)->k_slope rise)
#define LibKFactorWireCap(KF)
                                  ((KF)->k_wire_cap)
#define LibKFactorWireRes(KF)
                                  ((KF)->k wire res)
/*----*/
/* LIBC OPER COND
/*----*/
#define LibOperCondOcName(OperCond)
                                  ((OperCond) ->oc_name)
#define LibOperCondProcess(OperCond)
                                  ((OperCond)->process)
#define LibOperCondTemp(OperCond)
#define LibOperCondVolt(OperCond)
#define LibOperCondTreeType(OperCond)
                                  ((OperCond)->temperature)
                                  ((OperCond)->voltage)
                                       ((OperCond)->tree_type)
#define LibOperCondPowerRail(OperCond)
                                       ((OperCond)->power rail)
#define LibOperCondNext(OperCond)
                                  ((OperCond)->next)
/*----*/
/* LIBC_OC_POWER RAIL
/*----*/
/* Selectors related to the OC_POWER_RAIL group.
/*------*/
#define LibOCPowerRailPowerSupply(OCPowerRail) ((OCPowerRail) ->power_supply)
#define LibOCPowerRailVolt(OCPowerRail) ((OCPowerRail) ->volt)
#define LibOCPowerRailNext(OCPowerRail) ((OCPowerRail) ->next)
/*----*/
/* LIBC FAN LEN
.
/*----*/
#define LibFanLenFanout(FanLength)
                                ((FanLength)->fanout)
#define LibFanLenLength(FanLength)
#define LibFanLenCapa(FanLength)
                              ((FanLength)->length)
((FanLength)->capacitance)
#define LibFanLenCapa(FanLength)
```

```
#define LibFanLenResistance(FanLength) ((FanLength) --
#define LibFanLenArea(FanLength) ((FanLength) -->area)
#define LibFanLenNext(FanLength) ((FanLength) -->next)
                                ((FanLength)->resistance)
/*----*/
/* LIBC WIRE LOAD
/*----*/
((WireLoad)->wl_name)
#define LibWireLoadName(WireLoad)
#define LibWireLoadArea(WireLoad) ((WireLoad) ->area)
#define LibWireLoadCapa(WireLoad) ((WireLoad) ->capacitance)
#define LibWireLoadResistance(WireLoad) ((WireLoad) ->resistance)
#define LibWireLoadSlope(WireLoad) ((WireLoad) ->slope)
#define LibWireLoadFanLen(WireLoad) ((WireLoad) ->fanout_length)
#define LibWireLoadNext(WireLoad) ((WireLoad) ->next)
/*----*/
/* LIBC WIRE_LOAD_FROM_AREA
/*----*/
/* Selectors related to the WIRE LOAD FROM AREA. */
/*----*/
#define LibWireLFromAreaMinArea(WireLFromArea) ((WireLFromArea)->min area)
#define LibWireLFromAreaMaxArea(WireLFromArea) ((WireLFromArea)->max area)
#define LibWireLFromAreaLModel(WireLFromArea) -
> load model)
#define LibWireLFromAreaNext(WireLFromArea)
                                   ((WireLFromArea)->next)
/* LIBC WIRE LOAD SELECT
/*-----/
#define LibWireLoadSelName(WireLoadSel) ((WireLoadSel) ->wls name)
#define LibWireLoadSelTable(WireLoadSel) ((WireLoadSel) -> area_table)
#define LibWireLoadSelNext(WireLoadSel)
                               ((WireLoadSel)->next)
/*----*/
/* LIBC FF LATCH
/*-----/
/*----*/
((f) -> Q \text{ name} = n)
#define LibFFLatchQNName(f) ((f)->QN_name)
#define SetLibFFLatchQNName(f,n) ((f)->QN_name = n)
/* 1: normal, >1 bank
                                             */
#define LibFFLatchWidth(f) ((f)->width)
#define SetLibFFLatchWidth(f,n) ((f)->width = n)
#define LibFFLatchIsFF(f)
                          ((f)->is ff)
```

```
#define SetLibFFLatchIsFF(f,n)
                                           ((f) -> is_ff = n)
                                                               */
/* old model
                                      ((f)->is_state)
#define LibFFLatchIsState(f)
                                      ((f) - sis state = n)
#define SetLibFFLatchIsState(f,n)
/* L, H, N, T, X
#define LibFFLatchClPrVar1(f)
                                      ((f)->clear_preset_var1)
#define SetLibFFLatchClPrVar1(f,n)
                                      ((f)->clear preset var1 = n)
/* L, H, N, T, X
#define LibFFLatchClPrVar2(f)
                                      ((f)->clear_preset_var2)
#define SetLibFFLatchClPrVar2(f,n)
                                     ((f)->clear preset_var2 = n)
#define LibFFLatchClear(f)
                                      ((f)->clear)
#define SetLibFFLatchClear(f,n)
                                            ((f) -> clear = n)
#define LibFFLatchPreset(f)
                                      ((f)->preset)
#define SetLibFFLatchPreset(f,n)
                                      ((f)-preset = n)
/* ff group
                                                        */
#define LibFFLatchClockOn(f)
                                      ((f)->clock on)
#define SetLibFFLatchClockOn(f,n)
                                      ((f) - > clock on = n)
/* ff group
                                                        */
#define LibFFLatchNextState(f)
                                           ((f)->next_state)
#define SetLibFFLatchNextState(f,n) ((f)->next state = n)
/* clock on also, enable on also
                                                               */
#define LibFFLatchOnAlso(f)
                                      ((f) -> on also)
#define SetLibFFLatchOnAlso(f,n)
                                      ((f) -> on also = n)
/* latch group
#define LibFFLatchEnable(f)
                                      ((f)->enable)
#define SetLibFFLatchEnable(f,n)
                                      ((f) -> enable = n)
/* latch group
                                                               */
#define LibFFLatchDataIn(f)
                                      ((f)->data in)
#define SetLibFFLatchDataIn(f,n)
                                      ((f)->data in = n)
/* state group
                                                               */
#define LibFFLatchForce00(f)
                                      ((f)->force 00)
#define SetLibFFLatchForce00(f,n)
                                      ((f) -> force 00 = n)
/* state group
                                                               */
#define LibFFLatchForce01(f)
                                      ((f)->force 01)
#define SetLibFFLatchForce01(f,n)
                                      ((f) -> force_01 = n)
/* state group
                                                               */
#define LibFFLatchForce10(f)
                                      ((f)->force 10)
#define SetLibFFLatchForce10(f,n)
                                      ((f) - storce 10 = n)
/* state group
                                                               */
#define LibFFLatchForce11(f)
                                      ((f) -> force 11)
#define SetLibFFLatchForce11(f,n)
                                      ((f) \rightarrow force 11 = n)
```

```
/*----*/
/* LIBC TABLE VAL
/*----*/
/* Selectors related to the TABLE VAL.
/*----*/
/*-----
/*----*/
/*----*/
#define LibTimingRelatedPin(Timing) ((Timing)->related_pin)
#define LibTimingType(Timing)
                          ((Timing)->timing_type)
#define LibTimingSense(Timing)
                               ((Timing) ->timing_sense)
#define LibTimingCellRise(Timing)
#define LibTimingCellFall(Timing)
                          ((Timing)->cell rise)
                           ((Timing) -> cell fall)
#define LibTimingRisePropagation(Timing) ((Timing)->rise_propagation)
#define LibTimingFallPropagation(Timing) ((Timing) -> fall propagation)
#define LibTimingRiseTransition(Timing)
                             ((Timing) -> rise transition)
                             ((Timing) ->fall_transition)
((Timing) ->rise_constraint)
#define LibTimingFallTransition(Timing)
#define LibTimingRiseConstraint(Timing)
#define LibTimingFallConstraint(Timing)
                               ((Timing) -> fall constraint)
#define LibTimingNext(Table)
                          ((Timing)->next)
/*----*/
/* we have difined this global variable because
we cosult it always durring the timing analyser */
extern float G DeltaProcess;
extern float G_DeltaTemp;
extern float G_DeltaVoltage;
extern LIBC OPER COND G OperCond;
extern LIBC WIRE LOAD G WireLoad; /* The wire load corresponding a given
                   netlist. This wire_load is choosed in
                   the library taking into account the
                   global area of the netlist.*/
```

```
libc cell.c -- routines handle cell define
#include "libc def.h"
/* *
public
void libc_cell_init(
    text_buffer *cell_name)
 lib_cell = new_libc_cell_rec();
 lib_cell->_tlib = tech_lib;
 lib_cell->cell_name = cell_name;
 /* ---- set default value */
 lib_cell->cell_leakage_power
                             = tech lib->default cell leakage power;
 lib_cell->cell_power
                          = tech_lib->default_cell power;
public
void libc cell pin init(
    libc name list rec *pin name)
{ libc timing rec *tp;
 if (lib_pin != NULL && lib_pin->members != NULL) {
   /* ---- pin group inside the bundle group */
   lib_bundle = lib pin;
                              /* save */
   /* ---- allocate memory and set default value */
   lib_pin = copy_libc_pin_rec(lib_bundle);
   free_libc_name_list_rec(lib_pin->members);
   lib_pin->members = NULL;
   for(tp=lib pin->timing;tp!=NULL;tp=tp->next)
     tp->_current_pin = lib_pin;
   lib_pin->pin_name
                          = pin_name;
 else if (lib_pin != NULL && lib_pin->is_bus) {
   /* ---- pin group inside the bus group
   * keep the same lib_pin
   lib bus = lib pin;
                        /* save */
   /* --- compare the pin name must cover all bus range */
   /* ==== NOT IMPLEMENT YET ==== */
   free_libc_name_list_rec(pin_name);
 else {
   lib_pin = new_libc_pin_rec();
```

```
/* ---- set default value */
    lib_pin->fanout_load = tech_lib->default_fanout_load;
    lib_pin->max_fanout = tech_lib->default_max_fanout;
lib_pin->max_transition = tech_lib->default_max_transition;
    lib_pin->max_capacitance = tech_lib->default max capacitance;
    /* ---- for ECL model */
    lib pin->pin power
                             = tech_lib->default pin power;
    lib pin->fall wor emitter = tech lib->default fall wor emitter;
    lib_pin->fall_wor_intercept = tech_lib->default_fall_wor_intercept;
    lib_pin->rise_wor_emitter = tech_lib->default_rise_wor_emitter;
    lib pin->rise_wor_intercept = tech lib->default rise wor intercept;
    lib pin->emitter_count = tech_lib->default_emitter_count;
    lib_pin->pin_name
                               = pin_name;
  lib_pin->_current_cell = lib_cell;
/* ------ */
void libc_cell_pin_default(void)
{ switch (lib_pin->direction) {
    case INPUT E :
      if (lib pin->capacitance == 0.0)
       lib_pin->capacitance = tech_lib->default_input_pin_cap;
     break;
   case OUTPUT E :
      if (lib pin->capacitance == 0.0)
       lib pin->capacitance = tech lib->default output pin cap;
     break;
   case INOUT E:
      if (lib pin->capacitance == 0.0)
       lib pin->capacitance = tech lib->default inout pin cap;
     break;
public
libc pin rec *libc cell find pin by name(
      libc cell rec *cell,
      char *pin name,
                       /* < -10000 : no index */
      int index)
{ libc_pin_rec *pp;
  libc_name_list_rec *np;
  char id_name_idx[128],id_name[128],bus_name[128];
  sprintf(id name idx,tech lib->bus naming style,pin name,index);
  sprintf(id_name, "%s", pin_name);
  for (pp=cell->pins;pp!=NULL;pp=pp->next) {
    if (pp->is_bus) {
```

```
int i, from, to, temp;
       /* ---- whole bus pin */
       for (np=pp->pin_name;np!=NULL;np=np->next) {
        if (strcmp(np->name,id_name) == 0)
          return(pp);
                                   /* FOUND */
       /* ---- indexed bus pin */
      from = pp->_bus type->bit from;
      to = pp->_bus_type->bit_to;
      if (from > to) {
        temp = from; from = to; to = temp;
      for (i=from;i<=to;i++) {</pre>
      sprintf(bus_name,tech_lib->bus_naming_style,pp->pin_name->name,i);
        /* if (strcmp(bus_name,id_name) == 0)
         * return(pp);
                                   * FOUND */
        if (strcmp(bus_name,id_name_idx) == 0)
          return(pp);
                                   /* FOUND */
    }
    else {
                 /* ---- bundle and normal pin */
      for (np=pp->pin_name;np!=NULL;np=np->next) {
        if (strcmp(np->name,id_name) == 0)
          return(pp);
                                  /* FOUND */
  return (NULL);
     */
public
int libc_cell_get_bundle_idx(
      libc cell rec *cell,
      char *pin name)
{ libc_pin_rec *pp;
  libc_name_list_rec *np;
  int i;
  for (pp=cell->pins;pp!=NULL;pp=pp->next) {
    if (pp->members == NULL)
     continue;
    for (np=pp->members, i=0;np!=NULL;np=np->next,i++) {
     if (strcmp(np->name,pin_name) == 0)
       return(i);
  return(-99999);
            *---- */
static
libc_pin_rec *libc_cell find pin rec(
     libc_cell_rec *cell,
```

```
char *pin_name,
                       /* < -10000 : no index */
     int index)
{ libc pin rec *pp;
  libc name list rec *np;
  char id_name[128];
  if (index >= -10000)
   sprintf(id_name,tech_lib->bus_naming_style,pin_name,index);
  else
    sprintf(id name, "%s", pin name);
  for (pp=cell->pins;pp!=NULL;pp=pp->next) {
   if (pp->members | pp->is_bus)
                                   /* no bundle and bus group */
     continue;
    for (np=pp->pin_name;np!=NULL;np=np->next) {
     if (strcmp(np->name,id_name) == 0)
                                   /* FOUND */
       return(pp);
 return(NULL);
  */
public
void libc_cell_bundle_post(
     libc pin rec *bundle)
{ libc_cell_rec *cell;
  libc name list rec *mp, *np, *np head=NULL;
  libc_pin_rec *pp;
 libc timing rec *tp;
 cell = bundle-> current cell;
 /* ---- duplicate the members pin */
 for (mp=bundle->members;mp!=NULL;mp=mp->next) {
   pp = libc_cell_find_pin_rec(cell,mp->name,-99999);
   if (pp == NULL) {      /* no entry, need to duplicate */
             = new libc name list rec();
     np->name = copy string(mp->name);
     np->next = np head;
     np_head = np;
 }
 if (np_head != NULL) {
   pp = copy libc pin rec(bundle);
   free_libc_name_list_rec(pp->members);
   pp->members = NULL;
   free_libc_name_list_rec(pp->pin_name);
   pp->pin_name = np_head;
   for(tp=pp->timing;tp!=NULL;tp=tp->next)
     tp->_current_pin = pp;
   pp->next
                = cell->pins;
   cell->pins
                = pp;
}
```

```
public
libc_name_list_rec *libc_cell_bus_name(
     libc pin rec *bus)
{ libc name list rec *np, *np head = NULL;
  int from, to, i;
  char name[128];
 assert(bus->is bus);
  from = bus->_bus_type->bit_from;
  to = bus->_bus_type->bit_to;
  if (from > to) {
   i = from; from = to; to = i;
  for (i=to;i>=from;i--) {
   np = new_libc_name_list_rec();
   sprintf(name, tech_lib->bus_naming_style, bus->pin_name->name, i);
   np->name = copy_string(name);
   np->next = np_head;
   np head = np;
 return(np head);
/* ----- */
/* --- return 1 : in name is slice buse name : BUS[0:3]
       return 0 : A, D[0], ...
 */
static
int libc cell ext bus name (
     char *name,
     libc_name_list_rec **head,
     libc_name_list_rec **tail)
{ int from bit, to bit, tmp;
  char *s, *val, *t;
  libc_name_list_rec *np;
 char bus_n[256];
  (*head) = (*tail) = NULL;
  for (s=name; (*s)!='\0'&&(*s)!='[';s++);
  if ((*s) == ' \setminus 0')
   return(0);
 t = s;
  (*s) = ' \setminus 0';
 val = ++s;
 for (; (*s)!='\0'&&(*s)!=']'&&(*s)!=':';s++);
 if ((*s) == ']' \mid (*s) == ' \setminus 0') {
    (*t) = '[';
   return(0);
  (*s) = ' \setminus 0';
 from bit = atoi(val);
  (*s) = ':';
```

libc_cell.c

```
val = ++s;
 for (;(*s)!='\0'&&(*s)!=']';s++);
  (*s) = ' \ 0';
 to_bit = atoi(val);
  (*s) = ']';
#if O
 if (from bit > to bit) {
   tmp = from_bit; from_bit = to_bit; to_bit = tmp;
 for (tmp=from_bit;tmp<=to_bit;tmp++) {</pre>
   sprintf(bus_n,tech_lib->bus_naming_style,name,tmp);
           = new libc name list rec();
   np->name = copy string(bus n);
   if ((*head) == NULL)
     (*head) = np;
   else
     (*tail) -> next = np;
    (*tail) = np;
  }
#else
 /* ---- may be bug here */
 np = new libc name list rec();
 np->name = copy_string(name);
  (*head) = (*tail) = np;
#endif
  (*t) = '[';
 return(1);
/* ------ */
public
void libc_cell relative_pin handle(
     libc_name_list_rec *head)
{ libc name list rec *np, *next np, *nhp, *ntp;
  for (np=head;np!=NULL;np=next_np) {
   next np = np->next;
   if (libc cell ext bus name(np->name, &nhp, &ntp)) {
     free_text_buffer(np->name);
     np->name = nhp->name;
     nhp->name = NULL;
     if (nhp != ntp) {
       ntp->next = np->next;
       np->next = nhp->next;
       nhp->next = NULL;
     free_libc_name_list_rec(nhp);
 }
}
```

```
public
void libc_cell_name_list_list(
      libc_name_list_list **head,
      libc_name_list_rec *name_list1,
      libc_name_list_rec *name list2)
{ libc_name_list_list *nll;
  nll = new_libc_name_list_list();
  nll->name_list1 = name_list1;
  nll->name_list2 = name_list2;
  nll->next = (*head);
  (*head) = nll;
  *-----*
public
void libc_cell_find_pin_type(
     text buffer *type name)
{ libc_type_rec *p;
  for (p=tech_lib->type;p!=NULL;p=p->next) {
   if (strcmp(p->type_name,type_name) == 0) {
     lib_pin->_bus_type = p;
     break;
  free_text_buffer(type_name);
public
libc_k_factor_rec *libc_cell_k_factor(void)
 if (sc_kfc != NULL)
   return(sc_kfc);
 return(tech_lib->k factor);
/* ----- */
libc_k_factor_rec *libc_cell_find_sc_group(
     text_buffer *name)
{ libc_k_factor_rec *p;
 char msg[128];
 for (p=tech_lib->sc_k_factor;p!=NULL;p=p->next) {
   if (strcmp(name,p->kf_name) == 0) {
     free_text buffer(name);
     return(p);
 sprintf(msg,"scaling_factors group (%s) not found.",name);
 libcerror(msg);
 free_text_buffer(name);
```

libc_cell.c

```
return (NULL);
/* ========================== */
/* ---- ff, latch group */
public
void libc_cell_preset_var(
     char *preset_var,
     text_buffer *str)
  (*preset var) = 'X';
  if (str[0] == '\0' | str[1] != '\0')
   libcerror("only L,H,N,T,X is allowed.");
  else if (str[0] != 'L' && str[0] != 'H' && str[0] != 'N' &&
          str[0] != 'T' && str[0] != 'X')
   libcerror("only L,H,N,T,X is allowed.");
    (*preset var) = str[0];
  free_text_buffer(str);
/* ================ */
/* for test_cell */
/* ----- */
public
void libc_cell_pin_type_assign(
     libc_name_list_rec *pin_names,
     signal_type_E sig_type)
{ libc_pin_rec *pin;
 libc_name_list_rec *np;
 if (pin names == NULL)
   return;
 for (np=pin_names;np!=NULL;np=np->next) {
   pin = libc_cell_find_pin_by_name(lib_tc_cell,np->name,-99999);
   if (pin == NULL)
     continue;
   switch(sig type) {
     case ST_TSI_E :
     case ST_TSII_E :
       pin->pin_type |= l_axubPortTypeScanIn;
     case ST TSO E :
     case ST_TSOI E :
       pin->pin_type |= l_axubPortTypeScanOut;
       break;
     case ST_TSC E :
     case ST_TSCA_E :
     case ST TSCB E :
       pin->pin_type |= l_axubPortTypeScanClock;
       break;
```

```
libc_def.c -- define value handle
 ______ */
#include "libc def.h"
#define HASH SIZE 29
public
int libc def init(void)
 def_table = new_libc_def_table_rec();
 def table->gc_entry = (struct libc_glb_const_rec
**)get ptr buffer(HASH SIZE);
 def table->lib = (struct libc_def_entry_rec **)get_ptr_buffer(HASH_SIZE);
 def_table->cell = (struct libc_def_entry_rec **)get_ptr_buffer(HASH_SIZE);
 def_table->pin = (struct libc_def_entry_rec **)get_ptr buffer(HASH_SIZE);
/* =============== */
 static
 int libc_def_hash_func(
      text_buffer *name)
 { char *s;
   unsigned int v = 0;
  for (s=name; (*s)!='\0';s++)
     v += (*s);
   v %= HASH_SIZE;
   return(v);
  */
public
void libc_def insert(
     text_buffer *def_name,
     text_buffer *region_name, /* library, cell, pin */
     text_buffer *type_name)
{ unsigned int idx;
 libc_def_entry_rec *p;
 libc_def_entry_rec **entry head;
 char msg[256];
 enum value_type vt;
 if (strcmp(region name, "library") == 0)
   entry head = def table->lib;
 else if (strcmp(region_name, "cell") == 0)
   entry_head = def table->cell;
 else if (strcmp(region_name, "pin") == 0)
```

```
entry_head = def_table->pin;
    sprintf(msg, "Not support user define in group %s.", region name);
    libcerror(msg);
  free text buffer (region name);
  if (strcmp(type_name, "integer") == 0 || strcmp(type_name, "float") == 0)
    vt = REAL_VT;
  else if (strcmp(type_name, "string") == 0)
    vt = TEXT_VT;
  else if (strcmp(type_name, "boolean") == 0)
    vt = BOOL_VT;
  else {
    sprintf(msg, "Not support user define type %s.",type_name);
    libcerror (msq);
  free_text_buffer(type_name);
  if (entry_head == NULL) {
    free_text_buffer(def name);
    exit(44);
  idx = libc_def_hash_func(def name);
  for (p=entry_head[idx];p!=NULL;p=p->next) {
    if (strcmp(p->def_name, def_name) == 0) {
                                                    /* find it, overwrite
      free_text_buffer(def_name);
     p->v_type = vt;
     return;
  }
            = new_libc_def entry rec();
 p->next = entry_head[idx];
  p->def name
               = def name;
 p->v type
                  = vt;
  entry_head[idx] = p;
/* ----- */
public
void libc_def_cell_area(
     text_buffer *def_name,
     enum pad_type_E resource_type)
{ text_buffer *area_name, *region_name, *type_name;
  libc_cell_area_rec *cap;
  area_name = copy_string(def name);
 region_name = copy_string("cell");
 type_name = copy_string("float");
 libc_def_insert(def_name, region_name, type_name);
 cap = new libc cell area rec();
 cap->area_name = area_name;
 cap->resource_type = resource_type;
 cap->next = tech_lib->define_cell_area;
```

libc_def.c

```
tech_lib->define_cell_area = cap;
/* ----- */
public
int libc_def_find(
     text_buffer *name,
     enum value_type *vt)
{ unsigned int idx;
  libc_def_entry_rec *p;
  libc_def_entry_rec **entry_head;
  /* ---- find the region first */
  if (lib pin != NULL)
   entry_head = def_table->pin;
  else if (lib cell != NULL)
    entry_head = def_table->cell;
   entry_head = def_table->lib;
  idx = libc_def_hash_func(name);
  for (p=entry_head[idx];p!=NULL;p=p->next) {
   if (strcmp(p->def_name, name) == 0) {
                                                  /* found */
     (*vt) = p->v_type;
                     /* SUCC */
     return(1);
  return(0);
                          /* FAIL (not found) */
/* ================ */
public
void libc_def_gc_insert(
     text_buffer *name,
     float value)
{ int idx;
 libc_glb_const_rec *p;
 idx = libc_def_hash func(name);
 for (p=def_table->gc_entry[idx];p!=NULL;p=p->next) {
   free_text_buffer(name);
     p->value = value;
     return;
 }
           = new_libc_glb_const_rec();
         = def_table->gc_entry[idx];
 p->next
 p->gc_name
                = name;
 p->value
                = value;
 def_table->gc_entry[idx] = p;
}
```

```
libc_def.c
        */
public
int libc_def_gc_find(
      text_buffer *name,
      float *value)
{ unsigned int idx;
 libc_glb_const_rec *p;
 int code = 0;
 idx = libc_def_hash_func(name);
 for (p=def_table->gc_entry[idx];p!=NULL;p=p->next) {
   if (strcmp(p->gc_name, name) == 0) {
                                               /* found */
     free_text_buffer(name);
     (*value) = p->value;
                           /* 1: SUCC */
     return(1);
 }
 if (strcmp(name, "VDD") == 0) {
   (*value) = 3.3;
   code = 1;
 free_text_buffer(name);
 (*value) = 1.0;
 return(code);
                         /* 0: FAIL (not found) */
  *
```

```
/* { */
#ifndef LIBC DEF H
#define LIBC DEF H
#include <stdio.h>
#include <string.h>
#include <assert.h>
#include "libc mem.h"
/* ---- ref axu/axuExt.h */
#define l axubPortTypeIn
                            (0x1)
#define l_axubPortTypeOut
                            (1<<1)
#define l_axubPortTypeIO
                            (1<<2)
#define l axubPortTypeTri
                             (1<<3)
#define l_axubPortTypePower
                            (1 << 4)
                           (1<<5)
#define l_axubPortTypeGround
#define l axubPortTypeClock
                            (1<<6)
#define l axubPortTypeTieUp
                            (1<<7)
#define l_axubPortTypeTieDn
                             (1<<8)
#define l_axubPortTypeAsyncRise (1<<9)</pre>
#define l_axubPortTypeAsyncFall (1<<10)</pre>
#define l_axubPortTypeDataIn
                            (1<<11)
#define l_axubPortTypeDataOut
                             (1<<12)
#define l_axubPortTypeAddressIn (1<<13)</pre>
#define l axubPortTypeEnable (1<<14)</pre>
#define l_axubPortTypeScanIn
                            (1<<15)
#define l_axubPortTypeScanOut
                             (1<<16)
#define l_axubPortTypeScanClock (1<<17)</pre>
#define l_axubPortTypeTriDisable (1<<18)</pre>
#ifndef public
#define public
#endif
/* ----- */
/* sematic rec ----- */
                     /* sematic record */
union yystype {
 int
                     int_val;
 float
                          real val;
 text_buffer
                          *string;
 struct {
   struct libc_name_list rec *head;
   struct libc name list rec * tail;
 } name_list;
 struct {
   struct libc_float_list_rec
                                *head;
   struct libc_float_list_rec
                                *_tail;
```

```
} float list;
  struct {
    struct libc float list list
                                   *head;
    struct libc float list list
                                  * tail;
  } float_list_list;
  struct any unit rec
                             *unit;
  struct libc_cell_rec
                              *cell;
  enum pad_type_E
                       pad type;
  enum variable E
                        var_type;
  enum dont_false E
                        sa_type;
  enum signal type E
                             sig_type;
  struct libc_oc_power rail rec *prp;
  struct libc_bool opr rec
                             *bool opr;
  struct libc timing rec
                              *timing;
};
#define YYSTYPE union yystype
/* =============== */
#ifndef LIBC_MEM
#define LIBC_MEM extern
#define LIBC INIT(s)
#else
#define LIBC INIT(s)
#endif
LIBC_MEM int libc_version LIBC_INIT(330);
LIBC_MEM int libc_ignore_flag LIBC_INIT(0);
LIBC_MEM int libc error count LIBC INIT(0);
LIBC_MEM FILE *Aclf LIBC_INIT(NULL);
                                              /* xxx clf file */
LIBC MEM FILE *Sclf LIBC INIT(NULL);
                                             /* xxx clf file */
LIBC_MEM FILE *Sclf1 LIBC_INIT(NULL);
                                              /* xxx con file for version
3.2 */
LIBC_MEM FILE *Mclf LIBC_INIT(NULL);
                                               /* (power) zzz clf file */
/* ---- value for process */
LIBC_MEM float libc_p_nom LIBC_INIT(1.0); /* normal process */
LIBC_MEM float libc_t_nom LIBC_INIT(25.0); /* normal temp */
LIBC_MEM float libc_v_nom LIBC_INIT(3.3); /* normal volt */
LIBC_MEM float libc_p_min LIBC_INIT(0.8); /* min process */
LIBC_MEM float libc_t_min LIBC INIT(0.0); /* min temp */
LIBC MEM float libc_v_min LIBC_INIT(2.9); /* min volt */
LIBC_MEM float libc_p_max LIBC_INIT(1.2); /* max process */
LIBC_MEM float libc_t_max LIBC_INIT(85.0); /* max temp */
LIBC_MEM float libc_v_max LIBC INIT(3.6); /* max volt */
LIBC_MEM float cap_scale LIBC INIT(1.0);
```

```
LIBC MEM float resist scale LIBC INIT(1.0);
LIBC MEM float time scale LIBC INIT(1.0);
LIBC MEM float watt scale LIBC INIT(1.0);
LIBC MEM float joule scale LIBC INIT(1.0);
/* ----- */
LIBC_MEM struct libc_lib_rec *tech_lib LIBC_INIT(NULL);
LIBC_MEM struct libc_k_factor_rec *kfc LIBC_INIT(NULL);
LIBC MEM struct libc k factor rec *sc kfc LIBC INIT(NULL); /*
scaling factors group */
LIBC_MEM struct libc_def_table_rec *def table LIBC INIT(NULL);
LIBC MEM struct libc_lu_table_template_rec *lu_table_temp LIBC_INIT(NULL);
LIBC_MEM struct libc_lu_table_template_rec *plut_temp LIBC_INIT(NULL);
LIBC MEM struct libc operating condition rec *lib op cond LIBC INIT(NULL);
LIBC MEM struct libc oc power rail rec *lib prp LIBC INIT(NULL);
LIBC_MEM struct libc_power_supply_rec *lib ps LIBC INIT(NULL);
LIBC MEM struct libc timing range rec *lib timing range LIBC INIT(NULL);
LIBC MEM struct libc type rec *lib type name LIBC INIT(NULL);
LIBC MEM struct libc_wire_load_rec *lib_wire_load LIBC_INIT(NULL);
LIBC MEM struct libc wire load selection rec *lib wire load sel
LIBC INIT(NULL);
LIBC MEM struct libc cell rec *lib cell LIBC INIT(NULL);
LIBC_MEM struct libc_pin_rec *lib bundle LIBC INIT(NULL);
LIBC MEM struct libc pin rec *lib bus LIBC INIT(NULL);
LIBC_MEM struct libc_pin_rec *lib_pin LIBC_INIT(NULL);
/* ---- test cell */
LIBC MEM struct libc cell rec *lib tc cell LIBC INIT(NULL);
LIBC MEM struct libc name list rec *lib tc pin name LIBC INIT(NULL);
LIBC MEM struct libc ff latch rec *lib ff latch LIBC INIT(NULL);
LIBC MEM struct libc internal power *lib int power LIBC INIT(NULL);
LIBC MEM struct libc leakage power *lib lkg power LIBC INIT(NULL);
LIBC_MEM struct libc_memory_rec *lib_memory LIBC_INIT(NULL);
LIBC MEM struct libc routing track rec *lib routing track LIBC INIT(NULL);
LIBC MEM struct libc timing rec *lib timing LIBC INIT(NULL);
LIBC MEM struct libc_table_val_rec *lib_timing_tbl LIBC_INIT(NULL);
LIBC MEM struct libc min pulse width rec *lib mpw LIBC INIT(NULL);
LIBC MEM struct libc minimum period rec *lib mp LIBC INIT(NULL);
LIBC_MEM struct libc_memory_write_rec *lib_memory_write LIBC INIT(NULL);
/* ----- */
/* ---- libc cell.c */
libc pin rec *libc cell find pin by name(libc cell rec *,char *,int);
int libc cell get bundle idx(libc cell rec *,char *);
libc name list rec *libc cell bus name(libc pin rec *);
libc k factor rec *libc cell k factor(void);
libc_k_factor_rec *libc_cell_find_sc_group(text_buffer *);
```

```
/* ----- */
/* ---- libc_def.c */
void libc_def_insert(text_buffer *,text buffer *);
/* ----- */
/* ---- libc opr.c */
libc_bool_opr_rec *libc_opr_handle(libc_bool_type_E,int);
void libc_opr_print(FILE *,libc_bool_opr_rec *,libc_cell_rec *,int);
/* ----- */
/* ---- libc time.c */
float_buffer *libc_time_copy_float_buf(float_buffer *);
int libc_time_find_template(text_buffer *,libc lu table template rec
*, libc table val rec *);
void libc time handle(int);
void libc time minimum period(int,float);
/* ----- */
/* ----- */
/* ---- libc util.c */
any_unit_rec *libc_util_unit(float);
float_buffer *libc_util_float_list2buffer(libc_float_list_rec *);
float_buffer **libc_util_float_lists2buffer(libc_float_list_list_*);
float_buffer *libc util float lists3buffer(int,int,libc float list list
libc_bool_opr_rec *libc_util_bool_opr(libc_bool_type_E,int);
void libc_util_wire_load fanout(int,float,float,float,int);
void libc_util_wire_load_table(int,int,float);
void libc_util_wl_select(float,float,text_buffer *);
/* ========== */
                  /* } */
#endif
```

```
*
#include "libc def.h"
#define TRI TBL NO
                     70
/* ============= */
/* ---- k factor */
static
void libc gen k factor(
     libc k factor rec *kfc)
{ int i;
 char *sc_type[3];
 sc type[0] = "process";
 sc type[1] = "temp";
 sc_type[2] = "volt";
 if (kfc->kf name != NULL)
   fprintf(Aclf,"; ========== k factor name : %s ;\n", kfc-
>kf name);
 else
   fprintf(Aclf,"; ======== k factor
 for (i=0; i<3; i++) {
   if (kfc->k cell rise[i] != 0)
     fprintf(Aclf,"; k_%s_cell_rise : %G ;\n",sc type[i],kfc-
>k cell rise[i]);
   if (kfc->k_cell_fall[i] != 0)
     fprintf(Aclf,"; k_%s_cell_fall : %G ;\n",sc type[i],kfc-
>k cell fall[i]);
   if (kfc->k_cell_leakage_power[i] != 0)
     fprintf(Aclf,"; k_%s_cell_leakage_power : %G ;\n",sc_type[i],kfc-
>k_cell_leakage_power[i]);
   if (kfc->k_cell_power[i] != 0)
     fprintf(Aclf,"; k_%s_cell_power : %G ;\n",sc_type[i],kfc-
>k_cell_power[i]);
   if (kfc->k_drive_fall[i] != 0)
     fprintf(Aclf,"; k %s drive fall : %G ;\n",sc type[i],kfc-
>k drive fall[i]);
   if (kfc->k drive rise[i] != 0)
     fprintf(Aclf,"; k_%s_drive_rise : %G ;\n",sc_type[i],kfc-
>k drive rise[i]);
   if (kfc->k fall delay intercept[i] != 0)
     fprintf(Aclf,"; k_%s_fall_delay_intercept : %G ;\n",sc type[i],kfc-
>k_fall_delay_intercept[i]);
   if (kfc->k fall_pin_resistance[i] != 0)
     fprintf(Aclf,"; k_%s_fall_pin_resistance : %G ;\n",sc_type[i],kfc-
>k fall pin resistance[i]);
   if (kfc->k_fall_propagation[i] != 0)
```

```
fprintf(Aclf,"; k %s fall_propagation : %G ;\n",sc_type[i],kfc-
>k fall propagation[i]);
    if (kfc->k fall transition[i] != 0)
      fprintf(Aclf,"; k_%s_fall_transition : %G ;\n",sc_type[i],kfc-
>k fall transition[i]);
    if (kfc->k fall wire resistance[i] != 0)
      fprintf(Aclf,"; k_%s_fall_wire_resistance : %G ;\n",sc type[i],kfc-
>k_fall_wire_resistance[i]);
    if (kfc->k_fall_wor_emitter[i] != 0)
      fprintf(Aclf,"; k %s fall_wor_emitter : %G ;\n",sc_type[i],kfc-
>k fall wor emitter[i]);
    if (kfc->k fall wor intercept[i] != 0)
      fprintf(Aclf,"; k_%s_fall_wor_intercept : %G ;\n",sc_type[i],kfc-
>k fall wor intercept[i]);
    if (kfc->k hold fall[i] != 0)
      fprintf(Aclf,"; k %s hold fall : %G ;\n",sc type[i],kfc-
>k hold fall[i]);
    if (kfc->k_hold_rise[i] != 0)
      fprintf(Aclf,"; k_%s_hold_rise : %G ;\n",sc type[i],kfc-
>k hold rise[i]);
    if (kfc->k_internal_power[i] != 0)
      fprintf(Aclf,"; k_%s_internal_power : %G ;\n",sc_type[i],kfc-
>k internal power[i]);
    if (kfc->k intrinsic fall[i] != 0)
      fprintf(Aclf,"; k_%s_intrinsic_fall : %G;\n",sc_type[i],kfc-
>k intrinsic_fall[i]);
    if (kfc->k intrinsic rise[i] != 0)
      fprintf(Aclf,"; k_%s_intrinsic_rise : %G ;\n",sc_type[i],kfc-
>k intrinsic_rise[i]);
    if (kfc->k min period[i] != 0)
      fprintf(Aclf,"; k %s min period : %G ;\n",sc type[i],kfc-
>k min period[i]);
    if (kfc->k min pulse width high[i] != 0)
      fprintf(Aclf,"; k_%s_min_pulse_width_high : %G ;\n",sc_type[i],kfc-
>k min pulse width high[i]);
    if (kfc->k_min_pulse_width_low[i] != 0)
      fprintf(Aclf,"; k_%s_min_pulse_width low : %G ;\n",sc type[i],kfc-
>k min pulse width low[i]);
    if (kfc->k pin cap[i] != 0)
      fprintf(Aclf,"; k_%s_pin_cap : %G ;\n",sc_type[i],kfc->k_pin_cap[i]);
    if (kfc->k_pin_power[i] != 0)
      fprintf(Aclf,"; k_%s_pin_power : %G ;\n",sc_type[i],kfc-
>k_pin_power[i]);
    if (kfc->k recovery fall[i] != 0)
      fprintf(Aclf,"; k_%s_recovery_fall : %G ;\n",sc_type[i],kfc-
>k recovery fall[i]);
    if (kfc->k recovery rise[i] != 0)
      fprintf(Aclf,"; k %s recovery rise : %G ;\n",sc type[i],kfc-
>k recovery rise[i]);
    if (kfc->k_rise_delay_intercept[i] != 0)
      fprintf(Aclf,"; k_%s_rise_delay_intercept : %G ;\n",sc_type[i],kfc-
>k rise delay intercept[i]);
    if (kfc->k rise pin resistance[i] != 0)
      fprintf(Aclf,"; k_%s_rise_pin_resistance : %G ;\n",sc_type[i],kfc-
>k_rise_pin_resistance[i]);
    if (kfc->k rise propagation[i] != 0)
```

```
fprintf(Aclf,"; k %s rise propagation : %G ;\n",sc type[i],kfc-
>k rise propagation[i]);
    if (kfc->k_rise_transition[i] != 0)
      fprintf(Aclf,"; k_%s_rise_transition : %G;\n",sc type[i],kfc-
>k rise transition[i]);
    if (kfc->k_rise_wire_resistance[i] != 0)
      fprintf(Aclf,"; k_%s_rise_wire_resistance : %G ;\n",sc type[i],kfc-
>k_rise_wire_resistance[i]);
    if (kfc->k_rise_wor_emitter[i] != 0)
     fprintf(Aclf,"; k %s rise_wor_emitter : %G ;\n",sc_type[i],kfc-
>k_rise_wor_emitter[i]);
    if (kfc->k rise wor intercept[i] != 0)
     fprintf(Aclf,"; k_%s_rise_wor_intercept : %G ;\n",sc_type[i],kfc-
>k rise wor intercept[i]);
    if (kfc->k_setup_fall[i] != 0)
     fprintf(Aclf,"; k_%s_setup_fall : %G;\n",sc type[i],kfc-
>k setup fall[i]);
    if (kfc->k_setup_rise[i] != 0)
     fprintf(Aclf,"; k_%s_setup_rise : %G ;\n",sc type[i],kfc-
>k setup rise[i]);
    if (kfc->k_slope_fall[i] != 0)
     fprintf(Aclf,"; k_%s_slope_fall : %G ;\n",sc_type[i],kfc-
>k_slope_fall[i]);
    if (kfc->k slope rise[i] != 0)
     fprintf(Aclf,"; k_%s_slope_rise : %G ;\n",sc_type[i],kfc-
>k slope rise[i]);
   if (kfc->k_wire_cap[i] != 0)
     fprintf(Aclf,"; k_%s_wire_cap : %G ;\n",sc_type[i],kfc->k wire cap[i]);
    if (kfc->k_wire_res[i] != 0)
     fprintf(Aclf,"; k_%s_wire_res : %G ;\n",sc_type[i],kfc->k_wire_res[i]);
  /* ----- */
static
void libc gen scaled value (
     float *k factor,
                           /* OUT */
     float *min_scaled,
     float *max_scaled)
                           /* OUT */
  (*min_scaled) = (1+(libc_p_min-libc_p_nom) * k_factor[0]) *
             (1+(libc_t_min-libc_t_nom) * k_factor[1]) *
             (1+(libc v max-libc_v_nom) * k_factor[2]);
  (*max scaled) = (1+(libc p max-libc p nom) * k factor[0]) *
             (1+(libc t max-libc t nom) * k factor[1]) *
             (1+(libc v min-libc v nom) * k factor[2]);
}
static char *timing table name[8] =
{ "\"RiseCellDelayTable\"", "\"FallCellDelayTable\"",
  "\"RisePropagationTable\"", "\"FallPropagationTable\"", "\"FallTransitionTable\"", "\"FallTransitionTable\"",
  "\"RiseConstraintTable\"", "\"FallConstraintTable\"" };
```

```
----- */
  static
 void libc_gen_table_name(
       char *cell name,
       char *from_pin,
       char *to_pin,
       char unate,
       int tbl idx,
                                /* 0,1,2 */
       int min nom max)
  {
fprintf(Aclf, "\"%s:%s:%s%c%d:%d\"",cell name,from pin,to pin,unate,tbl idx,mi
n nom max);
/* ----- */
  static
  char *libc_gen_varE2str(
       enum variable E varE)
  { switch (varE) {
     case INPUT_NET_TRANSITION : return("\"InputNetTransition\"");
     /* used */
     case TOTAL_OUTPUT_NET_CAPACITANCE : return("\"OutputCapacitance\"");
     /* used */
     case OUTPUT_NET LENGTH :
     return("\"OutputNetLength\"");
                                     return("\"OutputNetWireCap\"");
     case OUTPUT NET WIRE CAP :
     case OUTPUT NET PIN CAP :
     return("\"OutputNetPinCap\"");
     case RELATED OUT TOTAL OUTPUT NET CAP :
     return("\"RelatedOutTotalNetCap\"");
     case RELATED_OUT_OUTPUT_NET_LENGTH :
     return("\"RelatedOutputOutNetLength\"");
     case RELATED OUT OUTPUT NET WIRE CAP :
     return("\"RelatedOutputOutNetWireCap\"");
     case RELATED_OUT_OUTPUT_NET_PIN_CAP :
     return("\"RelatedOutputOutNetPinCap\"");
     case CONSTRAINED_PIN_TRANSITION :
     return("\"ConstrainedPinTransition\"");  /* used */
     case RELATED_PIN_TRANSITION : return("\"RelatedPinTransition\"");
     /* used */
     case OUTPUT PIN TRANSITION :
                                    return("\"OutputPinTransition\"");
                                      return("\"ConnectDelay\"");
     case CONNECT DELAY :
   return(NULL); /* VARIABLE_E_NONE */
 static
  char *libc_gen_timetype2str(
       libc_timing_type_E tt)
```

```
{ switch(tt) {
     case RISING EDGE T :
                                  return("\"CLOCK RISING\"");
     case FALLING EDGE T :
                                  return("\"CLOCK_FALLING\"");
     case PRESET T :
                                  return("\"PRESET\"");
     case CLEAR T :
                                  return("\"CLEAR\"");
                                  return("\"HOLD_RISING\"");
     case HOLD RISING T :
     case HOLD_FALLING_T :
                                  return("\"HOLD_FALLING\"");
     case SETUP_RISING_T :
                                  return("\"SETUP_RISING\"");
                                  return("\"SETUP_FALLING\"");
     case SETUP_FALLING_T :
     case RECOVERY RISING T :
                                        return("\"RECOVERY_RISING\"");
     case RECOVERY FALLING T :
                                        return("\"RECOVERY FALLING\"");
     case THREE STATE DISABLE T :
                                  return("\"DISABLE\"");
     case THREE_STATE_ENABLE_T :
                                  return("\"PATH\"");
     case REMOVAL RISING T :
                                  return("\"REMOVAL RISING\"");
     case REMOVAL_FALLING_T :
                                        return("\"REMOVAL_FALLING\"");
     case COMBINATIONAL T :
                                  return("\"PATH\"");
                                                              /* <-- no
     case SKEW_RISING_T :
                                  return("\"SKEW RISING\"");
use */
                                  return("\"SKEW_FALLING\"");
     case SKEW_FALLING_T :
                                                              /* <-- no
use */
     case NON_SEQ_HOLD_RISING_T : return("\"NONSEQ_HOLD_RISING\"");
     case NON_SEQ_HOLD_FALLING_T : return("\"NONSEQ_HOLD_FALLING\"");
     case NON SEQ SETUP RISING T : return("\"NONSEQ SETUP RISING\"");
     case NON SEQ SETUP FALLING T :
                                        return("\"NONSEQ_SETUP_FALLING\"");
     case NOCHANGE HIGH HIGH T :
                                  return("\"NOCHANGE HIGH HIGH\"");
     case NOCHANGE_HIGH_LOW_T :
                                  return("\"NOCHANGE HIGH LOW\"");
     case NOCHANGE_LOW_HIGH_T :
                                  return("\"NOCHANGE_LOW HIGH\"");
     case NOCHANGE LOW LOW T :
                                        return("\"NOCHANGE LOW LOW\"");
   assert(0);
   return("");
      ----- */
 static
 char *libc_gen_unate2str(
       libc timing sense E ts)
  { switch (ts) {
     case POSITIVE_UNATE E :
                                  return("\"noninvert\"");
     case NEGATIVE_UNATE_E :
                                  return("\"invert\"");
     default :
                                  return("\"nonunate\"");
 }
  */
 static
 char libc gen unate2char(
       libc_timing_sense_E ts)
  { switch (ts) {
     case POSITIVE UNATE E :
                                 return('+');
     case NEGATIVE UNATE E :
                                 return('-');
     default :
                                  return(':');
 }
```

```
----- */
  void libc_gen_get_k_factor_table(
        libc_timing_type_E t_type,
        libc_k_factor_rec *kfc,
        float **kf1,
        float **kf2)
  {
    switch (t type) {
      case COMBINATIONAL T :
      case RISING_EDGE_T :
      case FALLING EDGE T :
      case PRESET T :
      case CLEAR T :
      case THREE STATE DISABLE T :
      case THREE STATE_ENABLE_T :
        /* ---- unchange */
        /* (*kf1) = (float *) (kfc->k_intrinsic_rise); or (kfc->k_cell_rise);
*/
        /* (*kf2) = (float *) (kfc->k_intrinsic_fall); or (kfc->k cell fall);
*/
        break;
      case HOLD_RISING_T :
      case HOLD_FALLING_T :
      case NON SEQ HOLD_RISING_T :
      case NON_SEQ_HOLD_FALLING_T :
        (*kf1) = (float *) (kfc->k_hold_rise);
        (*kf2) = (float *) (kfc->k hold fall);
      case SETUP RISING T :
      case SETUP FALLING T :
      case NON_SEQ_SETUP_RISING_T :
      case NON SEQ SETUP_FALLING_T :
        (*kf1) = (float *) (kfc->k_setup_rise);
        (*kf2) = (float *) (kfc->k_setup fall);
        break;
      case RECOVERY RISING T :
      case RECOVERY FALLING T :
        (*kf1) = (float *) (kfc->k_recovery_rise);
        (*kf2) = (float *) (kfc->k_recovery_fall);
        break;
      case REMOVAL_RISING_T :
      case REMOVAL_FALLING_T :
        (*kf1) = (float *) (kfc->k removal rise);
        (*kf2) = (float *) (kfc->k_removal_fall);
        break;
      case SKEW RISING T :
      case SKEW FALLING_T :
        (*kf1) = (float *) (kfc->k skew rise);
        (*kf2) = (float *) (kfc->k skew fall);
      case NOCHANGE HIGH HIGH T :
      case NOCHANGE_HIGH_LOW_T :
      case NOCHANGE LOW HIGH T :
      case NOCHANGE_LOW_LOW_T :
```

```
(*kf1) = (float *) (kfc->k nochange rise);
       (*kf2) = (float *) (kfc->k nochange fall);
       break;
/* ------ */
  static
 void libc_gen_get_scaling_factor(
       int idx,
       libc_timing_type_E t_type,
       libc k factor_rec *kfc,
       float *min scaled,
       float *max scaled)
  { float *k factor, *kf1, *kf2;
   if (idx >= 6 \&\& idx < TRI TBL NO) {
     kf1 = (float *) (kfc->k_cell rise);
     kf2 = (float *) (kfc->k_cell_fall);
     libc_gen_get_k_factor_table(t_type,kfc,&kf1,&kf2);
     k factor = ((idx%2)==0)? kf1 : kf2;
   else {
     switch((idx % 10)) {
       case 0 : k_factor = (float *) (kfc->k_cell_rise); break;
       case 1 : k_factor = (float *) (kfc->k_cell_fall); break;
       case 2 : k_factor = (float *) (kfc->k_rise_propagation); break;
       case 3 : k_factor = (float *) (kfc->k_fall_propagation); break;
       case 4 : k_factor = (float *) (kfc->k rise transition); break;
       case 5 : k_factor = (float *) (kfc->k_fall transition); break;
   libc_gen_scaled_value(k_factor, min scaled, max scaled);
/* ------- */
 void libc_gen_1D_array(
       FILE *fp,
       float_buffer *array,
       float k_factor)
 { int i, size;
   size = sizeof float buffer(array);
   assert(size > 0);
   fprintf(fp, "(");
   for (i=0;i<size;i++)
     fprintf(fp, "%G ", array[i] *k factor);
   fprintf(fp,")");
/* ----- */
 static
 void libc_gen_timing_values(
```

```
FILE *fp,
       int size1,
       int size2,
       int size3,
       float buffer *array,
       float k_factor)
  { int i,j,k;
    if (size2 == 0) {
                             /* 1D */
     fprintf(fp,"\n '(");
     for (i=0;i<size1;i++)
       fprintf(fp, "%G ", array[i] * k factor);
   else if (size3 == 0) {
                             /* 2D */
     for (i=0;i<size1;i++) {
       if (i == 0)
         fprintf(fp, "\n '(");
       else
         fprintf(fp,"\n
       for (j=0;j<size2;j++)</pre>
         fprintf(fp, "%G ", array[i*size2+j] * k_factor);
    }
                       /* 3D */
   else {
     for (i=0;i<size1;i++) {
       if (i == 0)
         fprintf(fp,"\n '(");
       else
         fprintf(fp,"\n
                         ");
       for (j=0; j < size2; j++) {
         for (k=0;k<size3;k++)
            fprintf(fp, "%G ", array[(i*size2+j)*size3+k] * k factor);
   fprintf(fp,")\n");
/* ----- */
   static
   float libc_gen_varE2scale(
         enum variable_E varE)
    { switch (varE) {
       case INPUT NET TRANSITION :
                                                 return(time scale);
       case TOTAL OUTPUT NET CAPACITANCE :
                                                return(cap scale);
       case OUTPUT NET LENGTH :
                                                 return(1.0);
       case OUTPUT NET WIRE CAP :
                                                 return(cap scale);
       case OUTPUT_NET_PIN_CAP :
                                                 return(cap scale);
       case RELATED OUT TOTAL OUTPUT NET CAP :
                                                 return(cap scale);
       case RELATED OUT OUTPUT NET LENGTH :
                                                 return(1.0);
       case RELATED OUT OUTPUT NET WIRE CAP :
                                                 return(cap scale);
       case RELATED OUT OUTPUT NET PIN CAP :
                                                 return(cap_scale);
       case CONSTRAINED PIN TRANSITION :
                                                 return(time scale);
       case RELATED_PIN_TRANSITION :
                                                 return(time_scale);
```

```
case OUTPUT_PIN_TRANSITION :
                                               return(time scale);
       case CONNECT DELAY :
                                                return(time scale);
     return(1.0);
/* ----- */
 static
 void libc_gen_clf_create_one_table(
       char *cell_name,
       char *from_pin,
       char *to_pin,
       char unate,
       int tbl idx,
       int min_nom_max,
                                  /* min:0,nom:1,max:2 */
       float k factor,
       libc table val rec *table)
  { char *idx_type1,*idx_type2,*idx_type3;
   variable_E v1,v2,v3;
   int size1, size2, size3, tbx name idx;
   fprintf(Aclf, "(clfCreateTable ");
   libc gen table name(cell_name, from pin, to pin, unate, tbl idx, min nom max);
   if (tbl_idx >= TRI_TBL_NO)
     tbl_idx %= 10;
   if (tbl idx > 6)
     tbx_name_idx = (tbl_idx%2)? 7 : 6;
     tbx name idx = tbl idx;
   fprintf(Aclf," %s\n '(",timing_table_name[tbx_name_idx]);
   if (table == NULL) {
                                         /* <--- no value(table) */
     fprintf(Aclf,") '(0)\n)\n");
   else if (table-> tbl == NULL) { /* ---- scalar table */
     fprintf(Aclf,") '(%G)\n)\n",table->scalar val * time scale);
   else {
              = table-> tbl->variable 1;
     v2
              = table->_tbl->variable_2;
               = table->_tbl->variable_3;
     idx_type1 = libc_gen_varE2str(v1);
     idx_type2 = libc_gen_varE2str(v2);
     idx_type3 = libc_gen_varE2str(v3);
     size1 = sizeof float buffer(table->index1);
     fprintf(Aclf, "(%s ", idx_type1);
     libc_gen_1D_array(Aclf,table->index1,libc_gen_varE2scale(v1));
     if (idx_type2 != NULL) {
       size2 = sizeof_float_buffer(table->index2);
       fprintf(Aclf,")\n
                           (%s ",idx type2);
       libc gen 1D array(Aclf,table->index2,libc gen varE2scale(v2));
       if (idx type3 != NULL) {
         size3 = sizeof float buffer(table->index3);
         fprintf(Aclf,")\n (%s ",idx_type3);
         libc gen 1D array(Aclf,table->index3,libc gen varE2scale(v3));
```

```
fprintf(Aclf,"))");
       libc gen timing values(Aclf, size1, size2, size3, table->values, k factor
* time scale);
       else {
         fprintf(Aclf,"))");
       libc gen timing values(Aclf, size1, size2, 0, table->values, k factor *
time_scale);
     else {
       fprintf(Aclf,"))");
     libc_gen_timing_values(Aclf,size1,0,0,table->values,k factor *
time scale);
     fprintf(Aclf,")\n");
/* ----- */
static
void libc_gen_clf_create_table(
     char *cell name,
     char *from pin,
     char *to_pin,
     char unate,
     int tbl idx,
     libc_k_factor_rec *kfc,
       libc_timing_type_E t_type,
     libc table val rec *table,
     int index t[])
                                  /* OUT for min nom max */
{ float min scaled, max scaled;
 libc gen get scaling factor(tbl idx,t type,kfc,&min scaled,&max scaled);
  index t[0] = 0; /* min */
  index_t[1] = 1; /* nom */
 index_t[2] = 2; /* max */
  if (min scaled == 1.0 | table == NULL)
   index t[0] = 1;
 else
libc_gen_clf_create_one_table(cell_name,from_pin,to_pin,unate,tbl idx,0,min s
caled, table);
libc_gen_clf_create one table(cell name, from pin, to pin, unate, tbl idx, 1, 1.0, t
 if (max_scaled == 1.0 || table == NULL)
   index t[2] = 1;
 else
libc_gen_clf_create_one_table(cell_name,from_pin,to_pin,unate,tbl idx,2,max s
caled, table);
}
/* ----- */
```

```
static
int libc_gen_tbx_idx(
      libc_timing_type_E t_type,
      int default value)
  switch (t_type) {
    case HOLD RISING T :
                                return(6);
   case HOLD_FALLING_T :
                                 return(8);
   case SETUP RISING_T :
                                 return(10);
                                 return(12);
return(20);
   case SETUP FALLING T :
   case RECOVERY RISING T :
   case RECOVERY_FALLING_T :
                                return(22);
return(24);
   case REMOVAL RISING T :
                                 return(26);
   case REMOVAL FALLING T :
   case SKEW RISING T :
                                 return(30);
   case SKEW FALLING T :
                                 return(32);
   case NON SEQ HOLD RISING T : return(40);
   case NON SEQ HOLD FALLING T : return(42);
    case NON SEQ SETUP RISING T : return(44);
    case NON_SEQ_SETUP_FALLING_T : return(46);
    case NOCHANGE HIGH HIGH T :
                                         return(50);
    case NOCHANGE_HIGH_LOW_T :
                                         return(52);
    case NOCHANGE_LOW_HIGH_T :
                                        return(54);
    case NOCHANGE LOW_LOW_T :
                                  return(56);
    default :
                                   return(default_value);
/* ----- */
libc pin rec *libc cell find pin by name(libc cell rec *, char *, int);
libc name list rec *libc cell bus name(libc pin rec *);
static
void libc gen TLU timing(
      libc_timing_rec *timing)
{ char *cell_name;
  libc_cell_rec *curr_cell;
  libc_pin_rec *from_pp,*to_pp;
  libc name list rec *from_pin, *to_pin;
  libc_name_list_rec *np1,*next_np1,*np1p,*np2,*next_np2,*np2p;
  libc name_list_rec *np1_head, *np2_head;
  libc_k_factor_rec *kfc;
  int rise_tbl[3],fall_tbl[3],rise_tbl1[3],fall_tbl1[3];
  libc_timing_type_E t_type;
  int i,j,tbl idx,tri idx;
  int iidx, jidx1, jidx2, oidx;
  char unate;
  curr_cell = timing->_current_pin->_current_cell;
          = libc_gen_unate2char(timing->timing_sense);
  cell_name = curr_cell->cell_name;
  from pin = timing->related pin;
           = timing-> current_pin->pin name;
            = (curr_cell->_scaling_factors)? curr_cell->_scaling_factors :
tech_lib->k_factor;
```

```
t type
         = timing->timing_type;
for (np2=to pin;np2!=NULL;np2=np2->next) {
           = libc cell get_bundle_idx(curr_cell,np2->name);
 next np2 = np2->next;
 np2->next = NULL;
 to pp = libc cell find pin_by_name(curr_cell,np2->name,-99999);
 np2_head = (to_pp->is_bus)? libc_cell_bus_name(to_pp) : np2;
          = (to_pp->is_bus)? 0 : -99999;
 for (np2p=np2_head;np2p!=NULL;np2p=np2p->next) {
   for (npl=from_pin;npl!=NULL;npl=npl->next) {
     next np1 = np1->next;
     np1->next = NULL;
     from pp = libc cell find pin by name(curr cell,np1->name,-99999);
   if (from pp != NULL) {
       np1 head = (from pp->members != NULL)? from pp->members :
                 (from_pp->is_bus)? libc cell bus name(from pp) : np1;
               = (from_pp->members != NULL || from_pp->is_bus)? 0 : -99999;
      jidx1
   else {
     np1_head = np1;
     jidx1
            = -99999;
     for (np1p=np1_head;np1p!=NULL;np1p=np1p->next) {
     if (jidx1 == -99999)
                                 /* pin -> pin/bundle/bus */
       iidx = -99999;
       else if (oidx != -99999) { /* bundle/bus -> bundle */
       if (jidx1 != oidx)
         goto next jidx1;
       iidx = oidx;
     else if (jidx2 != -99999) { /* bundle/bus -> bus */
       if (jidx1 != jidx2)
         goto next jidx1;
       iidx = jidx1;
                                  /* bundle/bus -> pin */
     else
       iidx = jidx1;
       switch (t_type) {
         case CLEAR T :
           if (timing->timing_sense == POSITIVE_UNATE_E)
           from_pp->pin_type |= l_axubPortTypeAsyncFall;
           from pp->pin type |= l axubPortTypeAsyncRise;
           break;
         case PRESET_T :
           if (timing->timing sense == POSITIVE UNATE E)
           from_pp->pin_type |= l_axubPortTypeAsyncRise;
           else
           from_pp->pin_type |= l_axubPortTypeAsyncFall;
           break;
         case THREE STATE DISABLE T :
         case THREE_STATE_ENABLE_T :
           from pp->pin type |= l axubPortTypeTriDisable;
           to_pp->pin_type |= l_axubPortTypeTri;
```

```
break;
            case RISING_EDGE_T :
            case FALLING EDGE T :
              to pp->pin type |= l_axubPortTypeDataOut;
             break;
            case SETUP_RISING_T :
            case SETUP FALLING T :
            case NON_SEQ_SETUP_RISING_T :
            case NON_SEQ_SETUP_FALLING_T :
              to pp->pin_type |= l_axubPortTypeDataIn;
              from pp->pin_type |= l_axubPortTypeClock;
             break;
          }
       tri idx = 0;
          switch (t type) {
            case COMBINATIONAL T :
            case RISING_EDGE_T :
            case FALLING EDGE T :
            case PRESET_T :
            case CLEAR_T :
            case THREE STATE DISABLE T :
            case THREE STATE_ENABLE_T :
            if (t_type == THREE_STATE_DISABLE_T)
            tri idx = TRI_TBL_NO;
            if (t_type == THREE_STATE_ENABLE_T)
            tri idx = TRI_TBL_NO + 10;
              if (timing->cell rise != NULL | | timing->cell fall != NULL) {
              tbl idx = 0;
              libc_gen_clf_create_table(cell_name,np1p->name,np2p-
>name, unate, tri_idx+0,
                  kfc,t type,timing->cell_rise,rise_tbl);
              libc gen clf_create_table(cell_name,np1p->name,np2p-
>name, unate, tri_idx+1,
                  kfc,t_type,timing->cell_fall,fall_tbl);
              else if (timing->rise_propagation != NULL || timing-
>fall propagation != NULL) {
              tbl_idx = 2;
              libc_gen_clf_create_table(cell_name,nplp->name,np2p-
>name,unate,tri idx+2,
                  kfc,t type,timing->rise_propagation,rise_tbl);
              libc_gen_clf_create_table(cell_name,np1p->name,np2p-
>name, unate, tri idx+3,
                  kfc,t_type,timing->fall_propagation,fall_tbl);
              if (timing->rise_transition != NULL || timing->fall_transition
!= NULL) {
              libc_gen_clf_create_table(cell_name,np1p->name,np2p-
>name,unate,tri_idx+4,
                  kfc,t type,timing->rise transition,rise tbl1);
              libc_gen_clf_create_table(cell_name,np1p->name,np2p-
>name, unate, tri_idx+5,
                  kfc,t type,timing->fall transition,fall_tbl1);
            break;
```

```
/* default : */
            case HOLD RISING T :
            case HOLD FALLING T :
            case SETUP RISING T :
            case SETUP_FALLING_T :
            case RECOVERY RISING T :
            case RECOVERY FALLING T :
            case REMOVAL_RISING_T :
            case REMOVAL_FALLING_T :
            case SKEW RISING_T :
            case SKEW FALLING T :
            case NON SEQ HOLD RISING T :
            case NON SEQ HOLD FALLING T :
            case NON SEQ_SETUP_RISING_T :
            case NON SEQ SETUP_FALLING T :
            case NOCHANGE HIGH HIGH T :
            case NOCHANGE HIGH LOW T :
            case NOCHANGE_LOW HIGH T :
            case NOCHANGE LOW LOW T :
              if (timing->rise constraint != NULL | | timing->fall constraint)
{
              tbl idx = libc gen tbx idx(t type,10);
              libc gen clf create_table(cell_name,np1p->name,np2p-
>name, unate, tbl_idx,
                  kfc,t_type,timing->rise_constraint,rise_tbl);
              libc gen clf create table(cell name, np1p->name, np2p-
>name, unate, tbl_idx+1,
                  kfc,t type,timing->fall constraint,fall_tbl);
            break;
          /* ---- generate defineTimeTLU */
        if (t type == SKEW RISING T | | t type == SKEW FALLING T) {
            fprintf(Aclf,"(defineCondTimingSkewTLU \"%s\" \"%s\" \"%s\"
\"%s\" \"%s\" \"",cell_name,
            np1p->name, (t_type==SKEW_RISING_T)?"RISE":"FALL",
            np2p->name, (t_type==SKEW_RISING_T)?"RISE":"FALL");
          libc opr print(Aclf, timing->when start, curr cell, iidx);
          fprintf(Aclf,"\" \"");
          libc opr print(Aclf,timing->when_end,curr_cell,iidx);
          fprintf(Aclf,"\"\n '(");
            for (i=0;i<3;i++) {
              libc gen table name(cell_name,np1p->name,np2p-
>name,unate,tbl idx,rise_tbl[i]);
              fprintf(Aclf," ");
              libc gen table name(cell_name,np1p->name,np2p-
>name,unate,tbl_idx+1,fall_tbl[i]);
              fprintf(Aclf, (i==2)?")) \n":" ");
                        /* ---- normal defineTimeTLU */
        else {
          if (timing->when != NULL | | timing->when_start != NULL | | timing-
>when end != NULL) {
              fprintf(Aclf,"(defineCondTimeTLU \"%s\" \"%s\" \"%s\" %s %s\n
\"",cell_name,np1p->name,np2p->name,
```

```
libc gen unate2str(timing-
>timing sense),libc gen timetype2str(t_type));
            libc opr print(Aclf, timing->when, curr_cell, iidx);
            fprintf(Aclf,"\" \"");
            libc opr print(Aclf,timing->when_start,curr_cell,iidx);
            fprintf(Aclf,"\" \"");
            libc opr_print(Aclf,timing->when_end,curr_cell,iidx);
            fprintf(Aclf,"\"\n");
          else {
              fprintf(Aclf,"(defineTimeTLU \"%s\" \"%s\" \"%s\" %s
%s\n",cell_name,np1p->name,np2p->name,
              libc gen unate2str(timing-
>timing sense),libc_gen_timetype2str(t_type));
          fprintf(Aclf," '(");
            for (i=0;i<3;i++) {
              libc gen table_name(cell_name,np1p->name,np2p-
>name,unate,tri_idx+tbl_idx,rise_tbl[i]);
              fprintf(Aclf," ");
              libc_gen_table_name(cell_name,np1p->name,np2p-
>name,unate,tri idx+tbl_idx+1,fall_tbl[i]);
              fprintf(Aclf, (i==2)?") \n '(":" ");
            if (timing->rise_transition != NULL || timing->fall_transition) {
              for (i=0;i<3;i++) {
                libc gen_table_name(cell_name,nplp->name,np2p-
>name,unate,tri idx+4,rise_tbl1[i]);
                fprintf(Aclf," ");
                libc gen table name(cell_name,nplp->name,np2p-
>name,unate,tri_idx+5,fall_tbl1[i]);
                fprintf(Aclf,(i==2)?"))\n":" ");
            else
              fprintf(Aclf,"))\n");
next_jidx1 :
        if (from_pp != NULL)
          jidx1 += (from pp->members != NULL || from_pp->is_bus)? 1 : 0;
        np1->next = next_np1;
        if (from_pp != NULL && from_pp->is_bus)
          free_libc_name_list_rec(np1_head);
      if (to_pp->is_bus)
      jidx2++;
    np2->next = next_np2;
    if (to pp->is bus)
      free libc name list_rec(np2_head);
  }
}
```

```
/* pin(X)
* bundle(A), A1,A2, bundle(B), B1,B2
* bus(C) C[1],C[2], bus(D) D[1],D[2]
* timing : A->B ==> A1->B1, A2->B2
* function : B = A ==> B1 = A1, B2 = A2
* timing : X->B ==> X->B1, X->B2
* function : B = X ==> B1 = X, B2 = X
* timing : A->X ==> A1->X, A2->X
 * function : X = A ==> (shall not happen)
 * timing : C->D ==> C[1]->D[1], C[2]->D[2]
 * function : D = C ==> D[1] = C[1], D[2] = C[2]
* timing : X->D ==> X->D[1], X->D[2]
 * function : D = X ==> D[1] = X, D[2] = X
 * timing : C->X ==> C[1]->X, C[2]->X
 * function : X = C ==> (shall not happen)
 */
static
void libc gen_linear_timing(
      libc timing_rec *timing)
{ char *cell_name;
  libc cell rec *curr cell;
  libc pin rec *from pp, *to pp;
  libc name list rec *from_pin, *to_pin;
  libc_name_list_rec *np1,*next_np1,*np1p,*np2,*next_np2,*np2p;
  libc name list rec *np1_head, *np2_head;
  libc timing type E t_type;
  libc k factor rec *kfc;
  float *k factor,*kf1,*kf2,min_scaled,max_scaled;
  float int_rise[3],int_fall[3];
  float slope_rise[3],slope_fall[3];
  float res rise[3], res_fall[3];
  int i, iidx, jidx1, jidx2, oidx;
  assert (timing->_current_pin->members == NULL);
  curr_cell = timing->_current_pin->_current_cell;
  cell name = curr cell->cell_name;
  from pin = timing->related_pin;
          = timing-> current_pin->pin_name;
            = (curr_cell->_scaling_factors)? curr_cell->_scaling_factors :
  kfc
tech lib->k_factor;
  t_type
            = timing->timing_type;
                                                /* default value */
  kf1 = (float *)(kfc->k_intrinsic_rise);
                                                /* default value */
  kf2 = (float *)(kfc->k_intrinsic_fall);
  libc gen get k_factor_table(t_type, kfc, &kf1, &kf2);
  k factor = kf1;
  libc_gen_scaled_value(k_factor,&min_scaled,&max_scaled);
  int rise[0] = timing->intrinsic_rise * time_scale * min_scaled;
  int_rise[1] = timing->intrinsic_rise * time_scale;
  int_rise[2] = timing->intrinsic_rise * time_scale * max_scaled;
```

```
k_factor = kf2;
libc_gen_scaled_value(k_factor,&min_scaled,&max_scaled);
int_fall[0] = timing->intrinsic_fall * time scale * min scaled;
int_fall[1] = timing->intrinsic_fall * time_scale;
int_fall[2] = timing->intrinsic_fall * time_scale * max_scaled;
/* ---- add time_scale in here */
k_factor = (float *) (kfc->k_slope_rise);
libc_gen_scaled_value(k_factor, &min_scaled, &max scaled);
slope_rise[0] = timing->slope_rise * min_scaled;
slope_rise[1] = timing->slope_rise ;
slope_rise[2] = timing->slope_rise * max scaled;
/* ---- add time scale in here */
k_factor = (float *) (kfc->k_slope_fall);
libc_gen_scaled_value(k_factor, &min_scaled, &max_scaled);
slope_fall[0] = timing->slope fall * min scaled;
slope_fall[1] = timing->slope_fall ;
slope_fall[2] = timing->slope_fall * max scaled;
k_factor = (float *) (kfc->k_drive_rise);
libc_gen_scaled_value(k_factor,&min_scaled,&max_scaled);
res_rise[0] = timing->rise_resistance * resist_scale * min_scaled;
res_rise[1] = timing->rise_resistance * resist scale;
res_rise[2] = timing->rise_resistance * resist_scale * max_scaled;
k_factor = (float *) (kfc->k_drive_fall);
libc_gen_scaled_value(k_factor,&min_scaled,&max_scaled);
res_fall[0] = timing->fall_resistance * resist_scale * min_scaled;
res_fall[1] = timing->fall_resistance * resist_scale;
res_fall[2] = timing->fall_resistance * resist_scale * max_scaled;
for (np2=to_pin;np2!=NULL;np2=np2->next) {
           = libc cell get_bundle_idx(curr_cell,np2->name);
 next np2 = np2 - next;
 np2->next = NULL;
 to_pp = libc_cell_find_pin_by_name(curr_cell,np2->name,-99999);
 np2_head = (to_pp->is_bus)? libc_cell_bus_name(to_pp) : np2;
          = (to_pp->is_bus)? 0 : -99999;
 for (np2p=np2_head;np2p!=NULL;np2p=np2p->next) {
   for (np1=from_pin;np1!=NULL;np1=np1->next) {
     next_np1 = np1->next;
     np1->next = NULL;
     from_pp = libc_cell_find_pin_by_name(curr_cell,np1->name,-99999);
   if (from pp != NULL) {
       np1_head = (from_pp->members != NULL)? from_pp->members :
                 (from_pp->is_bus)? libc_cell_bus_name(from_pp) : np1;
     jidx1
              = (from_pp->members != NULL || from_pp->is_bus)? 0 : -99999;
   else {
     npl head = npl;
     jidx1
            = -999999;
     for (np1p=np1_head;np1p!=NULL;np1p=np1p->next) {
     if (jidx1 == -99999)
                              /* pin -> pin/bundle/bus */
       iidx = -999999;
```

```
else if (oidx != -99999) { /* bundle/bus -> bundle */
          if (jidx1 != oidx)
            goto next jidx1;
          iidx = oidx;
        else if (jidx2 != -99999) { /* bundle/bus -> bus */
          if (jidx1 != jidx2)
           goto next jidx1;
          iidx = jidx1;
        else
                                    /* bundle/bus -> pin */
          iidx = jidx1;
          switch (t_type) {
            case CLEAR_T :
              if (timing->timing_sense == POSITIVE_UNATE_E)
              from pp->pin type |= l axubPortTypeAsyncFall;
              from_pp->pin_type |= l_axubPortTypeAsyncRise;
              break;
            case PRESET T :
              if (timing->timing_sense == POSITIVE_UNATE_E)
              from pp->pin type |= l axubPortTypeAsyncRise;
              from pp->pin type |= l axubPortTypeAsyncFall;
              break;
            case THREE_STATE_DISABLE_T :
            case THREE STATE ENABLE T :
              from pp->pin type |= l axubPortTypeTriDisable;
              to pp->pin type |= l axubPortTypeTri;
              break;
            case RISING EDGE T :
            case FALLING EDGE T :
              to pp->pin_type |= l_axubPortTypeDataOut;
              break;
            case SETUP RISING T :
            case SETUP FALLING T :
            case NON SEQ SETUP RISING T :
            case NON SEQ SETUP FALLING T :
              to pp->pin type |= l_axubPortTypeDataIn;
              from pp->pin type |= l_axubPortTypeClock;
              break;
          }
        if (t_type == SKEW RISING_T || t_type == SKEW FALLING_T) {
            fprintf(Aclf, "defineCondTimingSkew \"%s\" \"%s\" \"%s\" \"%s\"
\"%s\" \"", cell name,
            nplp->name, (t_type==SKEW_RISING T)?"RISE":"FALL",
            np2p->name,(t_type==SKEW_RISING_T)?"RISE":"FALL");
          libc_opr_print(Aclf,timing->when_start,curr_cell,iidx);
          fprintf(Aclf,"\" \"");
          libc_opr_print(Aclf,timing->when_end,curr_cell,iidx);
          fprintf(Aclf,"\" '(%G)\n",int rise[1]);
        }
        else {
          if (timing->when != NULL || timing->when start != NULL || timing-
>when end != NULL) {
```

```
fprintf(Aclf, "defineCondTimeIntrinsic \"%s\" \"%s\" \"%s\" %s
%s \"",
              cell name, np1p->name, np2p->name,
                libc gen unate2str(timing-
>timing_sense),libc_gen_timetype2str(t_type));
            libc opr print (Aclf, timing->when, curr cell, iidx);
            fprintf(Aclf,"\" \"");
            libc opr print(Aclf, timing->when start, curr cell, iidx);
            fprintf(Aclf, "\" \"");
            libc opr print(Aclf, timing->when end, curr cell, iidx);
            fprintf(Aclf, "\"\n");
          else {
              fprintf(Aclf, "defineTimeIntrinsic \"%s\" \"%s\" \"%s\" %s
%s\n",
              cell_name,np1p->name,np2p->name,
                libc gen unate2str(timing-
>timing_sense),libc_gen_timetype2str(t_type));
          }
            fprintf(Aclf," '(%G %G %G %G %G %G)",
int_rise[0],int_fall[0],int_rise[1],int_fall[1],int_rise[2],int_fall[2]);
            switch (t_type) {
              case COMBINATIONAL T :
              case RISING EDGE T :
              case FALLING EDGE T :
              case PRESET T :
              case CLEAR_T :
              case THREE STATE DISABLE T :
              case THREE STATE ENABLE T :
                fprintf(Aclf, "\n '(%G %G %G %G %G %G)\n",
slope_rise[0],slope_fall[0],slope_rise[1],slope_fall[1],slope_rise[2],slope_f
all[2]);
                fprintf(Aclf," '(%G %G %G %G %G)\n",
res_rise[0],res_fall[0],res_rise[1],res fall[1],res rise[2],res fall[2]);
              break;
            default :
              fprintf(Aclf," 0 0\n");
              break:
next_jidx1 :
          if (from pp != NULL)
          jidx1 += (from_pp->members != NULL | | from pp->is bus)? 1 : 0;
        np1->next = next np1;
        if (from pp != NULL && from pp->is bus)
          free_libc_name_list_rec(np1 head);
      if (to pp->is bus)
        jidx2++;
    np2->next = next_np2;
    if (to_pp->is_bus)
      free_libc_name_list_rec(np2_head);
```

libc gen.c

```
}
/* ---- min pulse width */
static
void libc gen min pulse width (
      libc pin rec *current pin)
{ char *cell name;
  libc_cell_rec *curr_cell;
  libc_name_list_rec *from pin;
  libc_name_list_rec *np1, *next_np1, *np1p, *np1 head;
  libc min_pulse_width rec *mpw;
  libc k factor rec *kfc;
  float *k_factor,min_scaled,max scaled;
  float constraint high[3], constraint low[3];
  int iidx, jidx;
  mpw = current_pin->min_pulse_width;
  if (mpw == NULL)
   return;
  /* ---- no bundle pin is allowed here */
  assert(current pin->members == NULL);
  curr_cell = current_pin->_current_cell;
  cell_name = curr cell->cell name;
  from_pin = current_pin->pin_name;
           = (curr_cell->_scaling factors)? curr cell-> scaling factors :
tech_lib->k_factor;
  k_factor = (float *)(kfc->k_min_pulse_width_high);
  libc_gen_scaled_value(k factor, &min scaled, &max scaled);
  constraint_high[0] = mpw->constraint high * min scaled;
  constraint_high[1] = mpw->constraint_high;
  constraint_high[2] = mpw->constraint high * max scaled;
  k_factor = (float *)(kfc->k min pulse width low);
  libc gen_scaled value(k_factor, &min_scaled, &max scaled);
  constraint_low[0] = mpw->constraint_low * min_scaled;
  constraint_low[1] = mpw->constraint low;
  constraint_low[2] = mpw->constraint_low * max_scaled;
  for (np1=from_pin;np1!=NULL;np1=np1->next) {
   next_npl = npl->next;
   np1->next = NULL;
   npl_head = (current_pin->is_bus)? libc_cell_bus_name(current pin) : npl;
            = (current_pin->is_bus)? 0 : -99999;
   for (nplp=npl_head;nplp!=NULL;nplp=nplp->next) {
     /* ---- assign iidx as bundle idx if np->name is belong to bundle-
>members */
     jidx = libc_cell_get_bundle_idx(curr_cell,np1p->name);
     iidx = (jidx != -99999)? jidx : iidx;
     if (mpw->when != NULL) {
```

```
fprintf(Aclf, "defineCondMinClkPulseWidth \"%s\" \"%s\" \"HIGH\"
\"",cell_name,np1p->name);
                  libc opr print(Aclf,mpw->when,curr cell,iidx);
              fprintf(Aclf,"\" '(%G %G
%G)\n",constraint_high[0],constraint_high[1],constraint_high[2]);
                  fprintf(Aclf, "defineCondMinClkPulseWidth \"%s\" \"ks\" \"LOW\"
\"",cell name,np1p->name);
                  libc opr print(Aclf, mpw->when, curr cell, iidx);
              fprintf(Aclf,"\" '(%G %G
%G)\n",constraint_low[0],constraint_low[1],constraint_low[2]);
             else {
                  fprintf(Aclf,"defineMinClkPulseWidth \verb||"%s|" | "%s|" | "HIGH|" '(%G %G %G for the first of th
 %G)\n",cell name,np1p->name,
                       constraint_high[0], constraint_high[1], constraint_high[2]);
                  fprintf(Aclf, "defineMinClkPulseWidth \"%s\" \"%s\" \"LOW\" '(%G %G
 %G)\n",cell name,np1p->name,
                      constraint_low[0],constraint_low[1],constraint_low[2]);
             if (current pin->is bus)
                  iidx++;
         np1->next = next np1;
         if (current pin->is bus)
             free libc name list rec(npl head);
}
/* ---- minimum period */
static
void libc gen min period(
             libc pin rec *current pin)
{ char *cell name;
    libc_cell_rec *curr_cell;
    libc_name_list_rec *from_pin;
    libc name list rec *np1, *next np1, *np1p, *np1 head;
    libc minimum period rec *mp;
    libc k factor rec *kfc;
    float *k_factor,min_scaled,max scaled;
    float constraint[3];
    int iidx, jidx;
    mp = current_pin->minimum_period;
    if (mp == NULL)
        return;
    /* ---- no bundle pin is allowed here */
    assert(current_pin->members == NULL);
    curr_cell = current_pin->_current_cell;
    cell_name = curr_cell->cell name;
    from pin = current pin->pin name;
                          = (curr_cell->_scaling_factors)? curr_cell->_scaling_factors :
tech lib->k_factor;
```

```
k factor = (float *) (kfc->k_min_period);
 libc_gen_scaled_value(k_factor,&min_scaled,&max_scaled);
 constraint[0] = mp->constraint* min scaled;
 constraint[1] = mp->constraint;
 constraint[2] = mp->constraint* max_scaled;
 for (np1=from pin;np1!=NULL;np1=np1->next) {
   next_npl = np1->next;
   np1->next = NULL;
   np1 head = (current pin->is bus)? libc cell bus name(current pin) : np1;
            = (current pin->is bus)? 0 : -99999;
    for (nplp=npl_head;nplp!=NULL;nplp=nplp->next) {
     /* ---- assign iidx as bundle idx if np->name is belong to bundle-
>members */
     jidx = libc_cell_get_bundle_idx(curr_cell,np1p->name);
     iidx = (jidx != -99999)? jidx : iidx;
     if (mp->when != NULL) {
       fprintf(Aclf, "defineCondMinClkPeriod \"%s\" \"%s\"
\"",cell name,nplp->name);
     libc opr print (Aclf, mp->when, curr cell, iidx);
     fprintf(Aclf,"\" '(%G %G
%G) \n", constraint [0], constraint [1], constraint [2]);
     else {
       fprintf(Aclf, "defineMinClkPeriod \"%s\" \"%s\" '(%G %G
%G) \n", cell name, np1p->name,
         constraint[0], constraint[1], constraint[2]);
     if (current_pin->is_bus)
     iidx++;
   npl->next = next npl;
    if (current_pin->is_bus)
     free_libc_name_list_rec(npl_head);
static
void libc_gen_cell_timing(
     libc_cell_rec *cell)
{ libc_pin_rec *pp;
 libc timing rec *tp;
  for(pp=cell->pins;pp!=NULL;pp=pp->next) {
    if (pp->members)
     continue;
    for (tp=pp->timing;tp!=NULL;tp=tp->next) {
     if (tech_lib->delay_model == GENERIC_CMOS) {
       libc_gen_linear_timing(tp);
     else if (tech_lib->delay_model == TABLE_LOOKUP) {
       libc_gen_TLU_timing(tp);
```

libc_gen.c

```
libc_gen_min_pulse_width(pp);
    libc_gen_min_period(pp);
}
/* ================ */
#define l_axucMaxPortTypes
                             19
static char * libc_PortTypeTable[l_axucMaxPortTypes+1] = {
        "Output",
        "Inout",
        "Tristate",
        "Power",
        "Ground",
        "Clock",
        "Tieup",
        "Tiedown",
        "Async_rising",
        "Async_falling",
       "DataIn",
       "DataOut"
       "AddressIn",
       "Enable",
       "ScanIn",
       "ScanOut"
       "ScanClock",
       "TristateDisable",
/* return 1 : is_filpflop, 0 : is not flip_flop */
 static
 int libc_gen_pin_port_type(
       libc_pin_rec *pin,
       libc_name_list_rec *np_head)
 { libc_name_list_rec *np;
   int i;
   /* ---- for pin only, no bus and bundle */
   for (np=np_head;np!=NULL;np=np->next) {
     fprintf(Aclf," ( \"%s\"",np->name);
     for (i=0;i<l_axucMaxPortTypes;i++) {</pre>
     if (pin->pin_type & (1<<i))</pre>
         fprintf(Aclf," \"%s\"",libc_PortTypeTable[i]);
     fprintf(Aclf," )\n");
   if (pin->pin_type & l_axubPortTypeClock)
     return(1);
   return(0);
```

libc gen.c

```
}
/* ---- must after LUT (timing) information */
void libc_gen_pin_info(
      libc cell rec *cell)
{ libc pin rec *pp;
  libc_k_factor_rec *kfc;
  float min_scaled, max_scaled, *k_factor;
  float min_cap,cap,max_cap;
  libc name list rec *np, *np head;
  char *cn; /* cell name */
  int isFlop = 0,isLatch = 0,isRam = 0,isRom = 0;
  cn = cell->cell_name;
  if (cell->ff_latch != NULL) {
    if (cell->ff_latch->is_ff)
      isFlop = 1;
    else if (!(cell->ff latch->is_state))
      isLatch = 1;
  if (cell->memory != NULL) {
    if (cell->memory->is_ram)
      isRam = 1;
   else
      isRom = 1;
  /* ---- 1. dbSetCellPortTypes */
 fprintf(Aclf, "dbSetCellPortTypes (clfGetCLFLibName) \"%s\" '(\n",cn);
  for (pp=cell->pins;pp!=NULL;pp=pp->next) {
    if (pp->members)
      continue;
   np_head = (pp->is_bus)? libc_cell bus name(pp) : pp->pin name;
   isFlop |= libc_gen_pin_port_type(pp,np_head);
    if (pp->is bus)
      free_libc_name_list_rec(np_head);
  fprintf(Aclf,") #f\n");
  /* ---- 2. definePortCapacitance */
 kfc = (cell->_scaling_factors)? cell->_scaling_factors : tech lib-
>k_factor;
 k_factor = (float *)(kfc->k pin_cap);
 libc_gen_scaled_value(k_factor, &min_scaled, &max_scaled);
  for (pp=cell->pins;pp!=NULL;pp=pp->next) {
    if (pp->members)
      continue;
           = pp->capacitance * cap_scale;
   min cap = cap * min scaled * cap scale;
   max_cap = cap * max_scaled * cap_scale;
```

```
np head = (pp->is bus)? libc_cell bus name(pp) : pp->pin name;
    for (np=np head;np!=NULL;np=np->next) {
      fprintf(Aclf, "definePortCapacitance \"%s\" \"%s\" \"fall max\"
%G\n",cn,np->name,max_cap);
      fprintf(Aclf, "definePortCapacitance \"%s\" \"%s\" \"rise max\"
%G\n",cn,np->name,max cap);
      fprintf(Aclf, "definePortCapacitance \"%s\" \"%s\" \"fall nom\"
%G\n",cn,np->name,cap);
      fprintf(Aclf, "definePortCapacitance \"%s\" \"%s\" \"rise nom\"
%G\n",cn,np->name,cap);
      fprintf(Aclf, "definePortCapacitance \"%s\" \"%s\" \"fall min\"
%G\n",cn,np->name,min cap);
      fprintf(Aclf, "definePortCapacitance \"%s\" \"%s\" \"rise min\"
%G\n",cn,np->name,min_cap);
    if (pp->is bus)
      free_libc_name_list_rec(np_head);
  /* ---- 3. dbSetLModelSubType */
  if(isFlop | isLatch | isRam | isRom) {
    char *lm type;
    if (isFlop)
     lm type = "flipflop";
    else if (isLatch)
     lm type = "latch";
    else if (isRam)
     lm type = "ram";
    else if (isRom)
      lm type = "rom";
    fprintf(Aclf, "setq tmpCellId (dbOpenCell \"%s.%s\" \"w\")\n",cn,"TIM");
    fprintf(Aclf, "dbSetLModelSubType tmpCellId \"%s\"\n", lm type);
    fprintf(Aclf,"; dbCloseCell unnecessary; CLF load has already opened cell
to load capacitance & will close \n");
static
void libc_gen_wire_load_model(
      libc lib rec *tlib)
{ libc_wire_load_rec *wlp;
  libc_fanout_length_rec *pp;
  libc wire load selection rec *wlsp;
  libc wire load from area rec *ap;
 char *mode;
#if 1
 return;
                 /* skip in this version (08/24/98) */
#else
  /* ---- defineWireLoad */
```

```
for (wlp=tlib->wire load;wlp!=NULL;wlp=wlp->next) {
    fprintf(Sclf, "defineWireLoad \"%s\" '(%G", wlp->wl name, wlp->slope);
    for (pp=wlp->fanout_length;pp!=NULL;pp=pp->next)
                       (%d %G %G %G %G)",pp->fanout,pp->length,
      fprintf(Sclf,"\n
      pp->capacitance * cap scale,pp->resistance * resist scale,pp->area);
    fprintf(Sclf,")\n");
  /* ---- defineWireLoadSel */
  for (wlsp=tlib->wire load selection;wlsp!=NULL;wlsp=wlsp->next) {
    if (wlsp->wls_name != NULL)
      fprintf(Sclf, "defineWireLoadSel \"%s\" '(", wlsp->wls name);
      fprintf(Sclf, "defineWireLoadSel \"default wire load sel\" '(");
    for (ap=wlsp->area_table;ap!=NULL;ap=ap->next)
                         (%G %G \"%s\")",ap->min area,ap->max area,ap-
      fprintf(Sclf,"\n
>_load_model->wl_name);
    fprintf(Sclf,")\n");
  /* ---- defineDefaultWireLoad */
  if (tlib->default_wire_load != NULL)
    fprintf(Sclf, "defineDefaultWireLoad \"%s\"\n", tlib->default_wire_load);
  if (tlib->default wire load selection != NULL)
    fprintf(Sclf, "defineDefaultWireLoadSel \"%s\"\n", tlib-
>default_wire_load_selection);
 else {
   if (tlib->wire load selection != NULL) {
      if (tlib->wire load selection->wls name == NULL)
        fprintf(Sclf, "defineDefaultWireLoadSel \"default_wire_load_sel\"\n");
     else
       fprintf(Sclf, "defineDefaultWireLoadSel \"%s\"\n", tlib-
>wire load selection->wls name);
  }
  /* ---- defineDefaultWireLoadMode */
  switch (tlib->default_wire_load_mode) {
                                               break;
   case TOP WL :
                     mode = "top";
   case SEGMENTED WL : mode = "segmented";
                                                     break;
   case ENCLOSED WL :
                            mode = "enclosed";
 fprintf(Sclf, "defineDefaultWireLoadMode \"%s\"\n\n", mode);
#endif
  */
static
void libc_gen_cell_lclf(
      libc_cell_rec *cp)
{ char *cn;
 libc pin rec *pp, *bundle p;
 libc name list rec *np, *np head;
  int is_ff=0, is_latch=0;
 libc ff latch rec *ffp;
```

```
int i,iidx = -99999,jidx;
  cn = cp->cell name;
  if (libc version >= 330) {
    if (cp->dont touch | cp->pad cell)
                                                                          /* --
      fprintf(Sclf, "defineCellDontTouch \"%s\"\n", cn);
-- dont touch */
    if (cp->dont use | | cp->pad cell)
      fprintf(Sclf, "defineCellDontUse \"%s\"\n", cn);
                                                                    /* ----
dont use */
    if (cp->cell footprint != NULL)
      fprintf(Sclf, "defineCellFootPrint \"%s\" \"%s\"\n",cn,cp-
>cell_footprint);
  /* ---- cell function */
  if (libc_version >= 330) {
    for (ffp=cp->ff_latch;ffp!=NULL;ffp=ffp->next) {
      if (ffp->is_ff)
        is ff = 1;
      else if (!ffp->is state)
        is_latch = 1;
      else {
                        /* ffp->is state */
        if (ffp->clock on != NULL || ffp->next state != NULL)
          is ff = 1;
        else
          is_latch = 1;
      /* ---- 2. ff or latch function */
      for(i=0, iidx=-1; i<ffp->width; i++) {
        if (ffp->width == 1) {
          iidx = -99999;
          fprintf(Sclf, "%s \"%s\" \"%s\" \"",
            (is_ff)?"defineFlipFlopFunction":"defineLatchFunction",cn,ffp-
>Q_name,ffp->QN_name);
        else {
          iidx += 1;
          fprintf(Sclf,"%s \"%s\" \"%s[%d]\" \"%s[%d]\" \"",
            (is_ff)?"defineFlipFlopFunction":"defineLatchFunction",cn,ffp-
>Q_name,iidx,ffp->QN_name,iidx);
        if (is_ff) {
          libc_opr_print(Sclf,ffp->clock_on,cp,iidx);
          fprintf(Sclf,"\" \"");
          libc_opr_print(Sclf,ffp->next_state,cp,iidx);
        }
        else {
          libc opr print (Sclf, ffp->enable, cp, iidx);
          fprintf(Sclf,"\" \"");
          libc opr print(Sclf, ffp->data in, cp, iidx);
        fprintf(Sclf,"\" \"");
        libc opr print(Sclf,ffp->clear,cp,iidx);
        fprintf(Sclf,"\" \"");
        libc opr print(Sclf,ffp->preset,cp,iidx);
```

```
fprintf(Sclf,"\" \"%c\" \"%c\" \"",ffp->clear_preset_varl,ffp-
 >clear_preset_var2);
         libc_opr_print(Sclf,ffp->on_also,cp,iidx);
         fprintf(Sclf,"\"\n");
   }
   if (Sclf1 != NULL)
                                    /* lib_version == 320 */
    fprintf(Sclf1, "setq cellId (dbOpenCell \"%s.TIM\" \"w\") \n", cn);
  for (pp=cp->pins;pp!=NULL;pp=pp->next) {
    if (pp->members)
      continue;
    np_head = (pp->is_bus)? libc_cell_bus_name(pp) : pp->pin_name;
          = (pp->is_bus)? 0 : -99999;
    for (np=np_head;np!=NULL;np=np->next) {
      if (libc version >= 330) {
        if (pp->fanout_load != 0)
                                                                  /* ---- for
max fanout */
        fprintf(Sclf, "defineCellFanoutLoad \"%s\" \"%s\" %G\n",cn,np-
>name,pp->fanout_load);
        if (pp->direction == OUTPUT_E || pp->direction == INOUT_E) {
          if (pp->max_fanout != 0)
max fanout */
          fprintf(Sclf, "defineCellMaxFanout \"%s\" \"%s\" %G\n",cn,np-
>name,pp->max_fanout);
          if (pp->max_transition != 0)
max_transition */
          fprintf(Sclf,"defineCellMaxTransition \verb|\"\%s\" \"\%s\\" \%G\n",cn,np-
>name,pp->max transition);
          if (pp->max_capacitance != 0)
                                                                  /* ----
max capacitance */
          >name,pp->max capacitance);
      else if (Sclf1 != NULL) {
                                        /* for version 320 */
        /* if (pp->fanout load != 0)
         * fprintf(Sclf1, "dbSetFanout \"%s\" %G\n", np->name, pp-
>fanout_load); */
       if (pp->direction == OUTPUT_E || pp->direction == INOUT_E) {
         if (pp->max_fanout != 0)
/* ---- max fanout */
         fprintf(Sclf1, "dbSetMaxFanout cellId \"%s\" %G\n", np->name, pp-
>max_fanout);
         if (pp->max transition != 0)
                                                                       /* --
-- max_transition */
         fprintf(Sclf1, "dbSetMaxTransition cellId \verb|\"%s\" %G\n", np->name, pp-
>max_transition);
         if (pp->max_capacitance != 0)
-- max_capacitance */
         fprintf(Sclf1, "dbSetMaxCapacitance cellId \"%s\" %G\n",np->name,pp-
>max_capacitance);
```

```
/* ---- pin function */
     if (pp->function == NULL)
       continue;
      /* ---- assign iidx as bundle idx if np->name is belong to bundle-
>members */
     jidx = libc cell get bundle idx(cp,np->name);
     iidx = (jidx != -99999)? jidx : iidx;
     if (pp->three state != NULL) {
     /* ---- 1. three state */
     if (libc version >= 330) {
       fprintf(Sclf, "defineTristateFunction \"%s\" \"%s\" \"", cn, np->name);
         libc_opr_print(Sclf,pp->three_state,cp,iidx);
       fprintf(Sclf,"\" \"");
         libc opr print(Sclf,pp->function,cp,iidx);
       fprintf(Sclf,"\"\n");
     else if (libc_version < 330 && cp->ff_latch != NULL)
     /* do nothing */;
     else {
     /* ---- 2. ff or latch function */
       /* ---- 3. combinational function */
       fprintf(Sclf, "defineBooleanFunction \"%s\" \"%s\" \"",cn,np->name);
       libc_opr_print(Sclf,pp->function,cp,iidx);
       fprintf(Sclf,"\"\n");
     if (pp->is bus)
       iidx++;
   if (pp->is bus)
     free libc name list rec(np head);
 fprintf(Sclf, "\n");
  if (Sclf1 != NULL)
                                  /* lib version == 320 */
   fprintf(Sclf1, "dbCloseCell cellId\n");
/* ---- power clf */
static char *power table name[3] =
{ "\"SwitchingPowerTable\"", "\"InternalPowerTable\"",
"\"LeakagePowerTable\"" };
/* ----- */
 static
 void libc_gen_power_table_name(
         char *cell_name,
         char *pin_name,
         int tidx,
       char rise fall)
                        /* r,f */
   if (rise_fall == 'r' | rise_fall == 'f')
     fprintf(Mclf,"\"%s:%s::%d%c\"",cell_name,pin_name,tidx,rise fall);
```

```
else
       fprintf(Mclf, "\"%s:%s::%d\"", cell_name, pin name, tidx);
 /* ----- */
   static
   void libc_gen_mclf_create_one_table(
        char *cell name,
        char *pin_name,
        int tidx,
        int tbl idx,
                                   /* 0: output, 1: input */
        char rise_fall,
                            /* r,f,t */
        libc_table_val_rec *table)
   { char *idx_type1,*idx_type2,*idx_type3;
    variable_E v1,v2,v3;
    int size1, size2, size3;
    float k_factor = 1.0;
    fprintf(Mclf,"(clfCreateTable ");
    libc_gen_power_table_name(cell_name,pin_name,tidx,rise_fall);
    fprintf(Mclf," %s\n '(",power_table_name[tbl_idx]);
    if (table == NULL) {
                                         /* <--- no value(table) */
      fprintf(Mclf,") '(0)\n)\n";
    else if (table->_tbl == NULL) {
                                        /* ---- scalar table */
      fprintf(Mclf,") '(%G)\n)\n",table->scalar_val * watt_scale);
    else {
      v1
               = table->_tbl->variable 1;
      v2
              = table->_tbl->variable_2;
              = table-> tbl->variable 3;
     idx_type1 = libc_gen_varE2str(v1);
     idx_type2 = libc_gen_varE2str(v2);
     idx_type3 = libc gen varE2str(v3);
      size1 = sizeof_float_buffer(table->index1);
     fprintf(Mclf, "(%s ", idx_type1);
     libc_gen_1D_array(Mclf,table->index1,libc_gen_varE2scale(v1));
      if (idx_type2 != NULL) {
        size2 = sizeof_float_buffer(table->index2);
        fprintf(Mclf,")\n (%s ",idx_type2);
       libc_gen_1D_array(Mclf,table->index2,libc_gen_varE2scale(v2));
        if (idx_type3 != NULL) {
         size3 = sizeof_float_buffer(table->index3);
         fprintf(Mclf,")\n (%s ",idx type3);
         libc_gen_1D_array(Mclf,table->index3,libc_gen_varE2scale(v3));
         fprintf(Mclf,"))");
       libc_gen_timing_values(Mclf,size1,size2,size3,table->values,k_factor
* watt_scale);
       }
       else {
         fprintf(Mclf,"))");
       libc_gen_timing_values(Mclf,size1,size2,0,table->values,k_factor *
watt_scale);
```

```
else {
        fprintf(Mclf,"))");
      libc gen timing values (Mclf, size1, 0, 0, table->values, k factor *
watt_scale);
      fprintf(Mclf,")\n");
/* ----- */
  static
 void libc_gen_define_power(
        libc cell rec *cell,
        char *pn,
                              /* pin_name */
        int is in pin,
        libc_internal_power *ipp,
        int tidx,
        int iidx,
       char rise_fall) /* r,f,t */
  { char *cn;
    libc name list rec *np;
    libc_pin_rec *pp;
    cn = cell->cell_name;
    fprintf(Mclf,"%s \"%s\" \"",(is_in_pin)? "defineInternalPower" :
"defineSwitchingPower",cn);
    if (is in pin) {
     /* ---- input */
     fprintf(Mclf, "%s%c", pn, rise fall);
     if (ipp->when) {
        /* ---- CPt*(S0+R0) */
        fprintf(Mclf, "*(");
        libc opr power when print (Mclf, ipp->when, cell, iidx);
       fprintf(Mclf,")");
     fprintf(Mclf,"\" \"");
     /* ---- output */
     if (ipp->related pin) {
       for (np=ipp->related pin;np;np=np->next) {
       libc_opr_id_print(Mclf,np->name,cell,iidx);
        fprintf(Mclf, "t%s", (np->next!=NULL)?"+":"");
     else
       fprintf(Mclf,"-");
     /* ---- input */
     if (ipp->related_pin == NULL && ipp->when == NULL)
        fprintf(Mclf,"-");
     else {
     if (ipp->related_pin) {
         fprintf(Mclf, "(");
         for (np=ipp->related_pin;np;np=np->next) {
```

```
libc opr id print(Mclf,np->name,cell,iidx);
        fprintf(Mclf, "t%s", (np->next!=NULL)?"+":"");
      fprintf(Mclf,")%s",(ipp->when)?"*":"");
    if (ipp->when) {
      fprintf(Mclf,"(");
      libc opr power when print(Mclf, ipp->when, cell, iidx);
      fprintf(Mclf,")");
  fprintf(Mclf,"\" \"");
  /* ---- output */
  fprintf(Mclf, "%s%c",pn,rise_fall);
  if (ipp->equal_or_opposite_output) {
  for (np=ipp->equal_or_opposite output;np;np=np->next) {
      libc_opr_id_print(Mclf,np->name,cell,iidx);
    fprintf(Mclf, "%c%s", rise_fall, (np->next!=NULL)?"*":"");
fprintf(Mclf,"\" \"TLU\" ");
libc_gen_power_table_name(cn,pn,tidx,rise fall);
fprintf(Mclf, "\n");
static
void libc gen switching name (
      libc cell rec *cell,
      libc name list rec *np head)
{ libc pin rec *pin;
  libc_name_list rec *np1, *np2, *np2 head;
  for (npl=np head;np1;np1=np1->next) {
    pin = libc_cell_find_pin_by_name(cell,np1->name,-99999);
  if (pin == NULL)
    fprintf(Mclf, "%st%s", npl->name, (npl->next)?"+":"");
  else {
    if (pin->members) {
      for (np2=pin->members;np2;np2=np2->next)
        fprintf(Mclf,"%st%s",np2->name,(np2->next||np1->next)?"+":"");
    else if (pin->is_bus) {
      np2_head = libc_cell_bus_name(pin);
      for (np2=np2 head;np2;np2=np2->next)
        fprintf(Mclf, "%st%s", np2->name, (np2->next||np1->next)?"+":"");
      free_libc_name_list_rec(np2_head);
    }
    else
      fprintf(Mclf, "%st%s", npl->name, (npl->next)?"+":"");
  }
}
```

```
static
  void libc gen define power 97(
        libc cell rec *cell,
        int is in pin,
        libc_internal_power *ipp,
        int tidx)
  { char *cn;
    libc_name_list_rec *np;
    cn = cell->cell name;
    fprintf(Mclf,"%s \"%s\" \"", (is_in_pin)? "defineInternalPower" :
"defineSwitchingPower",cn);
    if (is_in_pin) {
      assert(ipp->outputs == NULL);
      libc_gen_switching_name(cell,ipp->inputs);
      fprintf(Mclf,"\" \"-");
    else {
      if (ipp->outputs == NULL)
        fprintf(Mclf,"-\" \"-");
      else {
      if (ipp->inputs != NULL)
        libc_gen_switching name(cell,ipp->inputs);
        fprintf(Mclf,"-");
        fprintf(Mclf,"\" \"");
        libc gen switching name(cell, ipp->outputs);
    fprintf(Mclf,"\" \"TLU\" ");
    libc gen power table name(cn, "X", tidx, 't');
    fprintf(Mclf,"\n");
             ----- */
static
void libc gen cell mclf(
      libc_cell_rec *cp)
{ char *cn;
  libc_pin_rec *pp,*bundle_p;
  libc_name list rec *np, *np head;
  int iidx = -99999, jidx, tidx=0;
 libc_internal_power *ipp;
 libc_leakage_power *lpp;
 int is in pin;
  cn = cp->cell name;
  /* ---- 1. cell leakage power */
 for (lpp=cp->leakage_power;lpp;lpp=lpp->next) {
    fprintf(Mclf, "defineLeakagePower \"%s\" \"");
    libc_opr_power_when_print(Mclf,lpp->when,cp,-99999);
   fprintf(Mclf,"\" \"-\" \"Constant\" %G\n",cn,
```

```
lpp->value*joule_scale);
  if (cp->cell leakage_power != 0.0)
    fprintf(Mclf, "defineLeakagePower \"%s\" \"-\" \"Constant\"
%G\n",cn,
      cp->cell_leakage_power*joule scale);
  /* ---- 2. old method internal power (97) */
  for (ipp=cp->internal_power_c;ipp!=NULL;ipp=ipp->next) {
    if (ipp->inputs == NULL && ipp->outputs == NULL) {
      libc_gen_mclf_create_one_table(cn,"X",tidx,0,'t',ipp->power);
      libc_gen_define_power_97(cp,0,ipp,tidx++);
    else {
      if (ipp->outputs != NULL) {
        libc_gen_mclf_create_one_table(cn,"X",tidx,0,'t',ipp->power);
      libc gen define_power_97(cp,0,ipp,tidx++);
      else if (ipp->inputs != NULL) {
        libc_gen_mclf_create_one_table(cn,"X",tidx,1,'t',ipp->power);
      libc_gen_define_power_97(cp,1,ipp,tidx++);
    }
  }
  /* ---- 3. pin (internal/switching) power */
  for (pp=cp->pins;pp!=NULL;pp=pp->next) {
    if (pp->members)
      continue;
    is_in_pin = (pp->direction == INPUT_E || pp->direction == INTERNAL_E)? 1
: 0;
    for (ipp=pp->internal_power;ipp!=NULL;ipp=ipp->next,tidx++) {
      /* ---- 3.1 create pin power table */
      if (ipp->rise_power != NULL)
        libc_gen_mclf_create_one_table(cn,pp->pin_name-
>name,tidx,is_in_pin,'r',ipp->rise_power);
      if (ipp->fall power != NULL)
        libc_gen_mclf_create_one_table(cn,pp->pin name-
>name,tidx,is_in_pin,'f',ipp->fall power);
      if (ipp->power != NULL)
       libc_gen_mclf_create_one_table(cn,pp->pin_name-
>name,tidx,is_in_pin,'t',ipp->power);
     np_head = (pp->is_bus)? libc_cell_bus_name(pp) : pp->pin name;
             = (pp->is_bus)? 0 : -99999;
     for (np=np_head;np!=NULL;np=np->next) {
       jidx = libc_cell_get_bundle_idx(cp,np->name);
       iidx = (jidx != -99999)? jidx : iidx;
       /* ---- 3.2 generate define power */
     if (ipp->rise_power != NULL)
         libc_gen_define_power(cp,np->name,is_in_pin,ipp,tidx,iidx,'r');
       if (ipp->fall power != NULL)
         libc_gen_define_power(cp,np->name,is_in_pin,ipp,tidx,iidx,'f');
       if (ipp->power != NULL)
         libc_gen_define_power(cp,np->name,is_in_pin,ipp,tidx,iidx,'t');
```

```
if (pp->is_bus)
         iidx++;
      if (pp->is_bus)
        free libc name list_rec(np_head);
  }
public
void libc gen_clf main(
      FILE *xxx out p,
      FILE *yyy_out_p,
      FILE *yyy_out_con,
      FILE *zzz_out_p,
      libc_lib_rec *tlib)
{ libc_cell_rec *cp;
  if (tlib->delay_model != GENERIC_CMOS && tlib->delay_model != TABLE_LOOKUP)
   return;
 Aclf = xxx_out_p;
  Sclf = yyy_out_p;
  Sclf1 = yyy_out_con; /* libc_version == 320 */
 Mclf = zzz_out_p;
  fprintf(Aclf,"; CLF translated from synopsys library format file\n");
  fprintf(Aclf,"; ==== Library : %s ====\n",tlib->lib_name);
  fprintf(Aclf,"; Temperature
(%G:%G:%G) \n", libc_t min, libc_t_nom, libc_t_max);
  fprintf(Aclf,"; Voltage
(%G:%G:%G)\n",libc_v_min,libc_v_nom,libc_v_max);
  fprintf(Aclf,"; Process Factor
(%G:%G:%G) \n", libc p min, libc p nom, libc p max);
  fprintf(Aclf,"; Capacitance multiplier %G \n",cap_scale);
  fprintf(Aclf,"; Resistance multiplier %G \n",resist scale);
  fprintf(Aclf,"; Time
                             multiplier %G \n",time_scale);
  libc gen_k factor(tlib->k factor);
  if (Sclf != NULL) {
    if (Sclf != Aclf)
      fprintf(Sclf,"; ==== Library : %s ====\n",tlib->lib_name);
    libc_gen_wire_load_model(tlib);
  if (Sclf1 != NULL) {
                                   /* libc version == 320 */
   fprintf(Sclf1, "geOpenLib\n");
    fprintf(Sclf1, "setFormField \"Open Library\" \"Library Name\"
\"%s\"\n",tlib->lib name);
    fprintf(Sclf1, "formOK \"Open Library\"\n\n");
  for (cp=tlib->cells;cp!=NULL;cp=cp->next) {
```

libc_lex.1

```
웅 {
 #include "libc def.h"
#include "libc yacc.h"
                            /* from libc.y */
#include <stdlib.h>
#define YY_SKIP YYWRAP
#undef yywrap
#define yywrap()
                       1
#define echo
                 /* ECHO */
#define RN(s)
                 return(s)
void lex_error(char *);
static int libc lineno = 1;
extern char *file name;
static int pss[100];
                             /* prev_state_stack */
static int psc = 0;
                            /* prev_state count */
int scaling_attr; /* 0:k_process, 1:k_temp, 2:k_volt */
#define NEXT_STATE
                       pss[psc++]=YY START; BEGIN
#define PREV_STATE
                       BEGIN pss[--psc]
FILE *yyin;
용}
           PS LIB CU DM LU PU RU VU TU IPO
ទs
           BOOL MMP ANY_S WLM WLF TT SA DIR DRT NST
용도
           PFT PSC BUS PIN CBO CM CIP PIP CELL IV LTT
88
           OV OC PC PLT TD SC SF TR TYPE WL
           WLS WLT PP FF CL MR MW TI MPW MP
8$
           TIT TIS VS CTC RT PT CST CRT SIG T CENUM
용도
           OST SDF_E NAME QSTR LUT ROP REV CLP
٧ş
           SKIP
           [ \b\t\v\f\r] +
WS
integer
                 [0-9]+
           ([0-9]+([.][0-9]*)?)([Ee][+-]?[0-9]+)?|([0-9]*[.][0-9]+)([Ee][+-
real
]?[0-9]+)?
string
               [a-zA-Z_{$}][0-9a-zA-Z_{$}./\-]*([\[][0-9]+([:][0-9]+)?[\]])?
estring
                 [0-9a-zA-Z_$./?^-+^*]+([[][0-9]+([:][0-9]+)?[]])?
nstring
               "\\\""([0-9]+[a-zA-Z_] [0-9a-zA-Z_]*)|([a-zA-Z_] [0-9a-zA-
Z_]*)"\\\""
qstring
                 ["]([^\n"]|"\\\n"|"\\\"")+["]
웅웅
<SKIP>"\\\n"
                            { echo; libc_lineno++; }
<SKIP>[^\n;] *
                            { echo; RN(SDF_SKIP); }
<SKIP>[;]
                      { echo; PREV_STATE; RN(';'); }
<SKIP>[\n]
                      { echo; PREV_STATE; libc_lineno++; }
```

```
\{ws\}
                          { echo; }
 [\n] | "\\\n"
                         { echo; libc lineno++; }
11 / * 11
                         { echo; NEXT STATE PS; }
<PS>"*"+"/"
                                  { echo; PREV_STATE; }
<PS>[^*\n] *
                         { echo; }
<PS>"*"+[^*/\n]*
                         { echo; }
["]
                         { echo; RN(libctext[0]); }
{qstring}
                         { char *ch;
                           if (yy_start != (1 + 2 * QSTR)) {
                             REJECT;
                           for (ch=libctext; (*ch)!='\0';ch++) {
                             if ((*ch) == '\n')
                               libc_lineno++;
                           libclval.string = copy_string(libctext);
                           echo; PREV_STATE; RN(L_QSTRING);
<INITIAL>"library"
                                      { echo; NEXT STATE LIB; NEXT STATE NAME;
RN(K_LIBRARY); }
<LIB>[;]
                               { echo; RN(libctext[0]); }
<LIB>"aux_no_pulldown_pin_property" { echo; RN(K_ANPPP); }
<LIB>"bus_naming_style"
                                       echo; NEXT_STATE QSTR; RN(K_BNS); }
<LIB>"comment"
                                       echo; NEXT_STATE QSTR; RN(K_COMMENT); }
<LIB>"current_unit"
                                      { echo; NEXT_STATE CU; RN(K_CU); }
<LIB>"date"
                               { echo; NEXT_STATE QSTR; RN(K_DATE); }
<LIB>"delay model"
                                      { echo; NEXT_STATE DM; RN(K_DM); }
<LIB>"in place swap mode"
                                       echo; NEXT_STATE IPO; RN(K_IPSM); }
<LIB>"leakage power unit"
                                      { echo; NEXT_STATE PU; RN(K_LPU); }
<LIB>"max wired emitters"
                                      \{ echo; RN(K_MWE); \}
<LIB>"multiple_drivers legal"
                                      { echo; NEXT_STATE BOOL; RN(K_MDL); }
<LIB>"nom_process"
                                       echo; RN(K_NP); }
<LIB>"nom temperature"
                                       echo; RN(K NT);
<LIB>"nom voltage"
                                      \{ echo; RN(K_NV); \}
<LIB>"nonpaired_twin_inc_delay_func"
                                           { echo; NEXT_STATE MMP;
RN(K_NTIDF); }
<LIB>"no_pulldown_pin_property"
                                           { echo; RN(K NPPP); }
<LIB>"piece_type"
                               { echo; NEXT_STATE PT; RN(K_PT); }
<LIB>"power unit"
                               { echo; NEXT_STATE PU; RN(K_PU); }
<LIB>"preferred_output_pad_slew_rate_control" { echo; RN(K_POPSRC); }
<LIB>"preferred_output_pad_voltage"
                                           { echo; RN(K_POPV); }
<LIB>"preferred_input_pad_voltage"
                                           { echo; RN(K_PIPV); }
<LIB>"pulling resistance unit"
                                           { echo; NEXT_STATE RU; RN(K PRU); }
<LIB>"reference_capacitance"
                                     { echo; RN(K RC); }
                                                                    /* cmos2 */
<LIB>"revision"
                                     { echo; NEXT_STATE REV; RN(K_REVISION); }
<LIB>"simulation"
                               { echo; NEXT_STATE BOOL; RN(K_SIMULATION); }
                               { echo; NEXT_STATE TU; RN(K_TU); }
<LIB>"time unit"
<LIB>"unconnected pin property"
                                           { echo; RN(K_UPP); }
<LIB>"voltage_unit"
                                     { echo; NEXT_STATE VU; RN(K VU); }
<LIB>"wired_logic_function"
                                     { echo; NEXT_STATE WLF; RN(K_WLF); }
```

```
<LIB>"default_cell_leakage_power"
                                       echo; RN(K DCLP); }
<LIB>"default cell power"
                                       echo; RN(K DCP); }
<LIB>"default connection class"
                                             echo; RN(K DCC);
<LIB>"default_edge_rate_breakpoint f0"
                                             echo; RN(K DF0);
                                                                    /* cmos2 */
<LIB>"default edge rate breakpoint f1"
                                             echo; RN(K DF1);
                                                                    /* cmos2 */
<LIB>"default edge rate breakpoint r0"
                                             echo; RN(K DR0);
                                                                    /* cmos2 */
<LIB>"default_edge_rate_breakpoint r1"
                                             echo; RN(K DR1);
                                                                    /* cmos2 */
<LIB>"default_emitter_count"
                                       echo; RN(K_DEC); }
<LIB>"default_fall_delay_intercept"
                                       echo; RN(K_DFDI); }
<LIB>"default_fall_nonpaired_twin"
                                       echo; RN(K DFNT); }
<LIB>"default_fall_pin_resistance"
                                       echo; RN(K DFPR); }
<LIB>"default fall wire resistance"
                                       echo; RN(K_DFWR); }
<LIB>"default_fall_wor_emmiter"
                                           { echo; RN(K DFWE); }
<LIB>"default_fall_wor intercept"
                                       echo; RN(K DFWI); }
<LIB>"default_fanout_load"
                                       echo; RN(K_DFL); }
<LIB>"default_inout_pin_cap"
                                       echo; RN(K DIOPC);
<LIB>"default_inout_pin fall res"
                                       echo; RN(K DIOFR); }
<LIB>"default inout pin rise res"
                                       echo; RN(K DIORR); }
<LIB>"default_input_pin_cap"
                                       echo; RN(K_DIPC); }
<LIB>"default_intrinsic_fall"
                                       echo; RN(K_DIF); }
<LIB>"default_intrinsic_rise"
                                       echo; RN(K_DIR); }
<LIB>"default_max_capacitance"
                                           { echo; RN(K_DMC); }
<LIB>"default_max_fanout"
                                       echo; RN(K_DMFO); }
<LIB>"default_max_transition"
                                       echo; RN(K DMT); }
<LIB>"default max utilization"
                                           { echo; RN(K_DMU); }
<LIB>"default min porosity"
                                       echo; RN(K_DMP);
<LIB>"default_operating_conditions"
                                       echo; RN(K_DOC); }
<LIB>"default_output_pin_cap"
                                       echo; RN(K_DOPC);
<LIB>"default_output_pin_fall_res"
                                       echo; RN(K DOFR);
<LIB>"default_output_pin_rise_res"
                                       echo; RN(K DORR); }
<LIB>"default_pin_limit"
                                       echo; RN(K DPL); }
<LIB>"default_pin_power"
                                       echo; RN(K DPP); }
<LIB>"default_rc_fall coefficient"
                                       echo; RN(K DRFC);
                                                                    /* cmos2 */
<LIB>"default_rc_rise coefficient"
                                       echo; RN(K DRRC);
                                                                    /* cmos2 */
<LIB>"default_rise_delay intercept"
                                       echo; RN(K DRDI);
<LIB>"default_rise nonpaired twin"
                                       echo; RN(K_DRNT);
<LIB>"default_rise_pin_resistance"
                                       echo; RN(K DRPR);
<LIB>"default_rise_wire_resistance"
                                       echo; RN(K_DRWR); }
<LIB>"default_rise_wor_emitter"
                                           { echo; RN(K DRWE); }
<LIB>"default rise wor intercept"
                                       echo; RN(K_DRWI); }
<LIB>"default_setup_coefficient"
                                       echo; RN(K DSC);
                                                                    /* cmos2 */
<LIB>"default_slope_fall"
                                       echo; RN(K DSF); }
<LIB>"default slope rise"
                                       echo; RN(K_DSR); }
<LIB>"default_wire_load"
                                       echo; NEXT_STATE NAME; RN(K DWL); }
<LIB>"default_wire_load_area"
                                       echo; RN(K DWLA); }
<LIB>"default_wire_load capacitance"
                                           { echo; RN(K DWLC); }
                                       echo; NEXT STATE WLM; RN(K_DWLM); }
<LIB>"default_wire_load mode"
<LIB>"default_wire load resistance"
                                       echo; RN(K_DWLR); }
<LIB>"default_wire_load_selection"
                                       echo; NEXT_STATE NAME; RN(K_DWLS); }
<LIB>"k process cell rise"
                                       echo; scaling_attr = 0; RN(P CR); }
<LIB>"k_process_cell fall"
                                       echo; scaling_attr = 0; RN(P_CF); }
<LIB>"k_process cell leakage power"
                                       echo; scaling_attr = 0; RN(P_CLP); }
<LIB>"k process cell power"
                                       echo; scaling_attr = 0; RN(P CP); }
<LIB>"k process drive current"
                                             echo; scaling_attr = 0; RN(P DC);
```

```
<LIB>"k process drive_fall"
                                  { echo; scaling attr = 0; RN(P DF); }
     /* old */
<LIB>"k process drive rise"
                                  { echo; scaling attr = 0; RN(P_DR); }
     /* old */
<LIB>"k process fall delay intercept" { echo; scaling_attr = 0;
RN(P FDI); }
<LIB>"k process fall pin_resistance"
                                     { echo; scaling_attr = 0;
RN(P_FPR); }
<LIB>"k_process_fall_propagation"
                                   { echo; scaling_attr = 0; RN(P_FP); }
<LIB>"k process fall transition" { echo; scaling attr = 0; RN(P_FT); }
<LIB>"k_process_fall_wire_resistance" { echo; scaling attr = 0;
RN(P FWR); }
<LIB>"k_process_fall_wor_emitter"
                                    { echo; scaling attr = 0; RN(P FWE); }
<LIB>"k_process_fall_wor_intercept" { echo; scaling_attr = 0; RN(P_FWI); }
<LIB>"k process hold_fall"
                                    { echo; scaling_attr = 0; RN(P_HF); }
<LIB>"k_process_hold_rise"
                                    { echo; scaling attr = 0; RN(P_HR); }
<LIB>"k process internal power"
                                         { echo; scaling attr = 0; RN(P IP);
                                      { echo; scaling attr = 0; RN(P IF);
<LIB>"k_process_intrinsic_fall"
<LIB>"k process intrinsic rise"
                                  { echo; scaling attr = 0; RN(P IR);
<LIB>"k_process_min_period"
                                    { echo; scaling_attr = 0; RN(P_MP); }
<LIB>"k process min pulse width high"
                                      { echo; scaling_attr = 0;
RN(P MPWH); }
<LIB>"k_process_min_pulse_width_low"
                                          { echo; scaling_attr = 0;
RN(P_MPWL); }
<LIB>"k_process_nochange_fall"
                                          { echo; scaling attr = 0; RN(P NF);
                                          { echo; scaling attr = 0; RN(P NR);
<LIB>"k process nochange rise"
<LIB>"k_process_pin_cap"
                                    { echo; scaling attr = 0; RN(P PC); }
                                    { echo; scaling attr = 0; RN(P PP); }
<LIB>"k process pin power"
<LIB>"k process recovery fall"
                                          { echo; scaling attr = 0; RN(P RF);
<LIB>"k process recovery rise"
                                          { echo; scaling attr = 0; RN(P RR);
<LIB>"k_process_removal_rise"
<LIB>"k_process_removal_rise"
<LIB>"k process removal fall"
                                    { echo; scaling attr = 0; RN(P REF); }
                                    { echo; scaling attr = 0; RN(P RER); }
<LIB>"k_process_rise_delay_intercept"
                                      { echo; scaling_attr = 0;
RN(P_RDI); }
<LIB>"k_process_rise_pin_resistance"
                                        { echo; scaling_attr = 0;
RN(P_RPR); }
<LIB>"k_process_rise_propagation"
                                    { echo; scaling_attr = 0; RN(P_RP); }
<LIB>"k_process_rise_transition"
                                    { echo; scaling_attr = 0; RN(P_RT); }
<LIB>"k_process_rise_wire_resistance"
                                      { echo; scaling_attr = 0;
RN(P RWR); }
<LIB>"k_process_rise_wor_emitter"
                                    { echo; scaling_attr = 0; RN(P_RWE); }
<LIB>"k process rise wor intercept" { echo; scaling attr = 0; RN(P RWI); }
                                     echo; scaling attr = 0; RN(P SF); }
<LIB>"k process setup fall"
                                    { echo; scaling_attr = 0; RN(P_SR); }
<LIB>"k_process_setup_rise"
                                    { echo; scaling_attr = 0; RN(P_SKF); }
<LIB>"k process skew fall"
<LIB>"k process skew rise"
                                    { echo; scaling attr = 0; RN(P SKR); }
<LIB>"k process slope fall"
                                    { echo; scaling attr = 0; RN(P SLF); }
     /* old */
<LIB>"k_process_slope_rise"
                                   { echo; scaling_attr = 0; RN(P_SLR); }
      /* old */
```

```
<LIB>"k process wire cap"
                                     { echo; scaling_attr = 0; RN(P WC); }
<LIB>"k process wire res"
                                      echo; scaling attr = 0; RN(P WR); }
<LIB>"k temp cell rise"
                                       echo; scaling_attr = 1; RN(P CR); }
<LIB>"k temp cell fall"
                                       echo; scaling attr = 1; RN(P CF); }
<LIB>"k temp cell leakage power"
                                       echo; scaling_attr = 1; RN(P_CLP); }
<LIB>"k_temp_cell_power"
                                       echo; scaling_attr = 1; RN(P_CP); }
<LIB>"k_temp_drive_current"
                                       echo; scaling_attr = 1; RN(P_DC);
<LIB>"k_temp_drive_fall"
                                      echo; scaling_attr = 1; RN(P_DF); }
      /* old */
<LIB>"k temp drive rise"
                                     { echo; scaling_attr = 1; RN(P_DR); }
      /* old */
<LIB>"k_temp_fall_delay_intercept"
                                     { echo; scaling attr = 1; RN(P FDI); }
<LIB>"k temp fall pin resistance"
                                     { echo; scaling_attr = 1; RN(P_FPR);
<LIB>"k_temp_fall_propagation"
                                           { echo; scaling attr = 1; RN(P FP);
<LIB>"k temp fall transition"
                                      echo; scaling attr = 1; RN(P FT); }
<LIB>"k temp fall wire resistance"
                                      echo; scaling attr = 1; RN(P FWR); }
<LIB>"k_temp_fall_wor_emitter"
                                           { echo; scaling_attr = 1;
RN(P_FWE); }
                                       echo; scaling_attr = 1; RN(P_FWI); }
<LIB>"k_temp_fall_wor_intercept"
<LIB>"k_temp_hold_fall"
                                       echo; scaling_attr = 1; RN(P HF); }
<LIB>"k_temp_hold_rise"
                                       echo; scaling_attr = 1; RN(P HR); }
<LIB>"k temp internal power"
                                       echo; scaling_attr = 1; RN(P IP);
<LIB>"k temp intrinsic fall"
                                       echo; scaling_attr = 1; RN(P_IF);
<LIB>"k_temp_intrinsic_rise"
                                       echo; scaling_attr = 1; RN(P_IR);
<LIB>"k_temp_min_period"
                                       echo; scaling_attr = 1; RN(P_MP); }
                                       echo; scaling_attr = 1; RN(P_MPWH); }
<LIB>"k_temp_min_pulse_width_high"
<LIB>"k_temp_min_pulse_width_low"
                                       echo; scaling_attr = 1; RN(P MPWL); }
<LIB>"k_temp_nochange_fall"
                                       echo; scaling_attr = 1; RN(P_NF); }
<LIB>"k_temp_nochange_rise"
                                       echo; scaling attr = 1; RN(P NR);
<LIB>"k temp pin cap"
                                      echo; scaling attr = 1; RN(P PC);
<LIB>"k temp pin power"
                                      echo; scaling attr = 1; RN(P PP);
<LIB>"k temp recovery fall"
                                      echo; scaling_attr = 1; RN(P_RF);
<LIB>"k temp recovery rise"
                                      echo; scaling_attr = 1; RN(P_RR); }
<LIB>"k_temp_removal_fall"
                                      echo; scaling_attr = 1; RN(P_REF); }
<LIB>"k_temp_removal_rise"
                                      echo; scaling_attr = 1; RN(P_RER); }
<LIB>"k_temp_rise_delay intercept"
                                      echo; scaling_attr = 1; RN(P RDI); }
<LIB>"k_temp_rise_pin_resistance"
                                      echo; scaling attr = 1; RN(P RPR); }
<LIB>"k_temp_rise_propagation"
                                           { echo; scaling_attr = 1; RN(P_RP);
<LIB>"k_temp_rise_transition"
                                      echo; scaling_attr = 1; RN(P RT); }
<LIB>"k temp rise wire resistance"
                                      echo; scaling_attr = 1; RN(P_RWR); }
<LIB>"k_temp_rise_wor_emitter"
                                           { echo; scaling attr = 1;
RN(P_RWE); }
<LIB>"k_temp_rise_wor_intercept"
                                      echo; scaling attr = 1; RN(P RWI); }
<LIB>"k temp setup fall"
                                       echo; scaling attr = 1; RN(P SF); }
<LIB>"k_temp_setup_rise"
                                      echo; scaling_attr = 1; RN(P_SR); }
<LIB>"k_temp_skew_fall"
                                      echo; scaling attr = 1; RN(P SKF); }
<LIB>"k_temp_skew_rise"
                                      echo; scaling_attr = 1; RN(P_SKR);
<LIB>"k_temp_slope_fall"
                                     { echo; scaling attr = 1; RN(P SLF); }
      /* old */
<LIB>"k_temp_slope_rise"
                                     { echo; scaling attr = 1; RN(P SLR); }
      /* old */
<LIB>"k_temp_wire_cap"
                                     { echo; scaling attr = 1; RN(P WC); }
<LIB>"k_temp_wire_res"
                                     { echo; scaling_attr = 1; RN(P WR); }
```

```
<LIB>"k_volt_cell_rise"
                                       echo; scaling attr = 2; RN(P CR);
<LIB>"k volt cell fall"
                                       echo; scaling_attr = 2; RN(P CF); }
<LIB>"k_volt_cell_leakage_power"
                                       echo; scaling_attr = 2; RN(P_CLP); }
<LIB>"k volt cell power"
                                       echo; scaling attr = 2; RN(P CP); }
<LIB>"k volt drive current"
                                       echo; scaling attr = 2; RN(P DC);
<LIB>"k volt drive fall"
                                      echo; scaling attr = 2; RN(P DF);
      /* old */
<LIB>"k_volt_drive_rise"
                                     { echo; scaling_attr = 2; RN(P_DR); }
      /* old */
<LIB>"k_volt_fall_delay_intercept"
                                     { echo; scaling attr = 2; RN(P FDI); }
<LIB>"k_volt_fall_pin_resistance"
                                       echo; scaling attr = 2; RN(P FPR); }
<LIB>"k_volt_fall_propagation"
                                           { echo; scaling_attr = 2; RN(P FP);
<LIB>"k_volt_fall_transition"
                                       echo; scaling_attr = 2; RN(P_FT); }
<LIB>"k_volt_fall_wire_resistance"
                                      echo; scaling attr = 2; RN(P FWR); }
<LIB>"k volt fall wor emitter"
                                           { echo; scaling attr = 2;
RN(P FWE); }
<LIB>"k_volt_fall_wor_intercept"
                                       echo; scaling_attr = 2; RN(P FWI); }
<LIB>"k_volt_hold_fall"
                                       echo; scaling attr = 2; RN(P HF); }
<LIB>"k_volt_hold_rise"
                                       echo; scaling_attr = 2; RN(P_HR);
<LIB>"k_volt_internal_power"
                                       echo; scaling_attr = 2; RN(P_IP);
<LIB>"k_volt_intrinsic_fall"
                                       echo; scaling_attr = 2; RN(P_IF); }
<LIB>"k_volt_intrinsic_rise"
                                       echo; scaling attr = 2; RN(P IR);
<LIB>"k volt min period"
                                       echo; scaling_attr = 2; RN(P MP); }
<LIB>"k volt min pulse width high"
                                       echo; scaling_attr = 2; RN(P_MPWH);
<LIB>"k_volt_min_pulse_width_low"
                                       echo; scaling attr = 2; RN(P MPWL);
<LIB>"k_volt_nochange_fall"
                                       echo; scaling_attr = 2; RN(P_NF);
<LIB>"k_volt_nochange_rise"
                                       echo; scaling_attr = 2; RN(P NR);
<LIB>"k_volt_pin_cap"
                                       echo; scaling attr = 2; RN(P PC);
<LIB>"k_volt_pin_power"
                                      echo; scaling attr = 2; RN(P PP); }
<LIB>"k volt recovery fall"
                                      echo; scaling attr = 2; RN(P RF);
<LIB>"k volt recovery rise"
                                      echo; scaling_attr = 2; RN(P RR);
<LIB>"k volt removal fall"
                                      echo; scaling_attr = 2; RN(P REF); }
<LIB>"k volt removal rise"
                                       echo; scaling_attr = 2; RN(P_RER); }
                                       echo; scaling_attr = 2; RN(P_RDI);
<LIB>"k volt rise delay intercept"
<LIB>"k_volt_rise_pin_resistance"
                                      echo; scaling_attr = 2; RN(P_RPR); }
<LIB>"k_volt_rise_propagation"
                                           { echo; scaling_attr = 2; RN(P RP);
<LIB>"k volt rise transition"
                                      echo; scaling_attr = 2; RN(P RT); }
<LIB>"k volt rise wire resistance"
                                      echo; scaling_attr = 2; RN(P_RWR); }
<LIB>"k_volt_rise_wor_emitter"
                                           { echo; scaling attr = 2;
RN(P_RWE); }
<LIB>"k_volt_rise_wor_intercept"
                                      echo; scaling_attr = 2; RN(P_RWI); }
<LIB>"k_volt_setup_fall"
                                      echo; scaling_attr = 2; RN(P_SF); }
<LIB>"k_volt_setup_rise"
                                      echo; scaling attr = 2; RN(P SR); }
<LIB>"k volt skew fall"
                                      echo; scaling_attr = 2; RN(P SKF); }
<LIB>"k_volt_skew rise"
                                     { echo; scaling attr = 2; RN(P SKR); }
<LIB>"k_volt_slope_fall"
                                     { echo; scaling_attr = 2; RN(P SLF); }
      /* old */
<LIB>"k volt slope rise"
                                    { echo; scaling attr = 2; RN(P SLR); }
      /* old */
<LIB>"k volt wire cap"
                                     { echo; scaling attr = 2; RN(P WC); }
<LIB>"k_volt_wire_res"
                                     { echo; scaling_attr = 2; RN(P_WR); }
<LIB>"capacitive_load unit"
                                     { echo; NEXT_STATE LU; RN(K CLU); }
                                      echo; NEXT_STATE ANY_S; RN(K_DEFINE); }
<LIB>"define"
                                     { echo; NEXT_STATE RT; RN(K_DCA); }
<LIB>"define_cell_area"
```

```
<LIB>"library features"
                                  { echo; RN(K LF); }
                                  { echo; RN(K_PD); }
<LIB>"piece define"
                                  { echo; RN(K_RL); }
<LIB>"routing_layers"
<LIB>"technology"
                            { echo; NEXT STATE NAME; RN(K TECH); }
                           { echo; NEXT STATE CELL; NEXT STATE NAME;
<LIB>"cell"
RN(K CELL); }
<LIB>"input_voltage"
                                  { echo; NEXT_STATE IV; NEXT_STATE NAME;
RN(K_IV); }
<LIB>"lu_table_template"
                                 { echo; NEXT_STATE LTT; NEXT_STATE NAME;
RN(K LTT); }
<LIB>"operating_conditions"
                                 { echo; NEXT STATE OC; NEXT STATE NAME;
RN(K_OC); }
<LIB>"output voltage"
                                  { echo; NEXT STATE OV; NEXT STATE NAME;
RN(K_OV);
<LIB>"parameterized_cell"
                                 { echo; NEXT STATE PC; NEXT STATE NAME;
RN(K PCELL); }
<LIB>"power_lut_template" { echo; NEXT_STATE PLT; NEXT_STATE NAME;
RN(K PLT); }
<LIB>"power_supply"
                                  { echo; NEXT STATE PS; NEXT STATE NAME;
RN(K PS); }
<LIB>"rise_transition_degradation" { echo; NEXT_STATE TD; NEXT STATE NAME;
RN(K_RTD); }
<LIB>"fall_transition_degradation" { echo; NEXT STATE TD; NEXT STATE NAME;
RN(K_FTD); }
                                  { echo; NEXT STATE CELL; NEXT STATE NAME;
<LIB>"scaled_cell"
RN(K_SC);
<LIB>"scaling_factors"
                                  { echo; NEXT STATE LIB; NEXT STATE NAME;
RN(K_SF); }
<LIB>"timing range"
                                  { echo; NEXT_STATE TR; NEXT_STATE NAME;
RN(K TR); }
<LIB>"type"
                           { echo; NEXT STATE TYPE; NEXT STATE NAME;
RN(K_TYPE); }
<LIB>"wire_load"
                   { echo; NEXT_STATE WL; NEXT STATE NAME;
RN(K WL); }
<LIB>"wire_load_selection" { echo; NEXT_STATE WLS; NEXT_STATE NAME;
RN(K_WLS); }
<LIB>"wire_load_table"
                                 { echo; NEXT STATE WLT; NEXT STATE NAME;
RN(K_WLT); }
/* ---- cell group */
<CELL, PC, CENUM> "area"
                                        { echo; RN(AREA); }
<CELL, PC, CENUM> "auxiliary_pad_cell" { echo; NEXT_STATE BOOL; RN(C APC);
<CELL, PC, CENUM>"cell_footprint"
                                             { echo; NEXT_STATE NAME;
RN(C_CF); }
<CELL, PC, CENUM>"cell_power"
                                        { echo; RN(C POWER); }
<CELL, PC, CENUM>"cell_leakage_power"
                                        { echo; RN(C LP); }
<CELL, PC, CENUM>"contention condition"
                                             { echo; RN(C CC); }
                                        { echo; NEXT_STATE SA; RN(C_DF); }
<CELL, PC, CENUM>"dont false"
<CELL, PC, CENUM>"dont touch"
                                       { echo; NEXT_STATE BOOL; RN(C DT);
                                       { echo; NEXT_STATE BOOL; RN(C_DU);
<CELL, PC, CENUM>"dont use"
```

```
<CELL, PC, CENUM>"geometry_print"
                                               { echo; RN(C_GP); }
<CELL, PC, CENUM> "handle negative constraint" { echo; NEXT STATE BOOL;
RN(C HNC); }
<CELL>"interface_timing"
                                        { echo; NEXT STATE BOOL; RN(C IT);
} /* old version */
<CELL, PC, CENUM>"map_only"
                                        { echo; NEXT STATE BOOL; RN(C MO);
<CELL, PC, CENUM> "pad cell"
                                        { echo; NEXT STATE BOOL; RN(C PC);
<CELL, PC, CENUM>"pad type"
                                         { echo; RN(C PT); }
<CELL, PC, CENUM>"pin limit"
                                         { echo; RN(C_PL); }
<CELL, PC, CENUM>"preferred"
                                         { echo; NEXT_STATE BOOL; RN(C_P); }
                                         { echo; RN(C_SF); }
<CELL, PC, CENUM>"scaling_factors"
                                        { echo; NEXT_STATE QSTR; RN(C_SG);
<CELL, PC, CENUM>"scan group"
<CELL, PC, CENUM>"single_bit_degenerate"
                                          { echo; RN(C_SBD); }
<CELL, PC, CENUM>"vhdl name"
                                        { echo; RN(C_VN); }
<CELL, PC, CENUM>"pin_equal"
                                         { echo; RN(C PE); }
<CELL, PC, CENUM>"pin_opposite"
                                         { echo; RN(C_PO); }
<CELL, PC, CENUM>"rail_connection"
                                         { echo; RN(C RC); }
<CELL, PC>"bundle"
                             { echo; NEXT_STATE CBO; RN(C_BUNDLE); }
<CELL, PC, CENUM>"bus"
                                  { echo; NEXT_STATE BUS; RN(C_BUS); }
<CELL, PC>"internal_power"
                                   { echo; NEXT_STATE CIP; RN(C_IP); }
<CELL, PC>"leakage_power"
                                  { echo; NEXT_STATE CLP; RN(C_LPG); }
                             { echo; NEXT_STATE FF; RN(C_FF); }
<CELL, PC, CTC>"ff"
                                   { echo; NEXT_STATE FF; RN(C_FFB); }
{ echo; NEXT_STATE CL; RN(C_LATCH); }
<CELL, PC, CTC>"ff bank"
<CELL, PC, CTC>"latch"
                                   { echo; NEXT_STATE CL; RN(C_LB); }
<CELL, PC, CTC>"latch bank"
<CELL, PC>"lut"
                                   { echo; NEXT STATE LUT; RN(C LUT); }
 /* for FPGA */
<CELL, PC>"memory"
                             { echo; NEXT STATE CM; RN(C MEM); }
<CELL, PC, BUS>"pin"
                                   { echo; NEXT STATE PIN; NEXT STATE NAME;
RN(C PIN); }
<CELL, PC>"routing_track"
                                   { echo; NEXT_STATE CRT; RN(C RT); }
<CELL, PC, CTC>"state"
                                  { echo; NEXT_STATE OST; RN(C STATE); }
 /* old ff,latch */
<CELL, PC, CTC>"statetable"
                                 { echo; NEXT STATE CST; RN(C ST); }
                                   { echo; NEXT_STATE CTC; RN(C_TC); }
<CELL, PC, CENUM>"test_cell"
<CELL, PC, CENUM, BUS>";"
                                  { echo; RN(libctext[0]); }
/*
 /* ---- boundle() in cell */
<CBO>"members"
                                { echo; RN(MEMBERS); }
                             { echo; RN(libctext[0]); }
<CBO>[;]
/* ---- bus() group in cell */
<BUS>"bus_type"
                                   { echo; RN(BUS_TYPE); }
                                   { echo; NEXT_STATE MR; RN(MEM_READ); }
<BUS>"memory read"
```

```
<BUS>"memory write"
                                  { echo; NEXT STATE MW; RN(MEM WRITE); }
 /* ---- cell enum() group */
<CENUM>"cell property"
                                  { echo; RN(CE PROPERTY); }
<CENUM>"default_enum"
                                  { echo; NEXT_STATE BOOL; RN(CE_DE); }
<CENUM>"parameterized_pin"
                                  { echo; NEXT STATE PP; RN(CE PP); }
/* ---- parameterized_pin group */
<PP>"pin properties"
                                  { echo; RN(PP_PROPERTIES); };
<PP>"disabled"
                                  { echo; NEXT STATE BOOL; RN(PP DISABLE);
}
<PP>[;]
                                  { echo; RN(libctext[0]); }
 /* ---- ff(), ff bank() group <FF> */
 /* ---- latch(), latch_bank() group <CL> */
 /* ---- memory_write() group (MW) */
/* ---- state() group (OST) old ff,latch */
                          { echo; RN(CLOCK_ON); }
<FF,OST,MW>"clocked on"
<FF,OST>"next state"
                                  { echo; RN(NEXT_ST); }
<FF,OST,CL>"clear"
                                  { echo; RN(CLEAR); }
<FF,OST,CL>"preset"
                                  { echo; RN(PRESET); }
<FF,OST,CL>"clear_preset_var1"
                                     { echo; RN(CL_PS_V1); }
                                      { echo; RN(CL_PS_V2); }
<FF,OST,CL>"clear_preset_var2"
                                  { echo; RN(ON_ALSO); }
<FF,OST>"clocked on also"
<CL,OST,MW>"enable"
                                  { echo; RN(ENABLE); }
<CL,OST>"enable_on_also"
                                  { echo; RN(ON_ALSO); }
<CL,OST>"data_in"
                           { echo; RN(DATA_IN); }
<OST>"force 01"
                               { echo; RN(FORCE 01); }
<OST>"force 10"
                                  { echo; RN(FORCE_10); }
<OST>"force 00"
                                  { echo; RN(FORCE 00);
<OST>"force_11"
                                { echo; RN(FORCE 11); }
<FF, MW, CL, OST>[;]
                           { echo; RN(libctext[0]); }
/* -----
*/
/* ---- internal_power(), leakage_power() group */
<CIP>"related_input"
                                { echo; RN(REL INP); }
                                 { echo; RN(REL_INPS); } { echo; RN(REL_OUTP); }
<CIP>"related_inputs"
<CIP>"related_outputs"
                                  { echo; RN(VALUES); }
<CIP>"values"
<PIP>"equal_or_opposite_output"
                                  { echo; RN(IP_E000); }
                                { echo; RN(IP_PL); }
<PIP>"power_level"
                            { echo; NEXT_STATE VS; RN(IP FP);
<PIP>"fall_power"
                            { echo; NEXT_STATE VS; RN(IP_RP); }
<PIP>"rise_power"
<PIP>"power"
                                  { echo; NEXT_STATE VS; RN(IP_POWER); }
<CLP>"value"
                                  { echo; RN(VALUE); }
<CIP, CLP, PIP>[;]
                            { echo; RN(libctext[0]); }
*/
```

```
/* ---- lut() group */
                               { echo; RN(LUT IP); }
<LUT>"input pins"
                               { echo; RN(libctext[0]); }
<LUT>[;]
 /* ---- memory() group */
<CM>"type"
                               { echo; RN(CM_TYPE); }
<CM>"ram"
                               { echo; RN(CM_RAM); }
<CM>"rom"
                              { echo; RN(CM ROM); }
<CM>"address_width"
                                    { echo; RN(CM_ADDR_WIDTH); }
                              { echo; RN(CM_WORD_WIDTH); }
<CM>"word_width"
                                 { echo; RN(CM_C_ADDR); }
<CM>"column address"
                              { echo; RN(CM_R_ADDR); }
<CM>"row address"
                                   { echo; RN(libctext[0]); }
<CM>[;]
 /* ---- pin() group */
<PIN,CBO,BUS,PP>"capacitance"
<PIN,CBO,BUS,PP>"clock"
                                     { echo; RN(PIN CAP); }
                                     { echo; NEXT STATE BOOL; RN(PIN CLK); }
<PIN,CBO,BUS,PP>"clock_gate_enable_pin" { echo; NEXT_STATE BOOL;
RN(PIN_CGEP); }
<PIN,CBO,BUS,PP>"connection_class" { echo; RN(PIN_CC); }
<PIN, CBO, BUS, PP>"direction"
                                     { echo; NEXT_STATE DIR; RN(PIN_DIR); }
                                     { echo; NEXT_STATE SA; RN(PIN_DF); }
<PIN,CBO,BUS,PP>"dont_false"
<PIN,CBO,BUS,PP>"drive current"
                                       { echo; RN(PIN DC); }
<PIN, CBO, BUS, PP>"driver type" { echo; NEXT STATE DRT; RN(PIN DT); }
<PIN,CBO,BUS,PP>"edge rate breakpoint f0" { echo; RN(PIN ERBF0); }
<PIN,CBO,BUS,PP>"edge_rate_breakpoint_f1" { echo; RN(PIN_ERBF1); }
<PIN, CBO, BUS, PP>"edge_rate_breakpoint_r0" { echo; RN(PIN_ERBR0);
<PIN, CBO, BUS, PP>"edge_rate_breakpoint_r1" { echo; RN(PIN_ERBR1);
<PIN,CBO,BUS,PP>"edge_rate_fall"
<PIN,CBO,BUS,PP>"edge_rate_rise"
                                           { echo; RN(PIN_ERF); }
                                           { echo; RN(PIN_ERR); }
                                           { echo; RN(PIN_ERLF); }
<PIN,CBO,BUS,PP>"edge_rate_load_fall"
<PIN,CBO,BUS,PP>"edge_rate_load_rise"
                                                 { echo; RN(PIN ERLR); }
<PIN,CBO,BUS,PP>"emitter_count"
                                           { echo; RN(PIN_EC); }
<PIN,CBO,BUS,PP>"fall_current_slop_after_threshold" { echo; RN(PIN_FCSAT);
<PIN,CBO,BUS,PP>"fall_current_slop_before_threshold" { echo; RN(PIN_FCSBT);
<PIN,CBO,BUS,PP>"fall_time_after_threshold"
                                                 { echo; RN(PIN_FTAT); }
<PIN, CBO, BUS, PP>"fall_time_before_threshold" { echo; RN(PIN_FTBT); }
<PIN,CBO,BUS,PP>"fall_wor_emitter" { echo; RN(PIN_FWE); }
<PIN, CBO, BUS, PP>"fall_wor_intercept"
                                                 { echo; RN(PIN_FWI); }
                                     { echo; RN(PIN_FL); }
<PIN, CBO, BUS, PP>"fanout load"
                                      echo; RN(PIN FUNCTION); }
<PIN, CBO, BUS, PP>"function"
                                     { echo; NEXT STATE BOOL; RN(PIN H); }
<PIN, CBO, BUS, PP>"hysteresis"
<PIN,CBO,BUS,PP>"input_map"
                                     { echo; RN(PIN_IM); }
<PIN,CBO,BUS,PP>"input_signal level"
                                      { echo; RN(PIN_ISL); }
                                           { echo; RN(PIN_IV); }
<PIN,CBO,BUS,PP>"input voltage"
                                           { echo; RN(PIN IN); }
<PIN,CBO,BUS,PP>"internal node"
old */
<PIN,CBO,BUS,PP>"inverted output" { echo; NEXT STATE BOOL; RN(PIN IO); }
```

```
<PIN,CBO,BUS,PP>"is_pad"
                                     { echo; NEXT STATE BOOL; RN(PIN IP); }
<PIN, CBO, BUS, PP>"max_fanout"
                                      { echo; RN(PIN MAX FO); }
<PIN,CBO,BUS,PP>"max transition"
                                      { echo; RN(PIN_MAX_TRANS); }
<PIN, CBO, BUS, PP>"max_capacitance"
                                      { echo; RN(PIN MAX CAP); }
<PIN, CBO, BUS, PP>"min fanout"
                                      { echo; RN(PIN MIN FO); }
                                     { echo; RN(PIN_MIN_TRANS); }
<PIN, CBO, BUS, PP>"min transition"
<PIN,CBO,BUS,PP>"min_capacitance"
                                      { echo; RN(PIN_MIN_CAP); }
<PIN,CBO,BUS,PP>"min_period" { echo; RN(PIN MP); }
<PIN,CBO,BUS,PP>"min_pulse_width_high" { echo; RN(PIN_MPWH); } <PIN,CBO,BUS,PP>"min_pulse_width_low" { echo; RN(PIN_MPWL); }
<PIN,CBO,BUS,PP>"multicell_pad_pin" { echo; NEXT_STATE BOOL; RN(PIN_MPP); }
<PIN,CBO,BUS,PP>"multiple_drivers_legal" { echo; NEXT_STATE BOOL;
RN(PIN MDL); }
<PIN,CBO,BUS,PP>"nextstate type" { echo; NEXT STATE NST; RN(PIN NST); }
<PIN,CBO,BUS,PP>"output_signal_level" { echo; RN(PIN_OSL); }
<PIN,CBO,BUS,PP>"output_voltage"
                                     { echo; RN(PIN OV); }
<PIN,CBO,BUS,PP>"pin func type"
                                          { echo; NEXT STATE PFT;
RN(PIN PFT); }
<PIN,CBO,BUS,PP>"pin power"
                                      { echo; RN(PIN_PP); }
<PIN,CBO,BUS,PP>"prefer_tied"
                                      { echo; RN(PIN_PT); }
<PIN,CBO,BUS,PP>"primary_output"
                                     { echo; NEXT_STATE BOOL; RN(PIN PO); }
<PIN, CBO, BUS, PP>"pulling_current"
                                     { echo; RN(PIN_PC); }
<PIN,CBO,BUS,PP>"pulling_resistance" { echo; RN(PIN_PR); } <PIN,CBO,BUS,PP>"reference_capacitance" { echo; RN(PIN_RC); }
<PIN,CBO,BUS,PP>"rise_current_slop_after_threshold" { echo; RN(PIN_RCSAT);
<PIN,CBO,BUS,PP>"rise_current_slop_before_threshold" { echo; RN(PIN_RCSBT);
<PIN,CBO,BUS,PP>"rise_time_after_threshold"
                                                  { echo; RN(PIN_RTAT); }
<PIN,CBO,BUS,PP>"rise_time_before_threshold" { echo; RN(PIN RTBT); }
<PIN,CBO,BUS,PP>"rise_wor_emitter"
                                           { echo; RN(PIN RWE); }
<PIN,CBO,BUS,PP>"rise wor intercept"
                                                  { echo; RN(PIN RWI); }
<PIN,CBO,BUS,PP>"slew control"
                                                  { echo; NEXT STATE PSC;
RN(PIN SC); }
<PIN,CBO,BUS,PP>"state function"
                                            { echo; RN(PIN SF); }
<PIN,CBO,BUS,PP>"three state"
                                            { echo; RN(PIN TS); }
<PIN,CBO,BUS,PP>"vhdl_name"
                                            { echo; RN(PIN_VN); }
                                            { echo; RN(PIN_WC); }
<PIN, CBO, BUS, PP>"wire_capacitance"
<PIN,CBO,BUS,PP>"wired_connection_class" { echo; RN(PIN_WCC); }
<PIN, CBO, BUS, PP>"x function"
                                            { echo; RN(PIN XF); }
<PIN, CBO, BUS>"timing"
                                            { echo; NEXT_STATE TI; RN(TIMING);
<PIN, CBO, BUS>"min_pulse width"
                                                  { echo; NEXT STATE MPW;
RN(MIN_PLUSE_WIDTH); }
<PIN,CBO,BUS>"minimum period"
                                           { echo; NEXT STATE MP;
RN(MIN PERIOD); }
<PIN, CBO, BUS>"internal_power"
                                            { echo; NEXT_STATE PIP;
RN(PIN IPO); }
<PIN>[;]
                                  { echo; RN(libctext[0]); }
/* ---- memory read(), memory write() */
<MR, MW>"address" { echo; RN(ADDRESS); }
<MR>[;]
                                     { echo; RN(libctext[0]); }
```

```
/* ---- routing track() */
<CRT>"tracks"
<CRT>"total_track_area"
                                { echo; RN(TRACKS); }
                     { ecno; RN(TRACKS); } { echo; RN(TRACK_AREA); }
<CRT>[;]
                          { echo; RN(libctext[0]); }
 /* ------
 /* ---- statetable() */

<CST>"table" { echo; NEXT_STATE QSTR; RN(TABLE); }
<CST>[;] { echo; RN(libctext[0]); }

 /* -----
/* ---- test_cell() */
<PIN, CBO, BUS, PP, CTC>"test_output_only" { echo; NEXT_STATE BOOL;
RN(CTC_TOO); }
<CTC>[;]
                         { echo; RN(libctext[0]); }
/* ---- timing() group */
<TI>"edge rate sensitivity f0"
                                    { echo; RN(TI ERSF0); }
<TI>"edge rate sensitivity f1"
                                     { echo; RN(TI ERSF1); }
                                { echo; RN(TI_ERSR0); } 
{ echo; RN(TI_ERSR1); }
<TI>"edge_rate_sensitivity r0"
<TI>"edge_rate_sensitivity_r1"
<TI>"fall_resistance"
                                { echo; RN(TI FR); }
<TI>"rise resistance"
                               { echo; RN(TI_RR); }
<TI>"intrinsic fall"
                               { echo; RN(TI_IF); }
<TI>"intrinsic_rise"
                               { echo; RN(TI_IR); }
                             { echo; RN(TI_RBP); } { echo; NEXT_STATE ROP; RN(TI_ROP); } { echo; RN(TI_RP); }
<TI>"related_bus_pins"
<TI>"related_output_pin"
<TI,PIP>"related pin"
<TI, MPW, MP>"sdf_cond"
                               { echo; /* NEXT_STATE QSTR; */
RN(TI_SDF_C); }
<TI>"sdf_cond_start"
                               { echo; /* NEXT STATE QSTR; */
RN(TI SDF CS); }
<TI>"sdf_cond_end"
                               { echo; /* NEXT_STATE QSTR; */
RN(TI SDF CE); }
<TI>"sdf_edges"
                               { echo; NEXT_STATE SDF E; RN(TI SDF E); }
<TI>"slope_fall"
                          { echo; RN(TI_SF); }
<TI>"slope_rise"
                           { echo; RN(TI_SR); }
                          { echo; NEXT_STATE TIT; RN(TI_TT); }
<TI>"timing_type"
<TI>"timing_sense"
                               { echo; NEXT_STATE TIS; RN(TI_TS); }
                               { echo; RN(TI_WHEN); }
<TI, MPW, MP, PIP, CLP>"when"
                          { echo; RN(TI_WS); }
<TI>"when_start"
<TI>"when end"
                               { echo; RN(TI_WE); }
```

```
<TI>"fall_delay_intercept"
<TI>"fall_nonpaired_twin"
<TI>"fall_pin_resistance"
                                     { echo; RN(TI_FDI); }
                                       { echo; RN(TI FNT); }
                                        { echo; RN(TI_FPR);
                                       { echo; RN(TI FWR);
<TI>"fall wire resistance"
<TI>"rise delay intercept"
                                       { echo; RN(TI RDI);
<TI>"rise nonpaired twin"
                                       { echo; RN(TI RNT); }
                                        { echo; RN(TI_RPR);
<TI>"rise_pin_resistance"
                                        { echo; RN(TI_RWR); }
<TI>"rise_wire_resistance"
<TI>"cell_degradation"
                                        { echo; NEXT_STATE VS; RN(CELL_DEGR); }
<TI>"cell_fall"
                                        { echo; NEXT_STATE VS; RN(CELL FALL); }
<TI>"cell_rise"
                                        { echo; NEXT STATE VS; RN(CELL RISE); }
                                       { echo; NEXT_STATE VS; RN(R_PROP); }
<TI>"rise_propagation"
                                       { echo; NEXT_STATE VS; RN(F_PROP); }
<TI>"fall propagation"
<TI>"rise_transition"
                                       { echo; NEXT_STATE VS; RN(R_TRANS); }
<TI>"fall_transition"
<TI>"rise_constraint"
                                       { echo; NEXT_STATE VS; RN(F_TRANS); }
                                       { echo; NEXT_STATE VS; RN(R_CONS); }
<TI>"fall_constraint"
                                       { echo; NEXT_STATE VS; RN(F_CONS); }
<TI>[;]
                                       { echo; RN(libctext[0]); }
 /* -----
/* --- min_pulse_width() group */
<MPW>"constraint_high" { echo; RN(MPW_CH); }
<MPW>"constraint_low" { echo; RN(MPW_CL); }
<MPW>[;] { echo; RN(libctext[0]); }
/* ---- min_pulse_width() group */
<MP>"constraint" { echo; RN(MP_C); }
                                  { echo; RN(libctext[0]); }
 /* ---- input voltage group */
<IV>"vil"
                                 { echo; RN(IV L); }
                                  { echo; RN(IV_H); }
<IV>"vih"
                                  { echo; RN(IV_MIN); }
<IV>"vimin"
                                  { echo; RN(IV_MAX); }
<IV>"vimax"
                                       { echo; RN(libctext[0]); }
<IV>[;]
/* ---- lu table template group */
<LTT,PLT>"variable_1"
                                        { echo; RN(TBL VAR1); }
                                       { echo; RN(TBL_VAR2); }
<LTT, PLT>"variable 2"
<LTT,PLT>"variable_3"
                                       { echo; RN(TBL_VAR3); }
<LTT,PLT,VS,CIP>"index_1"
                                       { echo; RN(TBL_IDX1); }
<LTT,PLT,VS,CIP>"index_2" { echo; RN(TBL_IDX2); }
<LTT,PLT,VS,CIP>"index_3" { echo; RN(TBL_IDX3); }
<LTT,PLT>"input_net_transition" { echo; RN(LTT_INT); }
<PLT>"input_transition_time" { echo; RN(LTT_INT); }
<LTT,PLT>"total_output_net_capacitance" { echo; RN(LTT_TONC); }
<LTT,PLT>"output_net_length" { echo; RN(LTT_ONL); }
```

```
<LTT,PLT>"output_net_wire_cap"
                                           { echo; RN(LTT_ONWC); }
<LTT,PLT>"output net pin cap" { echo; RN(LTT ONPC); }
<LTT,ROP>"related out total output net capacitance"
                                                              { echo;
RN(LTT ROTONC); }
<LTT,ROP>"related_out_output_net_length"
                                                       { echo; RN(LTT ROONL);
<LTT,ROP>"related out output net wire cap"
                                                              { echo;
RN(LTT ROONWC); }
<LTT,ROP>"related out output net pin cap"
                                                        { echo; RN(LTT ROONPC);
<LTT>"constrained pin transition"
                                     { echo; RN(LTT CPT); }
                                     { echo; RN(LTT_RPT);
<LTT>"related_pin_transition"
                                     { echo; RN(LTT_OPT); }
<LTT, PLT>"output_pin_transition"
<LTT>"connect delay"
                                     { echo; RN(LTT CD); }
<VS>"values"
                                     { echo; RN(VALUES); }
<LTT, PLT, VS, ROP>[;]
                                     { echo; RN(libctext[0]); }
 /* ---- operating conditions group */
<OC>"process"
                                     { echo; RN(OC PROCESS); };
<OC>"temperature"
                               { echo; RN(OC_TEMP); };
                                     { echo; NEXT_STATE TT; RN(OC_TREE); };
<OC>"tree_type"
                                     { echo; RN(OC_VOLT); };
<OC>"voltage"
                                     { echo; RN(OC_PR); };
<OC, PS>"power_rail"
<OC>[;]
                                     { echo; RN(libctext[0]); }
/* ---- output voltage group */
                               { echo; RN(OV_L); }
<0V>"vol"
                                echo; RN(OV_H); }
<0V>"voh"
                                 echo; RN(OV_MIN); }
<OV>"vomin"
<OV>"vomax"
                                echo; RN(OV_MAX); }
                                     { echo; RN(libctext[0]); }
<OV>[;]
/* ---- parameterized_cell group */
<PC>"cell enum"
                                     { echo; NEXT STATE CENUM; NEXT STATE
NAME; RN(PC CELL ENUM); }
 /* ---- power lut template group PLT */
 /* ---- power supply group */
<PS>"default power rail"
                                     { echo; RN(PS DPR); }
 /* ---- rise transition degradation group */
 /* ---- fall transition degradation group */
                                     { echo; RN(VALUES); }
<TD>"values"
                                     { echo; RN(libctext[0]); }
<TD>[;]
 /* ---- scaled cell group ? */
 /* ---- scaling factors group ? */
 /* ---- timing range group */
<TR>"faster factor"
                                     { echo; RN(TR FF); }
<TR>"slower factor"
                                     { echo; RN(TR SF); }
<TR>[;]
                                     { echo; RN(libctext[0]); }
 /* ---- type group */
<TYPE>"base type"
                               { echo; RN(TYPE BASE); }
<TYPE>"array"
                                     { echo; RN(TYPE ARRAY); }
```

```
<TYPE>"bit from"
                             { echo; RN(TYPE FROM); }
<TYPE>"bit to"
                                    { echo; RN(TYPE_TO); }
<TYPE>"bit_width"
                               { echo; RN(TYPE_WIDTH); }
                               { echo; RN(TYPE_DT); }
<TYPE>"data_type"
                               { echo; RN(TYPE_BIT); }
<TYPE>"bit"
                                     { echo; NEXT_STATE BOOL; RN(TYPE_DOWNTO);
<TYPE>"downto"
<TYPE>[;]
                               { echo; RN(libctext[0]); }
 /* ---- wire load group */
<WL>"area"
                               { echo; RN(AREA); }
<WL>"capacitance"
                               { echo; RN(PIN_CAP); }
<WL>"resistance"
                               { echo; RN(WL_RES); }
<WL>"slope"
                               { echo; RN(WL SLOPE); }
                                   { echo; RN(WL_FL); }
<WL, WLT>"fanout_length"
                                     { echo; RN(libctext[0]); }
 /* ---- wire load selection group */
<WLS>"wire_load_from_area"
                                     { echo; RN(WLS_WLFA); };
<WLS, NAME, QSTR>[;]
                                     { echo; RN(libctext[0]); }
 /* ---- wire_load_table group */
<WLT>"fanout area"
                                     { echo; RN(WLT_AREA); }
<WLT>"fanout_capacitance"
                                     { echo; RN(WLT CAP); }
<WLT>"fanout_resistance"
                                    { echo; RN(WLT RES); }
<WLT>[;]
                              { echo; RN(libctext[0]); }
/* ---- boolean value */
<BOOL>"true"
                                     { echo; PREV STATE; RN(BOOL T); }
<BOOL>"false"
                                     { echo; PREV STATE; RN(BOOL F); }
<BOOL>";"
                               { echo; PREV_STATE; RN(libctext[0]); }
/* ---- current unit */
<CU>"A"
                                    { echo; PREV_STATE; RN(CU_A); }
<CU>"mA"
                               { echo; PREV_STATE; RN(CU_MA); }
<CU>"uA"
                               { echo; PREV_STATE; RN(CU UA); }
<CU>";"
                                    { echo; PREV STATE; RN(libctext[0]); }
/* ---- load unit */
<LU>"mf"
                               { echo; PREV_STATE; RN(LU MF); }
                               { echo; PREV_STATE; RN(LU_UF);
<LU>"uf"
                               { echo; PREV_STATE; RN(LU_NF); }
<LU>"nf"
<LU>"pf"
                               { echo; PREV_STATE; RN(LU PF); }
<LU>"ff"
                               { echo; PREV_STATE; RN(LU_FF); }
<LU>";"
                                    { echo; PREV STATE; RN(libctext[0]); }
 /* ---- vote unit */
<VU>"V"
                                    { echo; PREV_STATE; RN(VU_V); }
<VU>"mV"
                               { echo; PREV_STATE; RN(VU MV); }
<VU>";"
                                    { echo; PREV_STATE; RN(libctext[0]); }
 /* ---- power unit */
<PU>"W"
                                    { echo; PREV STATE; RN(PU W); }
<PU>"mW"
                              { echo; PREV STATE; RN(PU MW); }
```

```
<PU>"uW"
                               { echo; PREV_STATE; RN(PU_UW); }
                                 echo; PREV_STATE; RN(PU_NW); }
<PU>"nW"
"Wq"<U9>
                                 echo; PREV STATE; RN(PU PW); }
<PU>";"
                                      { echo; PREV STATE; RN(libctext[0]); }
/* ---- resistance unit */
<RU>"ohm"
                               { echo; PREV_STATE; RN(RU_OHM); }
                               { echo; PREV_STATE; RN(RU_KOHM); }
<RU>"kohm"
<RU>";"
                                      { echo; PREV_STATE; RN(libctext[0]); }
/* ---- time unit */
                               { echo; PREV_STATE; RN(TU NS); }
<TU>"ns"
"2q"<UT>
                               { echo; PREV STATE; RN(TU PS); }
<TU>";"
                                      { echo; PREV_STATE; RN(libctext[0]); }
                               { echo; PREV_STATE; RN(DM G ECL); }
<DM>"generic_ecl"
<DM>"generic_cmos"
                                      { echo; PREV_STATE; RN(DM_G_CMOS); }
<DM>"table_lookup"
                                      { echo; PREV_STATE; RN(DM TBL LOOKUP); }
<DM>"cmos2"
                               { echo; PREV_STATE; RN(DM CMOS2); }
<DM>"piecewise_cmos"
                                      { echo; PREV_STATE; RN(DM P COMS); }
<DM>";"
                                      { echo; PREV STATE; RN(libctext[0]); }
<IPO>"match footprint"
                                      { echo; PREV STATE; RN(IPO MF); }
                                      { echo; PREV_STATE; RN(IPO_NS);
<IPO>"no_swapping"
                                      { echo; PREV_STATE; RN(IPO_IF); }
<IPO>"ignore footprint"
<IPO>";"
                               { echo; PREV_STATE; RN(libctext[0]); }
<MMP>"max"
                               { echo; PREV STATE; RN(MMP MAX); }
                               { echo; PREV STATE; RN(MMP MIN); }
<MMP>"min"
                               { echo; PREV STATE; RN(MMP PLUS); }
<MMP>"plus"
<MMP>";"
                               { echo; PREV STATE; RN(libctext[0]); }
                                      { echo; PREV_STATE; RN(PT LENGTH); }
<PT>"piece_length"
                                      { echo; PREV_STATE; RN(PT WIRE CAP); }
<PT>"piece_wire_cap"
                                      { echo; PREV_STATE; RN(PT_PIN_CAP); } { echo; PREV_STATE; RN(PT_TOTAL_CAP); }
<PT>"piece_pin_cap"
<PT>"piece_total_cap"
                                      { echo; PREV_STATE; RN(libctext[0]); }
<PT>";"
/* ---- wire logic function */
                               { echo; PREV_STATE; RN(WLF_WAND); }
<WLF>"wired and"
                                     { echo; PREV_STATE; RN(WLF_WOR); }
<WLF>"wired or"
<WLF>";"
                               { echo; PREV_STATE; RN(libctext[0]); }
<WLM>"top"
                               { echo; PREV_STATE; RN(WLM_T); }
<WLM>"segmented"
                               { echo; PREV_STATE; RN(WLM_S); }
<WLM>"enclosed"
                                     { echo; PREV_STATE; RN(WLM_E); }
<WLM>";"
                               { echo; PREV_STATE; RN(libctext[0]); }
 /* --- resource type (pad type) */
<RT>"pad slots"
                                      { echo; PREV STATE; RN(RT PS); }
<RT>"pad_driver_sites"
                                      { echo; PREV_STATE; RN(RT_PDS); }
<RT>"pad_input_driver_sites"
                                      { echo; PREV_STATE; RN(RT_PIDS); }
<RT>"pad_output_driver_sites"
                                      { echo; PREV_STATE; RN(RT_PODS); }
<RT>[,]
                                      { echo; RN(libctext[0]); }
<RT>[;]
                                      { echo; PREV_STATE; RN(libctext[0]); }
 /* ---- any string (include keywords) */
```

```
<ANY_S>{string}
                                   { echo; libclval.string =
copy_string(libctext); RN(L_STRING); }
<ANY_S>";"
                            { echo; PREV STATE; RN(libctext[0]); }
/* ---- tree type */
<TT>"best_case_tree"
                                 { echo; PREV STATE; RN(TT BEST); }
<TT>"balanced_tree"
                                   { echo; PREV_STATE; RN(TT_BAL); }
<TT>"worst case tree"
                                   { echo; PREV STATE; RN(TT WORST); }
<TT>";"
                                   { echo; PREV STATE; RN(libctext[0]); }
 /* ---- dont_false condition */
                              { echo; PREV_STATE; RN(SA_SA0); }
<SA>"sa0"
<SA>"sa1"
                             { echo; PREV_STATE; RN(SA_SA1); }
<SA>"sa01"
                             { echo; PREV STATE; RN(SA_SA01); }
<SA>";"
                                   { echo; PREV_STATE; RN(libctext[0]); }
/* ---- direction */
<DIR>"input"
                                   { echo; PREV_STATE; RN(DIR_INPUT); }
                                   { echo; PREV STATE; RN(DIR_OUTPUT); }
<DIR>"output"
<DIR>"inout"
                                   { echo; PREV_STATE; RN(DIR_INOUT); }
                                   { echo; PREV_STATE; RN(DIR_INTERNAL); }
<DIR>"internal"
<DIR>";"
                             { echo; PREV STATE; RN(libctext[0]); }
/* ---- driver_type */
<DRT>"pull_up"
                                   { echo; PREV_STATE; RN(DRT_PULL_UP); }
<DRT>"pull_down"
                             { echo; PREV STATE; RN(DRT PULL DOWN); }
<DRT>"open_drain"
                            { echo; PREV STATE; RN(DRT OPEN DRAIN); }
<DRT>"open source"
                                   { echo; PREV_STATE; RN(DRT OPEN SOURCE);
<DRT>"bus_hold"
                                   { echo; PREV_STATE; RN(DRT_BUS_HOLD); }
<DRT>"resistive"
                             { echo; PREV_STATE; RN(DRT RES); }
                                  { echo; PREV_STATE; RN(DRT_RES0); }
<DRT>"resistive 0"
<DRT>"resistive_1"
                                   { echo; PREV_STATE; RN(DRT_RES1); }
                             { echo; PREV_STATE; RN(libctext[0]); }
<DRT>";"
/* ---- nextstate_type */
<NST>"data"
                             { echo; PREV_STATE; RN(N_DATA); }
                                   { echo; PREV_STATE; RN(N_PRESET); }
<NST>"preset"
                                   { echo; PREV_STATE; RN(N_CLEAR); }
<NST>"clear"
                             { echo; PREV_STATE; RN(N_LOAD); }
<NST>"load"
<NST>"scan_in"
                                   { echo; PREV_STATE; RN(N_SCAN_IN); }
<NST>"scan_enable"
                                   { echo; PREV_STATE; RN(N_SCAN_ENABLE); }
<NST>";"
                             { echo; PREV_STATE; RN(libctext[0]); }
/* ---- pin_func_type */
<PFT>"clock_enable"
                                   { echo; PREV_STATE; RN(CLK_ENABLE); }
                                   { echo; PREV_STATE; RN(ACT HIGH); }
<PFT>"active_high"
<PFT>"active_low"
                             { echo; PREV_STATE; RN(ACT_LOW); }
<PFT>"active_rising"
                                   { echo; PREV_STATE; RN(ACT_RISING); }
<PFT>"active_falling"
                                   { echo; PREV_STATE; RN(ACT_FALLING); }
                             { echo; PREV_STATE; RN(libctext[0]); }
<PFT>";"
/* ---- slew control */
<PSC>"none"
                             { echo; PREV_STATE; RN(NONE SC); }
```

```
<PSC>"low"
                               { echo; PREV STATE; RN(LOW SC); }
<PSC>"medium"
                                      { echo; PREV_STATE; RN(MED_SC); }
                                { echo; PREV_STATE; RN(HIGH_SC); }
<PSC>"high"
<PSC>";"
                                { echo; PREV STATE; RN(libctext[0]); }
<SDF E>"noedge"
                                      { echo; PREV STATE; RN(NO EDGE); }
                                      { echo; PREV STATE; RN(BOTH EDGES); }
<SDF E>"both edges"
<SDF E>"start edge"
                                      { echo; PREV STATE; RN(START EDGE); }
<SDF_E>"end_edge"
                                { echo; PREV_STATE; RN(END_EDGE); }
<SDF E>";"
                                { echo; PREV STATE; RN(libctext[0]); }
/* ---- timing type */
<TIT>"rising edge"
                                      { echo; PREV_STATE; RN(TIT_RE); }
<TIT>"falling edge"
                                      { echo; PREV STATE; RN(TIT FE); }
                                     { echo; PREV_STATE; RN(TIT_PS);
<TIT>"preset"
                                     { echo; PREV_STATE; RN(TIT_CL); }
<TIT>"clear"
<TIT>"hold_rising"
                                     { echo; PREV_STATE; RN(TIT_HR); }
<TIT>"hold_falling"
                                     { echo; PREV_STATE; RN(TIT_HF); }
<TIT>"setup rising"
                                     { echo; PREV STATE; RN(TIT SR); }
<TIT>"setup_falling"
                                     { echo; PREV_STATE; RN(TIT_SF); }
                                     { echo; PREV STATE; RN(TIT RR);
<TIT>"recovery_rising"
                                    { echo; PREV_STATE; RN(TIT_RF); } 
 { echo; PREV_STATE; RN(TIT_TSD); } 
 { echo; PREV_STATE; RN(TIT_TSE); } 
 { echo; PREV_STATE; RN(TIT_RMR); }
<TIT>"recovery_falling"
<TIT>"three_state_disable"
<TIT>"three state enable"
<TIT>"removal_rising"
<TIT>"removal_falling"
                                     { echo; PREV_STATE; RN(TIT_RMF); }
<TIT>"combinational"
                                     { echo; PREV STATE; RN(TIT C); }
<TIT>"skew rising"
                                      { echo; PREV STATE; RN(TIT SKR); }
<TIT>"skew_falling"
                                      { echo; PREV STATE; RN(TIT SKF); }
                                     { echo; PREV_STATE; RN(TIT_NSHR); } { echo; PREV_STATE; RN(TIT_NSHF); }
<TIT>"non_seq_hold_rising"
<TIT>"non seq hold falling"
<TIT>"non_seq_setup_rising"
                                      { echo; PREV_STATE; RN(TIT_NSSR);
<TIT>"non_seq_setup_falling"
                                     { echo; PREV STATE; RN(TIT NSSF);
<TIT>"nochange high high"
                                      { echo; PREV_STATE; RN(TIT_NCHH);
<TIT>"nochange_high_low"
                                      { echo; PREV STATE; RN(TIT NCHL);
<TIT>"nochange low high"
                                      { echo; PREV STATE; RN(TIT NCLH);
<TIT>"nochange low low"
                                      { echo; PREV_STATE; RN(TIT_NCLL); }
<TIT>";"
                                { echo; PREV STATE; RN(libctext[0]); }
/* ---- timing sense */
<TIS>"positive unate"
                                      { echo; PREV_STATE; RN(TIS_POS); }
<TIS>"negative_unate"
                                      { echo; PREV STATE; RN(TIS NEG); }
<TIS>"non_unate"
                                { echo; PREV STATE; RN(TIS NON); }
<TIS>";"
                                { echo; PREV STATE; RN(libctext[0]); }
/* ---- signal_type in test_cell */
<SIG T>"test scan in"
                                      { echo; PREV_STATE; RN(ST_TSI); }
<SIG_T>"test_scan_in_inverted"
                                            { echo; PREV STATE; RN(ST TSII); }
<SIG_T>"test_scan_out"
                                      { echo; PREV STATE; RN(ST TSO); }
<SIG_T>"test_scan_out_inverted"
                                        { echo; PREV STATE; RN(ST TSOI); }
<SIG T>"test scan enable"
                                      { echo; PREV STATE; RN(ST TSE); }
<SIG_T>"test_scan_enable_inverted"
                                     { echo; PREV STATE; RN(ST TSEI); }
<SIG T>"test scan clock"
                                      { echo; PREV STATE; RN(ST TSC); }
```

```
{ echo; PREV STATE; RN(ST_TSCA); }
<SIG T>"test scan clock a"
<SIG T>"test scan clock b"
                                    { echo; PREV_STATE; RN(ST_TSCB); }
<SIG_T>"test_clock"
                                    { echo; PREV_STATE; RN(ST TCLK); }
<SIG_T>";"
                              { echo; PREV_STATE; RN(libctext[0]); }
<REV>([0-9.-/]+[a-zA-Z]?)+
                                   { echo; PREV STATE; libclval.string =
copy_string(libctext); RN(REV V); }
<REV>{string}
                                    { echo; PREV STATE; libclval.string =
copy string(libctext); RN(REV V); }
<REV>{qstring}
                                    { echo; PREV STATE; libclval.string =
copy string(libctext); RN(REV V); }
<REV>";"
                             { echo; PREV_STATE; RN(libctext[0]); }
 */
{estring}
                        { if (yy_start != (1+ 2*NAME)) {
                           REJECT;
                         else {
                           echo;
                           libclval.string = copy_string(libctext);
                           RN(L ESTRING);
{integer}
                         echo;
                         if (yy start == (1+ 2*NAME)) {
                           libclval.string = copy string(libctext);
RN(L_ESTRING);
                         else {
                           libclval.int val = atoi(libctext); RN(L INT);
                       }
{real}
                              { echo; libclval.real val = atof(libctext);
RN(L REAL); }
                        { enum value_type vt;
{string}
                         echo;
                         libclval.string = copy_string(libctext);
                         if (libc_def_find(copy_string(libctext),&vt)) {
                           if (vt == REAL VT)
                                                     { RN(L DEF REAL); }
                           else if (vt == BOOL VT)
                                                     { NEXT_STATE BOOL;
RN(L DEF BOOL); }
                           else
                                               { NEXT_STATE QSTR;
RN(L DEF TEXT); }
                         else
                                               { RN(L STRING); }
{nstring}
                         echo; libclval.string = copy_string(libctext);
RN(L NSTRING); }
[:,{}\+\-\*/'!&\|\(\)^=]
                             { echo; RN(libctext[0]); }
<<EOF>>
                             { yyterminate(); }
                       { echo; lex_error(libctext); }
```

```
웅웅
void lex_next_QSTR_state(void)
{ NEXT STATE QSTR; }
/* ----- */
void lex_next_NAME_state(void)
{ NEXT STATE NAME; }
/* ----- */
void lex next SKIP state(void)
{ NEXT_STATE SKIP; }
/* ----- */
void lex_prev_state()
{ PREV_STATE;
 /* printf(" ====> yystart == %d\n",yy_start); */
void lex error(
    char *S)
 fprintf(stderr, "%s(%d): ERROR: lex error near
%s\n",file_name,libc_lineno,S);
 libc error count++;
 if (libc error count > 10)
  exit(44);
void libcerror(
     char *S)
 fprintf(stderr, "%s(%d): ERROR: near %s,
%s\n",file_name,libc_lineno,libctext,S);
 libc_error_count++;
 if (libc_error_count > 10)
  exit(44);
/* ----- */
void libc error(
     char *S)
 fprintf(stderr, "%s(%d): ERROR: %s\n", file name, libc lineno, S);
 libc_error_count++;
 if (libc_error_count > 10)
  exit(44);
}
```

```
#define LIBC MEM
#include "libc def.h"
double atof(char *);
public
int libc main(
     int argc,
     char *arqv[])
{ int i, j, code = 0;
 char *s;
 char tmpFileName[256];
                       /* for xxx */
 char clfFileName[256];
                        /* for xxx */
 char tmpFileName1[256];
                        /* for yyy */
 char clfFileName1[256];
                        /* for yyy */
 char tmpFileName2[256];
                        /* for zzz */
 char clfFileName2[256];
                        /* for zzz */
 char inFileName[256] = "";
 int linear2TLU = 0;
 FILE *xxx_clf = NULL, *yyy_clf, *yyy_con=NULL, *zzz_clf;
 char conFileNameTmp[256]; /* for yyy version 320 */
 char conFileName[256];
                        /* for yyy version 320 */
 libc_version = 330;
 for (i=1;i<argc;i++) {</pre>
  if (argv[i][0] != '-')
    sprintf(inFileName,"%s",argv[i]) ;
  else {
    if(strcmp(argv[i],"-linear2TLU") == 0)
    linear2TLU = 1;
    else if (strcmp(argv[i],"-3.2") == 0)
    libc_version = 320;
    else if(strcmp(&argv[i][1],"o") == 0) {
    char *str = NULL;
      i++;
    for (str=argv[i];(*str)!='\0';str++) {
      if ((*str) == '/')
       break;
      if ((*str) != '\0') {
       sprintf(tmpFileName, "%s_time.tmp",argv[i]);
       sprintf(tmpFileName1,"%s_logic.tmp",argv[i]);
       sprintf(tmpFileName2, "%s_power.tmp", argv[i]);
```

```
}
   else {
     sprintf(tmpFileName, "%s_time..tmp",argv[i]);
     sprintf(tmpFileName1, "%s_logic..tmp", argv[i]);
     sprintf(tmpFileName2, "%s_power..tmp", argv[i]);
   sprintf(clfFileName, "%s.time",argv[i]);
   sprintf(clfFileName1, "%s.logic", arqv[i]);
   sprintf(clfFileName2,"%s.power",argv[i]);
   if( (xxx_clf = fopen(tmpFileName, "w")) == NULL) {
   fprintf(stderr, "Could not open output file %s.\n",argv[i]);
   return(44);
   if( (yyy_clf = fopen(tmpFileName1, "w")) == NULL) {
  fprintf(stderr, "Could not open output file %s.\n",argv[i]);
  return (44);
  if( (zzz_clf = fopen(tmpFileName2, "w")) == NULL) {
  fprintf(stderr, "Could not open output file %s.\n",argv[i]);
  return (44);
else if(strcmp(&argv[i][1],"temp") == 0) {
i++;
libc_t_max = atof(argv[i]); /*max*/
i++;
libc_t_nom = atof(argv[i]); /* nom */
libc_t_min = atof(argv[i]); /* min */
else if(strcmp(&argv[i][1],"volt") == 0) {
i++; /* WARNING: Notice reverse index convention! */
libc_v_max = atof(argv[i]); /* max */
libc_v_nom = atof(argv[i]); /* nom */
i++;
libc_v_min = atof(argv[i]); /* min */
else if(strcmp(&argv[i][1], "process") == 0) {
i++;
libc_p_max = atof(argv[i]);
libc_p_nom = atof(argv[i]);
i++;
libc_p_min = atof(argv[i]);
else if(strcmp(&argv[i][1], "facts") == 0) {
cap_scale = atof(argv[i]);
i++;
resist_scale = atof(argv[i]);
i++;
time_scale = atof(argv[i]);
else if(strcmp(&argv[i][1], "power_facts") == 0) {
watt scale = atof(arqv[i]);
```

```
i++;
      joule_scale = atof(argv[i]);
    }
  }
  if (libc version < 330 ) {
    sprintf(conFileNameTmp,"%s_con",tmpFileName1);
    sprintf(conFileName,
                           "%s_con",clfFileName1);
    if( (yyy_con = fopen(conFileNameTmp, "w")) == NULL) {
      fprintf(stderr, "Could not open output file %s.\n",conFileNameTmp);
      return(44);
  }
  if( (!xxx_clf) || (inFileName[0] == '\0') || (argc <= 1)) {
    printf("Usage: %s -o output_file\n",argv[0]);
    printf("
                        [-3.2]\n");
   printf("
                         [-temp max nom min] [-process max nom min] [-volt max
nom min]\n");
   printf("
                         [-facts cap_scale resist scale time_scale]\n");
   printf("
                         [-power_facts watt_scale joule_scale]\n");
   printf("
                        input_file\n");
   return(44);
 libc_error_count = 0;
 fprintf(stderr, "Parsing %s ...\n", inFileName);
 if( code = parse_a_tlib_file(inFileName) || libc_error_count) {
   fprintf(stderr,"Error found, translation failed.\n");
   code = 1;
 if (!code) {
   if (tech_lib->delay_model == UNKNOW M)
     tech_lib->delay_model = GENERIC CMOS;
   libc_gen_clf_main(xxx_clf,yyy_clf,yyy_con,zzz_clf,tech lib);
 fclose(xxx clf);
 fclose(yyy_clf);
 fclose(yyy con);
 fclose(zzz_clf);
 if (linear2TLU)
   /* syLinear2TLU(tmpFileName, clfFileName) */;
 else {
   if (rename(tmpFileName, clfFileName) == -1) {
     fprintf(stderr, "Cannot create CLF file %s\n", clfFileName);
     return (45);
   }
 if (rename(tmpFileName1, clfFileName1) == -1) {
   fprintf(stderr, "Cannot create CLF file %s\n", clfFileName1);
   return(45);
 if (libc_version < 330) {</pre>
```

```
#include <stdio.h>
#include <assert.h>
#include <stdlib.h>
#include <ctype.h>
#include <string.h>
#include "libc mem.h"
#if DMP MEM ANY
static int libc_mem_curr_size=0;
static int libc_mem_peak_size=0;
static int any_unit_rec_curr_msize=0;
static int any_unit_rec_peak_msize=0;
static int any_unit_rec_lcurr msize=0;
static int any unit_rec_lpeak msize=0;
static int libc name list rec curr msize=0;
static int libc_name_list_rec_peak_msize=0;
static int libc_name_list_rec_lcurr_msize=0;
static int libc name list rec lpeak msize=0;
static int libc_name_list_list_curr msize=0;
static int libc name list list peak msize=0;
static int libc name list list lcurr msize=0;
static int libc_name_list_list_lpeak_msize=0;
static int libc_float_list_rec_curr_msize=0;
static int libc_float_list_rec_peak_msize=0;
static int libc_float_list_rec_lcurr msize=0;
static int libc_float_list_rec_lpeak_msize=0;
static int libc_float_list_list_curr_msize=0;
static int libc_float_list_list_peak_msize=0;
static int libc_float_list_lcurr_msize=0;
static int libc float list list lpeak msize=0;
static int libc_define_rec_curr_msize=0;
static int libc define rec peak msize=0;
static int libc define rec lcurr msize=0;
static int libc_define_rec_lpeak_msize=0;
static int libc cell area rec curr msize=0;
static int libc_cell_area_rec_peak_msize=0;
static int libc_cell_area_rec_lcurr_msize=0;
static int libc_cell_area_rec_lpeak_msize=0;
static int libc k factor rec curr msize=0;
static int libc k factor rec peak msize=0;
static int libc k factor_rec_lcurr_msize=0;
static int libc_k_factor_rec_lpeak_msize=0;
static int libc lib rec curr msize=0;
static int libc_lib_rec_peak_msize=0;
static int libc_lib_rec_lcurr_msize=0;
static int libc_lib_rec_lpeak_msize=0;
static int libc lu table template rec curr msize=0;
static int libc_lu_table_template_rec peak msize=0;
static int libc_lu_table_template_rec_lcurr_msize=0;
static int libc lu table template rec lpeak msize=0;
static int libc_oc_power_rail_rec_curr_msize=0;
static int libc_oc_power_rail_rec_peak msize=0;
static int libc_oc_power_rail_rec_lcurr_msize=0;
static int libc oc power rail rec lpeak msize=0;
static int libc_operating_condition_rec_curr_msize=0;
static int libc_operating_condition_rec_peak_msize=0;
static int libc_operating_condition_rec_lcurr_msize=0;
static int libc_operating_condition_rec_lpeak_msize=0;
```

```
static int libc_power_supply_rec_curr msize=0;
static int libc_power_supply rec peak msize=0;
static int libc_power_supply_rec_lcurr_msize=0;
static int libc_power_supply_rec_lpeak_msize=0;
static int libc_timing_range_rec_curr_msize=0;
static int libc_timing_range_rec_peak_msize=0;
static int libc_timing_range_rec_lcurr_msize=0;
static int libc_timing_range_rec_lpeak_msize=0;
static int libc type rec_curr_msize=0;
static int libc_type_rec_peak_msize=0;
static int libc_type_rec_lcurr_msize=0;
static int libc type rec lpeak msize=0;
static int libc_fanout_length_rec_curr_msize=0;
static int libc_fanout_length_rec_peak_msize=0;
static int libc_fanout_length_rec_lcurr_msize=0;
static int libc_fanout_length_rec_lpeak_msize=0;
static int libc wire load rec curr msize=0;
static int libc_wire_load_rec_peak_msize=0;
static int libc wire_load_rec_lcurr_msize=0;
static int libc_wire_load_rec lpeak msize=0;
static int libc_wire_load from area rec curr msize=0;
static int libc_wire_load_from_area_rec_peak_msize=0;
static int libc_wire_load_from_area_rec_lcurr_msize=0;
static int libc_wire_load_from_area_rec_lpeak_msize=0;
static int libc_wire_load_selection_rec_curr_msize=0;
static int libc_wire_load_selection_rec_peak_msize=0;
static int libc_wire_load_selection_rec_lcurr_msize=0;
static int libc_wire_load_selection_rec lpeak msize=0;
static int libc_cell_rec_curr_msize=0;
static int libc_cell_rec_peak_msize=0;
static int libc_cell_rec_lcurr_msize=0;
static int libc_cell_rec_lpeak_msize=0;
static int libc memory_write_rec_curr msize=0;
static int libc_memory_write_rec_peak_msize=0;
static int libc memory write rec lcurr msize=0;
static int libc_memory_write_rec_lpeak_msize=0;
static int libc_bool_opr_rec_curr_msize=0;
static int libc_bool_opr_rec_peak_msize=0;
static int libc_bool_opr_rec_lcurr_msize=0;
static int libc bool opr rec lpeak msize=0;
static int libc_pin_rec_curr_msize=0;
static int libc_pin_rec_peak msize=0;
static int libc_pin_rec lcurr msize=0;
static int libc_pin_rec_lpeak_msize=0;
static int libc_ff_latch_rec_curr_msize=0;
static int libc_ff_latch_rec_peak_msize=0;
static int libc_ff latch rec lcurr msize=0;
static int libc_ff_latch_rec_lpeak_msize=0;
static int libc_internal_power_curr msize=0;
static int libc_internal_power_peak_msize=0;
static int libc_internal_power_lcurr_msize=0;
static int libc_internal_power lpeak msize=0;
static int libc leakage power curr msize=0;
static int libc leakage power peak msize=0;
static int libc_leakage_power_lcurr_msize=0;
static int libc_leakage_power_lpeak_msize=0;
static int libc_memory_rec_curr_msize=0;
```

```
static int libc memory rec peak msize=0;
static int libc_memory_rec_lcurr msize=0;
static int libc memory rec lpeak msize=0;
static int libc_piece_value_rec_curr_msize=0;
static int libc_piece_value_rec_peak_msize=0;
static int libc_piece_value_rec_lcurr_msize=0;
static int libc piece value rec_lpeak msize=0;
static int libc_float_rec_curr_msize=0;
static int libc_float_rec_peak_msize=0;
static int libc_float_rec_lcurr_msize=0;
static int libc_float_rec_lpeak_msize=0;
static int libc_table_val_rec_curr_msize=0;
static int libc table val rec peak msize=0;
static int libc_table_val_rec lcurr msize=0;
static int libc_table_val rec lpeak msize=0;
static int libc_timing_rec_curr_msize=0;
static int libc_timing_rec_peak_msize=0;
static int libc_timing_rec_lcurr_msize=0;
static int libc timing rec lpeak msize=0;
static int libc_min_pulse width_rec curr msize=0;
static int libc_min_pulse_width_rec_peak_msize=0;
static int libc_min_pulse_width_rec_lcurr_msize=0;
static int libc_min_pulse_width_rec_lpeak_msize=0;
static int libc_minimum_period_rec_curr_msize=0;
static int libc_minimum_period_rec_peak_msize=0;
static int libc_minimum period rec lcurr msize=0;
static int libc_minimum_period_rec_lpeak_msize=0;
static int libc_routing_track_rec_curr_msize=0;
static int libc_routing_track_rec_peak_msize=0;
static int libc_routing_track_rec_lcurr_msize=0;
static int libc_routing_track_rec_lpeak_msize=0;
static int libc_def_entry_rec_curr_msize=0;
static int libc_def_entry_rec_peak_msize=0;
static int libc_def entry rec lcurr msize=0;
static int libc_def_entry_rec_lpeak_msize=0;
static int libc_define_value_rec_curr_msize=0;
static int libc_define_value_rec_peak_msize=0;
static int libc_define_value_rec_lcurr_msize=0;
static int libc_define_value_rec_lpeak_msize=0;
static int libc_glb const rec curr msize=0;
static int libc_glb_const_rec_peak_msize=0;
static int libc glb const rec lcurr msize=0;
static int libc_glb_const_rec_lpeak_msize=0;
static int libc_def_table_rec_curr_msize=0;
static int libc_def_table_rec_peak_msize=0;
static int libc_def_table_rec_lcurr_msize=0;
static int libc_def_table_rec_lpeak_msize=0;
void reset_libc_mem_size(){
  any_unit_rec_lcurr_msize=any_unit_rec_lpeak_msize=0;
  libc_name_list_rec_lcurr_msize=libc_name_list_rec_lpeak_msize=0;
  libc_name_list_list_lcurr_msize=libc_name_list_list_lpeak_msize=0;
  libc_float_list_rec_lcurr_msize=libc_float_list_rec_lpeak_msize=0;
  libc_float_list_list_lcurr_msize=libc_float_list_list_lpeak msize=0;
 libc_define_rec_lcurr_msize=libc_define_rec_lpeak_msize=0;
 libc_cell_area_rec_lcurr_msize=libc_cell_area_rec_lpeak_msize=0;
  libc_k_factor_rec_lcurr_msize=libc_k_factor_rec_lpeak_msize=0;
 libc_lib_rec_lcurr_msize=libc_lib rec_lpeak_msize=0;
```

```
libc lu table_template_rec_lcurr_msize=libc_lu_table_template_rec_lpeak_msize
=0;
  libc_oc_power_rail_rec_lcurr_msize=libc_oc_power_rail_rec_lpeak_msize=0;
libc_operating_condition_rec_lcurr_msize=libc_operating_condition_rec lpeak m
  libc_power_supply_rec_lcurr_msize=libc_power_supply_rec_lpeak_msize=0;
  libc timing range rec lcurr msize=libc timing range rec lpeak msize=0;
  libc_type_rec_lcurr_msize=libc_type_rec_lpeak msize=0;
  libc_fanout_length_rec_lcurr_msize=libc_fanout_length_rec_lpeak_msize=0;
  libc_wire_load rec_lcurr_msize=libc wire load rec_lpeak msize=0;
libc_wire_load_from_area_rec_lcurr_msize=libc_wire_load_from_area_rec_lpeak_m
size=0;
libc_wire_load_selection_rec_lcurr_msize=libc_wire_load_selection_rec_lpeak_m
  libc_cell_rec_lcurr_msize=libc cell_rec_lpeak msize=0;
  libc_memory_write_rec_lcurr_msize=libc_memory_write_rec_lpeak_msize=0;
  libc_bool_opr_rec_lcurr_msize=libc bool opr rec lpeak msize=0;
  libc_pin_rec_lcurr_msize=libc_pin_rec_lpeak_msize=0;
  libc_ff_latch_rec_lcurr_msize=libc_ff_latch_rec_lpeak_msize=0;
  libc_internal_power_lcurr_msize=libc_internal_power_lpeak_msize=0;
  libc_leakage_power_lcurr_msize=libc_leakage_power_lpeak_msize=0;
  libc_memory_rec_lcurr_msize=libc_memory_rec_lpeak_msize=0;
  libc_piece_value_rec_lcurr_msize=libc_piece_value_rec_lpeak msize=0;
  libc_float_rec_lcurr_msize=libc_float_rec_lpeak_msize=0;
  libc table_val rec_lcurr_msize=libc_table_val_rec_lpeak_msize=0;
  libc_timing_rec_lcurr_msize=libc_timing rec lpeak msize=0;
libc_min_pulse_width_rec_lcurr_msize=libc min_pulse width rec lpeak msize=0;
  libc_minimum_period_rec_lcurr_msize=libc_minimum_period_rec_lpeak_msize=0;
  libc routing track_rec_lcurr_msize=libc_routing_track_rec_lpeak_msize=0;
  libc_def_entry_rec_lcurr_msize=libc_def_entry_rec_lpeak_msize=0;
  libc_define_value_rec_lcurr_msize=libc_define_value_rec_lpeak_msize=0;
  libc_glb_const_rec_lcurr_msize=libc_glb_const_rec_lpeak_msize=0;
  libc_def_table_rec_lcurr_msize=libc_def_table_rec_lpeak_msize=0;
void rpt_libc_mem_lsize(int peak) {
dmp clr statistic(100);
dmp_ins_statistic("libc_mem", "any_unit_rec", peak?any_unit_rec_lcurr msize:any
_unit_rec_lpeak msize);
dmp_ins_statistic("libc_mem","libc_name_list_rec",peak?libc_name_list_rec_lcu
rr_msize:libc_name_list_rec lpeak msize);
dmp_ins_statistic("libc_mem","libc_name_list_list",peak?libc_name_list_list_l
curr_msize:libc_name_list_list_lpeak_msize);
dmp_ins_statistic("libc_mem","libc_float_list_rec",peak?libc_float_list_rec_l
curr_msize:libc_float_list_rec_lpeak_msize);
dmp_ins_statistic("libc_mem","libc_float_list_list",peak?libc_float_list_list
_lcurr_msize:libc_float_list list lpeak msize);
```

```
dmp_ins_statistic("libc_mem","libc_define_rec",peak?libc_define_rec_lcurr_msi
 ze: libc define rec lpeak msize);
dmp_ins_statistic("libc_mem","libc_cell_area_rec",peak?libc_cell_area_rec_lcu
rr_msize:libc_cell_area_rec_lpeak_msize);
dmp_ins_statistic("libc_mem","libc_k_factor_rec",peak?libc_k_factor_rec_lcurr
_msize:libc_k_factor_rec_lpeak_msize);
dmp_ins_statistic("libc_mem","libc_lib_rec",peak?libc_lib_rec_lcurr_msize:lib
c_lib_rec_lpeak msize);
dmp_ins_statistic("libc_mem","libc_lu_table_template rec",peak?libc lu table
template_rec_lcurr_msize:libc_lu_table_template_rec_lpeak_msize);
dmp_ins_statistic("libc_mem","libc_oc_power_rail_rec",peak?libc_oc_power_rail
_rec_lcurr_msize:libc_oc_power_rail_rec_lpeak_msize);
dmp_ins_statistic("libc_mem","libc_operating_condition_rec",peak?libc_operati
ng_condition_rec_lcurr_msize:libc_operating_condition_rec_lpeak_msize);
dmp_ins_statistic("libc_mem","libc_power_supply_rec",peak?libc_power_supply_r
ec_lcurr_msize:libc_power_supply_rec_lpeak msize);
dmp_ins_statistic("libc_mem","libc_timing_range_rec",peak?libc_timing_range_r
ec_lcurr_msize:libc_timing_range_rec_lpeak_msize);
dmp_ins_statistic("libc_mem","libc_type_rec",peak?libc_type_rec_lcurr_msize:1
ibc type rec lpeak msize);
dmp_ins_statistic("libc_mem","libc_fanout_length_rec",peak?libc_fanout_length
_rec_lcurr_msize:libc_fanout length rec lpeak msize);
dmp_ins_statistic("libc_mem","libc_wire_load_rec",peak?libc_wire_load_rec_lcu
rr_msize:libc_wire_load_rec_lpeak_msize);
dmp_ins_statistic("libc_mem","libc_wire_load_from_area_rec",peak?libc_wire_lo
ad_from_area_rec_lcurr_msize:libc_wire_load_from_area_rec_lpeak msize);
dmp_ins_statistic("libc_mem","libc_wire_load_selection_rec",peak?libc_wire_lo
ad_selection_rec_lcurr_msize:libc_wire_load_selection_rec_lpeak_msize);
dmp_ins_statistic("libc_mem","libc_cell_rec",peak?libc_cell_rec_lcurr_msize:1
ibc_cell_rec_lpeak msize);
dmp_ins_statistic("libc_mem","libc_memory_write_rec",peak?libc_memory_write_r
ec_lcurr_msize:libc_memory_write_rec_lpeak_msize);
dmp_ins_statistic("libc_mem","libc_bool_opr_rec",peak?libc_bool_opr_rec_lcurr
_msize:libc_bool_opr_rec_lpeak_msize);
dmp_ins_statistic("libc_mem","libc_pin_rec",peak?libc_pin_rec_lcurr_msize:lib
c_pin_rec_lpeak_msize);
dmp_ins_statistic("libc_mem","libc_ff_latch_rec",peak?libc_ff_latch_rec_lcurr
_msize:libc_ff_latch_rec_lpeak_msize);
```

```
dmp_ins_statistic("libc_mem","libc_internal_power",peak?libc_internal_power_l
 curr_msize:libc_internal_power_lpeak_msize);
dmp_ins_statistic("libc_mem","libc_leakage_power",peak?libc_leakage_power lcu
rr msize:libc_leakage_power_lpeak_msize);
dmp ins statistic("libc_mem","libc_memory_rec",peak?libc_memory_rec_lcurr_msi
ze:libc_memory_rec_lpeak_msize);
dmp_ins_statistic("libc_mem","libc_piece_value_rec",peak?libc_piece_value_rec
_lcurr_msize:libc_piece_value_rec_lpeak_msize);
dmp_ins_statistic("libc_mem","libc_float_rec",peak?libc_float_rec_lcurr_msize
:libc_float_rec_lpeak_msize);
dmp_ins statistic("libc_mem","libc_table_val_rec",peak?libc_table_val_rec_lcu
rr_msize:libc_table_val_rec_lpeak_msize);
dmp_ins_statistic("libc_mem","libc_timing_rec",peak?libc_timing_rec_lcurr_msi
ze:libc_timing_rec_lpeak_msize);
dmp_ins_statistic("libc_mem","libc_min_pulse_width_rec",peak?libc min pulse w
idth_rec_lcurr_msize:libc_min_pulse_width_rec_lpeak msize);
dmp_ins_statistic("libc_mem","libc_minimum_period_rec",peak?libc_minimum_peri
od_rec_lcurr_msize:libc_minimum_period_rec_lpeak_msize);
dmp_ins_statistic("libc_mem","libc_routing_track_rec",peak?libc_routing_track
_rec_lcurr_msize:libc_routing_track_rec_lpeak_msize);
dmp_ins statistic("libc_mem","libc_def_entry_rec",peak?libc_def_entry_rec lcu
rr_msize:libc_def_entry_rec_lpeak_msize);
dmp_ins_statistic("libc_mem","libc_define_value_rec",peak?libc_define_value_r
ec_lcurr_msize:libc_define_value_rec_lpeak msize);
dmp_ins_statistic("libc_mem","libc_glb_const_rec",peak?libc_glb_const_rec_lcu
rr_msize:libc_glb_const_rec_lpeak msize);
dmp_ins_statistic("libc_mem","libc_def_table_rec",peak?libc_def_table_rec_lcu
rr_msize:libc_def_table_rec_lpeak_msize);
dmp_rpt_statistic(0);
void acc_libc_mem size(int peak) {
dmp_ins_statistic("libc_mem", "any_unit_rec", peak?any_unit_rec_peak_msize:any_
unit_rec_curr_msize);
dmp_ins_statistic("libc_mem","libc_name_list_rec",peak?libc_name_list_rec_pea
k_msize:libc_name_list_rec_curr_msize);
dmp_ins_statistic("libc_mem","libc_name_list_list",peak?libc_name_list_list_p
eak_msize:libc_name_list_list_curr_msize);
dmp_ins_statistic("libc_mem","libc_float_list_rec",peak?libc_float_list_rec_p
eak_msize:libc_float_list rec curr msize);
```

```
dmp_ins_statistic("libc_mem","libc_float_list_list",peak?libc_float_list_list
 _peak_msize:libc_float list list curr msize);
 dmp_ins_statistic("libc_mem","libc_define_rec",peak?libc_define_rec_peak_msiz
 e:libc_define rec curr msize);
 dmp_ins_statistic("libc_mem","libc_cell_area_rec",peak?libc_cell_area_rec_pea
 k_msize:libc_cell_area rec_curr_msize);
 dmp_ins_statistic("libc_mem","libc_k_factor_rec",peak?libc_k_factor_rec_peak_
msize:libc_k_factor_rec_curr_msize);
dmp_ins_statistic("libc_mem","libc_lib_rec",peak?libc_lib_rec_peak_msize:libc
_lib_rec_curr_msize);
dmp_ins_statistic("libc_mem","libc_lu_table_template_rec",peak?libc_lu_table_
template_rec_peak_msize:libc_lu_table_template_rec_curr_msize);
dmp_ins_statistic("libc_mem","libc_oc_power_rail_rec",peak?libc_oc_power_rail
_rec_peak_msize:libc_oc_power_rail_rec_curr_msize);
dmp_ins_statistic("libc_mem","libc_operating_condition_rec",peak?libc_operati
ng_condition_rec_peak_msize:libc_operating_condition_rec_curr_msize);
dmp_ins_statistic("libc_mem","libc_power_supply_rec",peak?libc_power_supply_r
ec_peak_msize:libc_power_supply_rec_curr_msize);
dmp_ins_statistic("libc_mem","libc_timing_range_rec",peak?libc_timing_range_r
ec_peak_msize:libc_timing_range_rec_curr_msize);
dmp_ins_statistic("libc_mem","libc_type_rec",peak?libc_type_rec_peak_msize:li
bc type rec curr msize);
dmp_ins_statistic("libc_mem","libc_fanout_length_rec",peak?libc_fanout_length
_rec_peak_msize:libc_fanout_length_rec_curr_msize);
dmp_ins_statistic("libc_mem","libc_wire_load_rec",peak?libc_wire_load_rec_pea
k_msize:libc_wire_load rec_curr_msize);
dmp_ins_statistic("libc_mem","libc_wire_load_from_area_rec",peak?libc_wire_lo
ad_from_area_rec_peak_msize:libc_wire_load_from_area_rec_curr_msize);
dmp_ins_statistic("libc_mem","libc_wire_load_selection_rec",peak?libc_wire_lo
ad_selection_rec_peak_msize:libc_wire_load_selection_rec_curr_msize);
dmp_ins_statistic("libc_mem","libc_cell_rec",peak?libc_cell_rec_peak_msize:li
bc_cell_rec_curr msize);
dmp_ins_statistic("libc_mem","libc_memory_write_rec",peak?libc_memory_write_r
ec_peak_msize:libc_memory_write_rec_curr_msize);
dmp_ins_statistic("libc_mem","libc_bool_opr_rec",peak?libc_bool_opr_rec_peak_
msize:libc_bool_opr_rec_curr_msize);
dmp_ins_statistic("libc_mem","libc_pin_rec",peak?libc_pin_rec_peak_msize:libc
_pin_rec_curr msize);
```

```
dmp_ins_statistic("libc_mem","libc_ff_latch_rec",peak?libc_ff_latch_rec_peak
msize:libc_ff_latch_rec_curr_msize);
dmp_ins_statistic("libc_mem","libc_internal_power",peak?libc_internal_power_p
eak_msize:libc_internal_power_curr_msize);
dmp_ins_statistic("libc_mem","libc_leakage_power",peak?libc_leakage_power_pea
k_msize:libc_leakage_power_curr_msize);
dmp_ins_statistic("libc_mem","libc_memory_rec",peak?libc_memory_rec_peak_msiz
e:libc_memory_rec_curr_msize);
dmp_ins_statistic("libc_mem","libc_piece_value_rec",peak?libc_piece_value_rec
_peak_msize:libc_piece_value_rec_curr_msize);
dmp_ins_statistic("libc_mem","libc_float_rec",peak?libc_float_rec_peak_msize:
libc_float_rec_curr msize);
dmp_ins_statistic("libc_mem","libc_table_val_rec",peak?libc_table_val_rec_pea
k msize:libc_table_val_rec_curr_msize);
dmp_ins_statistic("libc_mem","libc_timing_rec",peak?libc_timing_rec_peak_msiz
e:libc_timing_rec curr msize);
dmp_ins_statistic("libc_mem","libc_min_pulse_width_rec",peak?libc_min_pulse_w
idth_rec_peak_msize:libc_min_pulse_width_rec_curr_msize);
dmp_ins_statistic("libc_mem","libc_minimum_period_rec",peak?libc_minimum_peri
od_rec_peak_msize:libc_minimum_period_rec_curr_msize);
dmp_ins_statistic("libc_mem","libc_routing_track_rec",peak?libc_routing_track
_rec_peak_msize:libc_routing_track_rec_curr_msize);
dmp_ins_statistic("libc_mem","libc_def_entry_rec",peak?libc_def_entry_rec_pea
k_msize:libc_def_entry_rec curr msize);
dmp_ins_statistic("libc_mem","libc_define_value_rec",peak?libc_define_value_r
ec_peak_msize:libc_define_value_rec_curr_msize);
dmp_ins_statistic("libc_mem","libc_glb_const_rec",peak?libc_glb_const_rec_pea
k_msize:libc_glb_const_rec_curr_msize);
dmp_ins_statistic("libc_mem","libc_def_table_rec",peak?libc_def_table_rec pea
k_msize:libc_def_table_rec_curr msize);
#define dmp_libc_mem_size inc(sz) \
    libc_mem_curr_size += sz; \
    if (libc_mem curr size>libc mem_peak_size) \
    libc_mem_peak_size = libc_mem_curr_size;
#define dmp_libc_mem_size_dec(sz) libc_mem_curr_size -= sz
#define dmp_any_unit_rec_msize_inc(sz)
    any_unit_rec_lcurr_msize += sz; \
    if (any_unit_rec_lcurr_msize>any_unit_rec_lpeak_msize) \
   any_unit_rec_lpeak_msize = any_unit_rec_lcurr_msize;\
   any_unit_rec_curr_msize += sz; \
   if (any_unit_rec_curr_msize>any_unit_rec_peak_msize) \
```

```
any_unit_rec_peak_msize = any_unit_rec_curr msize;
 #define dmp_any_unit_rec_msize_dec(sz) \
       any_unit_rec_curr msize -= sz;\
       any_unit_rec lcurr msize -= sz
 struct any_unit_rec *new any unit rec(void)
 { struct any_unit_rec *p = (struct any_unit_rec *) get_mb_ptr(1);
 #if DMP MEM ANY
  dmp_libc_mem_size_inc(8);
  dmp_any_unit_rec_msize inc(8);
 #endif
  return(p);
#define dmp_libc_name_list_rec_msize inc(sz)
    libc_name_list_rec_lcurr_msize += sz; \
    if (libc_name_list_rec_lcurr_msize>libc_name_list_rec_lpeak_msize) \
    libc_name_list_rec_lpeak_msize = libc_name_list_rec_lcurr_msize;\
    libc_name_list_rec_curr_msize += sz; \
    if (libc_name_list_rec_curr_msize>libc_name list rec peak msize) \
    libc_name_list_rec_peak_msize = libc_name_list_rec_curr_msize;
#define dmp_libc_name_list_rec_msize_dec(sz) \
      libc_name_list_rec_curr_msize -= sz;\
      libc_name_list_rec_lcurr_msize -= sz
struct libc name list rec *new_libc name list rec(void)
{ struct libc_name_list_rec *p = (struct libc_name_list_rec *) get_mb_ptr(1);
#if DMP MEM ANY
  dmp_libc_mem size inc(8);
  dmp_libc_name_list_rec_msize_inc(8);
#endif
  return(p);
#define dmp_libc_name_list list msize inc(sz)
    libc_name_list_list_lcurr_msize += sz; \
    if (libc_name_list_list_lcurr_msize>libc_name_list_list_lpeak_msize) \
    libc_name_list_list_lpeak_msize = libc_name_list_list_lcurr msize;\
    libc_name_list_list_curr_msize += sz; \
    if (libc_name_list_list_curr_msize>libc_name_list_list_peak_msize) \
    libc_name_list_list_peak_msize = libc_name_list_list_curr_msize;
#define dmp_libc_name_list_list_msize_dec(sz) \
      libc_name_list_list_curr_msize -= sz;\
      libc_name_list_list_lcurr_msize -= sz
struct libc_name_list_list *new_libc_name_list_list(void)
{ struct libc_name_list_list *p = (struct libc_name_list_list *)
get_mb_ptr(2);
#if DMP MEM ANY
  dmp_libc_mem_size_inc(12);
  dmp_libc_name_list_list_msize_inc(12);
#endif
  return(p);
#define dmp_libc_float_list_rec_msize_inc(sz)
    libc_float_list_rec_lcurr_msize += sz; \
    if (libc_float_list_rec_lcurr_msize>libc_float_list_rec_lpeak_msize) \
    libc_float_list_rec_lpeak_msize = libc_float_list_rec_lcurr_msize;\
    libc_float_list_rec_curr msize += sz; \
```

```
if (libc_float_list_rec_curr_msize>libc_float_list_rec_peak_msize) \
     libc_float_list_rec_peak_msize = libc_float_list_rec_curr_msize;
 #define dmp_libc_float_list_rec_msize_dec(sz) \
       libc_float_list_rec_curr msize -= sz;\
       libc_float_list_rec_lcurr_msize -= sz
 struct libc_float_list_rec *new_libc_float_list_rec(void)
 { struct libc_float_list_rec *p = (struct libc_float_list_rec *)
 get mb ptr(1);
 #if DMP MEM ANY
   dmp_libc_mem_size inc(8);
   dmp_libc_float_list_rec_msize_inc(8);
 #endif
   return(p);
 #define dmp_libc_float_list_list_msize inc(sz)
     libc_float_list_list_lcurr_msize += sz; \
     if (libc_float_list_list_lcurr_msize>libc_float_list_list_lpeak_msize) \
     libc_float_list_list_lpeak_msize = libc_float_list_list_lcurr_msize;\
     libc_float_list_list_curr_msize += sz; \land
    if (libc_float_list_list_curr_msize>libc_float_list_list_peak_msize) \
    libc_float_list_list_peak_msize = libc_float_list_list_curr_msize;
 #define dmp_libc_float_list_list_msize_dec(sz) \
       libc_float_list_list_curr_msize -= sz;\
       libc_float_list list lcurr msize -= sz
struct libc_float_list_list *new_libc_float_list_list(void)
{ struct libc_float_list_list *p = (struct libc_float_list_list *)
get_mb_ptr(1);
#if DMP MEM ANY
  dmp_libc_mem_size_inc(8);
  dmp_libc_float_list_list_msize_inc(8);
#endif
  return(p);
#define dmp_libc_define_rec_msize_inc(sz) \
    libc_define_rec_lcurr_msize += sz; \
    if (libc_define_rec_lcurr_msize>libc_define_rec_lpeak_msize) \
    libc_define_rec_lpeak_msize = libc_define_rec_lcurr_msize;\
    libc_define_rec_curr_msize += sz; \
    if (libc_define_rec_curr_msize>libc_define_rec_peak_msize) \
    libc_define_rec_peak_msize = libc_define_rec_curr_msize;
#define dmp_libc_define rec_msize_dec(sz) \
      libc_define_rec_curr_msize -= sz;\
      libc_define_rec_lcurr_msize -= sz
struct libc_define_rec *new_libc_define rec(void)
{ struct libc_define_rec *p = (struct libc_define_rec *) get_mb_ptr(3);
#if DMP MEM ANY
  dmp_libc_mem_size_inc(16);
  dmp_libc_define_rec_msize_inc(16);
#endif
  return(p);
#define dmp_libc_cell_area_rec_msize_inc(sz)
    libc_cell_area_rec_lcurr_msize += sz; \
    if (libc_cell_area_rec_lcurr_msize>libc_cell_area_rec_lpeak_msize) \
```

```
libc_cell_area_rec_lpeak_msize = libc_cell_area_rec_lcurr_msize;\
     libc_cell_area_rec_curr_msize += sz; \
     if (libc_cell_area_rec_curr_msize>libc_cell_area_rec_peak_msize) \
     libc_cell_area_rec_peak_msize = libc_cell_area_rec_curr_msize;
 #define dmp_libc_cell_area_rec_msize_dec(sz) \
       libc_cell_area_rec_curr_msize -= sz;\
       libc_cell_area_rec_lcurr_msize -= sz
 struct libc_cell_area_rec *new_libc_cell_area_rec(void)
 { struct libc_cell_area_rec *p = (struct libc_cell_area_rec *) get_mb_ptr(2);
 #if DMP MEM ANY
   dmp_libc_mem_size_inc(12);
   dmp_libc_cell_area_rec_msize inc(12);
 #endif
   return(p);
 #define dmp_libc_k_factor_rec_msize_inc(sz)
     libc_k_factor_rec_lcurr_msize += sz; \
     if (libc_k_factor_rec_lcurr_msize>libc_k_factor_rec_lpeak_msize) \
     libc_k_factor_rec_lpeak_msize = libc_k_factor_rec_lcurr_msize;\
     libc_k_factor_rec_curr_msize += sz; \
    if (libc_k_factor_rec_curr_msize>libc_k_factor_rec_peak_msize) \
    libc_k_factor_rec_peak_msize = libc_k_factor_rec_curr_msize;
#define dmp_libc_k_factor_rec_msize_dec(sz) \
       libc_k_factor_rec_curr_msize -= sz;\
       libc_k_factor_rec_lcurr_msize -= sz
struct libc_k_factor_rec *new_libc_k_factor_rec(void)
{ struct libc_k_factor_rec *p = (struct libc_k_factor_rec *) get_mb_ptr(29);
#if DMP MEM ANY
  dmp_libc_mem_size_inc(548);
  dmp_libc_k_factor_rec_msize_inc(548);
#endif
  return(p);
#define dmp_libc_lib_rec_msize_inc(sz) \
    libc lib rec_lcurr_msize += sz; \
    if (libc_lib_rec_lcurr_msize>libc_lib_rec_lpeak_msize) \
    libc_lib_rec_lpeak_msize = libc_lib_rec_lcurr msize;\
    libc_lib_rec_curr_msize += sz; \
    if (libc_lib_rec_curr_msize>libc_lib_rec_peak_msize) \
    libc_lib_rec_peak_msize = libc_lib_rec_curr_msize;
#define dmp_libc_lib_rec_msize_dec(sz) \
      libc_lib_rec_curr_msize -= sz;\
      libc_lib_rec_lcurr_msize -= sz
struct libc_lib_rec *new_libc_lib_rec(void)
{ struct libc_lib_rec *p = (struct libc_lib_rec *) get_mb_ptr(28);
#if DMP MEM ANY
  dmp_libc_mem_size inc(392);
  dmp_libc_lib_rec_msize_inc(392);
#endif
  return(p);
#define dmp_libc_lu_table_template_rec_msize_inc(sz)
    libc_lu_table_template_rec_lcurr_msize += sz; \
```

```
if
(libc lu table template rec lcurr msize>libc lu table template rec lpeak msiz
    libc_lu_table_template_rec_lpeak_msize =
libc lu table template rec lcurr msize;\
    libc lu table template rec curr msize += sz; \
(libc_lu_table_template_rec_curr_msize>libc lu table template rec peak msize)
    libc lu table template rec peak msize =
libc lu table template rec curr msize;
#define dmp libc lu table template rec msize dec(sz) \
      libc lu table template rec curr msize -= sz;\
      libc_lu_table_template_rec_lcurr_msize -= sz
struct libc_lu_table_template_rec *new libc lu table template rec(void)
{ struct libc_lu_table_template_rec *p = (struct libc_lu_table_template_rec
*) get mb ptr(7);
#if DMP MEM ANY
  dmp libc mem size inc(32);
  dmp libc lu table template rec msize inc(32);
#endif
  return(p);
#define dmp libc oc power rail rec msize inc(sz)
    libc oc power rail rec lcurr msize += sz; \
    if
(libc_oc_power_rail_rec_lcurr_msize>libc_oc_power_rail_rec_lpeak_msize) \
    libc_oc_power_rail_rec_lpeak_msize = libc_oc_power_rail_rec_lcurr_msize;\
    libc_oc_power_rail_rec_curr_msize += sz; \
    if (libc_oc_power_rail_rec_curr_msize>libc_oc_power_rail_rec_peak_msize)
    libc_oc_power_rail_rec_peak_msize = libc_oc_power_rail_rec_curr_msize;
#define dmp libc oc_power_rail_rec_msize_dec(sz) \
      libc_oc_power_rail_rec curr msize -= sz;\
      libc_oc_power rail rec lcurr msize -= sz
struct libc_oc_power_rail_rec *new_libc_oc_power_rail_rec(void)
{ struct libc_oc_power rail_rec *p = (struct libc_oc_power_rail rec *)
get mb ptr(2);
#if DMP MEM ANY
  dmp libc mem size inc(12);
  dmp libc oc power rail rec msize inc(12);
#endif
 return(p);
#define dmp libc operating condition rec msize inc(sz)
    libc operating condition rec lcurr msize += sz; \
(libc_operating_condition rec lcurr msize>libc operating condition rec lpeak
msize) \
    libc operating condition rec lpeak msize =
libc_operating_condition rec lcurr msize;\
    libc operating condition rec curr msize += sz; \
(libc_operating_condition_rec_curr_msize>libc_operating_condition_rec_peak_ms
ize) \
```

```
libc_operating_condition_rec_peak_msize =
 libc_operating_condition_rec_curr_msize;
 #define dmp_libc_operating_condition_rec_msize_dec(sz) \
       libc_operating_condition_rec_curr_msize -= sz;\
       libc_operating_condition_rec_lcurr_msize -= sz
 struct libc_operating_condition_rec *new_libc_operating_condition_rec(void)
 { struct libc_operating_condition_rec *p = (struct
 libc_operating_condition_rec *) get_mb_ptr(6);
 #if DMP MEM ANY
   dmp_libc_mem_size_inc(28);
  dmp_libc_operating_condition_rec_msize_inc(28);
 #endif
  return(p);
 #define dmp_libc_power_supply_rec_msize_inc(sz)
     libc_power_supply_rec_lcurr_msize += sz; \
    if (libc_power_supply_rec_lcurr_msize>libc_power_supply_rec_lpeak_msize)
    libc_power_supply_rec_lpeak_msize = libc_power_supply_rec_lcurr_msize;\
    libc_power_supply_rec_curr_msize += sz; \
    if (libc_power_supply_rec_curr_msize>libc_power_supply_rec_peak_msize) \
    libc_power_supply_rec_peak_msize = libc_power_supply_rec_curr_msize;
#define dmp_libc_power_supply_rec_msize_dec(sz) \
      libc_power_supply_rec_curr_msize -= sz;\
      libc_power_supply_rec_lcurr_msize -= sz
struct libc_power_supply_rec *new_libc_power_supply_rec(void)
{ struct libc_power_supply_rec *p = (struct libc_power_supply_rec *)
get mb ptr(3);
#if DMP MEM ANY
  dmp_libc_mem_size_inc(16);
  dmp_libc_power_supply_rec_msize_inc(16);
#endif
  return(p);
#define dmp_libc_timing_range_rec_msize_inc(sz)
    libc_timing_range_rec_lcurr_msize += sz; \
    if (libc_timing_range_rec_lcurr_msize>libc_timing_range_rec_lpeak_msize)
    libc_timing_range_rec_lpeak_msize = libc_timing_range_rec_lcurr_msize;\
    libc_timing_range_rec_curr_msize += sz; \
    if (libc_timing_range_rec_curr_msize>libc_timing_range_rec_peak_msize) \
    libc_timing_range_rec_peak_msize = libc_timing_range_rec_curr_msize;
#define dmp_libc_timing_range_rec_msize_dec(sz) \
      libc_timing_range_rec_curr_msize -= sz;\
      libc_timing_range_rec_lcurr_msize -= sz
struct libc_timing_range_rec *new_libc_timing_range_rec(void)
{ struct libc_timing_range_rec *p = (struct libc_timing_range_rec *)
get mb ptr(3);
#if DMP MEM ANY
  dmp_libc_mem_size_inc(16);
 dmp_libc_timing_range_rec_msize_inc(16);
#endif
 return(p);
```

```
#define dmp_libc_type_rec_msize_inc(sz) \
     libc_type_rec_lcurr_msize += sz; \
     if (libc_type_rec_lcurr_msize>libc_type_rec_lpeak_msize) \
     libc_type_rec_lpeak_msize = libc_type_rec_lcurr_msize;\
     libc_type_rec_curr_msize += sz; \
     if (libc_type_rec_curr_msize>libc_type_rec_peak_msize) \
     libc_type_rec_peak_msize = libc_type rec curr msize;
 #define dmp_libc_type_rec_msize_dec(sz) \
       libc_type_rec_curr_msize -= sz;\
       libc_type_rec_lcurr_msize -= sz
 struct libc_type_rec *new_libc_type_rec(void)
 { struct libc_type_rec *p = (struct libc_type_rec *) get_mb_ptr(5);
 #if DMP MEM ANY
  dmp_libc_mem_size_inc(24);
  dmp_libc_type_rec_msize_inc(24);
 #endif
  return(p);
#define dmp_libc_fanout_length_rec_msize_inc(sz) \
    libc fanout_length rec_lcurr_msize += sz; \
 (libc_fanout_length_rec_lcurr_msize>libc_fanout_length_rec_lpeak_msize) \
    libc_fanout_length_rec_lpeak_msize = libc_fanout_length_rec_lcurr_msize;\
    libc_fanout_length_rec_curr_msize += sz; \
    if (libc_fanout_length_rec_curr_msize>libc_fanout_length_rec_peak_msize)
    libc_fanout_length_rec_peak_msize = libc_fanout_length_rec_curr_msize;
#define dmp_libc_fanout_length_rec_msize_dec(sz) \
      libc_fanout_length_rec_curr_msize -= sz;\
      libc_fanout_length_rec_lcurr_msize -= sz
struct libc_fanout_length_rec *new_libc_fanout_length_rec(void)
{ struct libc_fanout_length_rec *p = (struct libc_fanout_length_rec *)
get_mb_ptr(8);
#if DMP MEM ANY
  dmp_libc mem size inc(36);
  dmp_libc_fanout_length_rec_msize_inc(36);
#endif
  return(p);
#define dmp_libc_wire_load_rec_msize_inc(sz) \
    libc wire load_rec_lcurr_msize += sz; \
    if (libc_wire_load_rec_lcurr_msize>libc_wire_load_rec_lpeak_msize) \
    libc_wire_load_rec_lpeak_msize = libc_wire_load_rec_lcurr_msize;\
    libc_wire_load_rec_curr_msize += sz; \
    if (libc_wire_load_rec_curr_msize>libc_wire_load_rec_peak_msize) \
    libc_wire_load_rec_peak_msize = libc_wire_load_rec_curr_msize;
#define dmp_libc_wire_load_rec_msize_dec(sz) \
      libc_wire_load_rec_curr_msize -= sz;\
      libc_wire_load_rec_lcurr_msize -= sz
struct libc_wire_load_rec *new_libc_wire_load_rec(void)
{ struct libc_wire_load_rec *p = (struct libc_wire_load_rec *) get_mb_ptr(6);
#if DMP MEM ANY
  dmp_libc_mem_size_inc(28);
  dmp_libc_wire_load_rec_msize_inc(28);
#endif
```

```
return(p);
 #define dmp_libc_wire_load_from_area_rec_msize_inc(sz)
     libc_wire_load_from_area_rec_lcurr_msize += sz; \
 (libc_wire_load_from_area_rec_lcurr_msize>libc_wire_load_from_area_rec_lpeak_
 msize) \
     libc_wire_load_from_area_rec_lpeak_msize =
 libc_wire_load_from_area_rec_lcurr_msize;\
     libc_wire_load_from_area_rec_curr_msize += sz; \
     if
 (libc_wire_load_from_area_rec_curr_msize>libc_wire_load_from_area_rec_peak_ms
     libc_wire_load_from_area_rec_peak_msize =
 libc_wire_load_from_area_rec_curr_msize;
 #define dmp_libc_wire_load_from_area_rec_msize_dec(sz) \
       libc_wire_load_from_area_rec_curr_msize -= sz;\
       libc_wire_load_from_area_rec_lcurr_msize -= sz
struct libc_wire_load_from_area_rec *new_libc_wire_load_from_area_rec(void)
 { struct libc_wire_load_from_area_rec *p = (struct
libc_wire_load_from_area_rec *) get_mb_ptr(3);
#if DMP MEM ANY
  dmp_libc_mem_size_inc(16);
  dmp_libc_wire_load_from_area_rec_msize_inc(16);
#endif
  return(p);
#define dmp_libc_wire_load_selection_rec_msize_inc(sz)
    libc_wire_load_selection_rec_lcurr_msize += sz; \
(libc_wire_load_selection_rec_lcurr_msize>libc_wire_load_selection_rec_lpeak_
msize) \
    libc_wire_load_selection_rec_lpeak_msize =
libc_wire_load_selection_rec_lcurr_msize;\
    libc_wire_load_selection_rec_curr_msize += sz; \
(libc_wire_load_selection_rec_curr_msize>libc_wire_load_selection_rec_peak_ms
ize) \
    libc_wire_load_selection_rec_peak_msize =
libc_wire_load_selection_rec_curr_msize;
#define dmp_libc_wire_load_selection_rec_msize_dec(sz) \
      libc_wire_load_selection_rec_curr_msize -= sz;\
      libc_wire_load_selection_rec_lcurr_msize -= sz
struct libc_wire_load_selection_rec *new_libc_wire_load_selection_rec(void)
{ struct libc_wire_load_selection_rec *p = (struct
libc_wire_load_selection_rec *) get_mb_ptr(2);
#if DMP MEM ANY
  dmp_libc_mem_size_inc(12);
  dmp_libc_wire_load_selection_rec_msize_inc(12);
#endif
  return(p);
#define dmp_libc_cell_rec_msize_inc(sz)
    libc_cell_rec_lcurr_msize += sz; \
```

```
if (libc_cell_rec_lcurr_msize>libc_cell_rec_lpeak_msize) \
    libc_cell_rec_lpeak_msize = libc_cell_rec_lcurr_msize;\
    libc_cell_rec_curr_msize += sz; \
    if (libc cell rec curr msize>libc cell rec peak msize) \
    libc cell rec peak msize = libc cell rec curr msize;
#define dmp libc cell rec msize dec(sz) \
      libc cell rec curr msize -= sz;\
      libc cell rec_lcurr msize -= sz
struct libc cell rec *new libc cell rec(void)
{ struct libc cell rec *p = (struct libc cell rec *) get mb ptr(23);
#if DMP MEM ANY
  dmp libc mem size inc(132);
  dmp libc cell rec msize inc(132);
#endif
  return(p);
#define dmp libc memory write rec msize inc(sz)
    libc_memory_write_rec_lcurr_msize += sz; \
    if (libc memory_write_rec_lcurr_msize>libc memory_write_rec_lpeak_msize)
\
    libc memory_write_rec_lpeak_msize = libc memory_write_rec_lcurr_msize;\
    libc_memory_write_rec_curr_msize += sz; \
    if (libc_memory_write_rec_curr_msize>libc memory write rec peak msize) \
    libc memory_write_rec_peak_msize = libc memory_write_rec_curr_msize;
#define dmp libc memory write rec msize dec(sz) \
      libc_memory_write_rec_curr_msize -= sz;\
      libc_memory_write_rec_lcurr_msize -= sz
struct libc_memory_write_rec *new_libc_memory_write_rec(void)
{ struct libc_memory_write_rec *p = (struct libc_memory_write_rec *)
get mb ptr(2);
#if DMP MEM ANY
  dmp libc mem_size_inc(12);
  dmp libc memory write rec msize inc(12);
#endif
 return(p);
#define dmp libc bool opr rec msize inc(sz) \
    libc bool opr rec lcurr msize += sz; \
    if (libc bool opr rec lcurr msize>libc bool opr rec lpeak msize) \
    libc_bool_opr_rec_lpeak_msize = libc_bool_opr_rec_lcurr_msize;\
    libc_bool_opr_rec_curr_msize += sz; \
    if (libc_bool_opr_rec_curr_msize>libc bool opr_rec_peak_msize) \
    libc_bool_opr_rec_peak_msize = libc_bool_opr_rec_curr_msize;
#define dmp_libc_bool_opr_rec_msize_dec(sz) \
      libc_bool_opr_rec_curr_msize -= sz;\
      libc_bool_opr_rec_lcurr_msize -= sz
struct libc bool opr rec *new libc bool opr rec(void)
{ struct libc_bool_opr_rec *p = (struct libc_bool_opr_rec *) get mb ptr(3);
#if DMP MEM ANY
 dmp_libc_mem_size_inc(16);
  dmp_libc_bool_opr_rec msize inc(16);
#endif
 return(p);
```

```
#define dmp_libc_pin_rec_msize_inc(sz)
     libc pin_rec_lcurr_msize += sz; \
     if (libc_pin_rec_lcurr_msize>libc_pin_rec_lpeak_msize) \
     libc_pin_rec_lpeak_msize = libc_pin_rec_lcurr msize;\
     libc_pin_rec_curr_msize += sz; \
     if (libc_pin_rec_curr_msize>libc_pin_rec_peak_msize) \
     libc_pin_rec_peak_msize = libc_pin_rec_curr_msize;
 #define dmp_libc_pin_rec_msize_dec(sz) \
      libc_pin_rec_curr_msize -= sz;\
      libc_pin_rec_lcurr_msize -= sz
struct libc_pin_rec *new_libc_pin_rec(void)
{ struct libc_pin_rec *p = (struct libc_pin_rec *) get_mb_ptr(27);
#if DMP MEM ANY
  dmp_libc_mem_size_inc(276);
  dmp_libc_pin_rec_msize_inc(276);
#endif
  return(p);
#define dmp_libc_ff_latch_rec_msize_inc(sz) \
    libc_ff_latch_rec_lcurr_msize += sz; \
    if (libc_ff_latch_rec_lcurr_msize>libc_ff_latch_rec_lpeak_msize) \
    libc_ff_latch_rec_lpeak_msize = libc_ff_latch_rec_lcurr_msize;\
    libc_ff_latch_rec_curr_msize += sz; \
    if (libc_ff_latch_rec_curr_msize>libc_ff_latch_rec_peak_msize) \
    libc_ff_latch_rec_peak_msize = libc_ff_latch_rec_curr_msize;
#define dmp_libc_ff_latch_rec_msize_dec(sz) \
      libc_ff_latch_rec_curr_msize -= sz;\
      libc_ff_latch_rec_lcurr_msize -= sz
struct libc_ff_latch_rec *new_libc_ff_latch_rec(void)
{ struct libc_ff_latch_rec *p = (struct libc_ff_latch_rec *) get mb_ptr(15);
#if DMP_MEM_ANY
  dmp_libc_mem_size_inc(64);
  dmp_libc_ff_latch_rec_msize_inc(64);
#endif
  return(p);
#define dmp_libc_internal_power_msize_inc(sz)
    libc_internal_power_lcurr_msize += sz; \
    if (libc_internal_power_lcurr_msize>libc_internal_power_lpeak_msize) \
    libc_internal_power_lpeak_msize = libc_internal_power_lcurr_msize;\
    libc_internal_power_curr_msize += sz; \
    if (libc_internal_power_curr_msize>libc_internal_power_peak_msize) \
    libc_internal_power_peak_msize = libc_internal power curr_msize;
#define dmp_libc_internal_power_msize_dec(sz) \
      libc_internal_power_curr_msize -= sz;\
      libc_internal_power_lcurr_msize -= sz
struct libc_internal_power *new_libc_internal_power(void)
{ struct libc_internal_power *p = (struct libc_internal_power *)
get_mb_ptr(9);
#if DMP MEM ANY
 dmp_libc mem size inc(40);
 dmp_libc_internal_power_msize_inc(40);
#endif
 return(p);
```

```
#define dmp_libc_leakage_power_msize_inc(sz)
     libc_leakage_power_lcurr_msize += sz; \
     if (libc_leakage_power_lcurr_msize>libc_leakage_power_lpeak msize) \
     libc_leakage_power_lpeak_msize = libc_leakage_power_lcurr_msize;\
     libc leakage power curr msize += sz; \
     if (libc_leakage_power_curr_msize>libc_leakage_power_peak msize) \
     libc_leakage_power_peak_msize = libc_leakage_power_curr_msize;
 #define dmp_libc_leakage_power_msize_dec(sz) \
       libc_leakage_power_curr_msize -= sz;\
       libc_leakage_power_lcurr_msize -= sz
struct libc leakage_power *new_libc_leakage_power(void)
 { struct libc_leakage_power *p = (struct libc_leakage_power *) get_mb_ptr(2);
#if DMP MEM ANY
  dmp_libc_mem size inc(12);
  dmp_libc_leakage_power_msize_inc(12);
#endif
  return(p);
#define dmp_libc_memory_rec_msize_inc(sz)
    libc_memory_rec_lcurr msize += sz; \
    if (libc_memory_rec_lcurr_msize>libc_memory_rec lpeak msize) \
    libc_memory_rec_lpeak_msize = libc_memory_rec_lcurr_msize;\
    libc_memory_rec_curr_msize += sz; \
    if (libc_memory_rec_curr_msize>libc_memory_rec_peak_msize) \
    libc_memory_rec_peak_msize = libc_memory_rec_curr_msize;
#define dmp_libc_memory_rec_msize_dec(sz) \
      libc_memory_rec_curr_msize -= sz;\
      libc_memory_rec_lcurr_msize -= sz
struct libc_memory_rec *new_libc_memory_rec(void)
{ struct libc_memory_rec *p = (struct libc_memory_rec *) get mb_ptr(2);
#if DMP_MEM_ANY
  dmp libc mem size inc(12);
  dmp_libc_memory_rec_msize_inc(12);
#endif
  return(p);
#define dmp_libc_piece_value rec_msize inc(sz)
    libc piece value rec_lcurr_msize += sz; \
    if (libc_piece_value_rec_lcurr_msize>libc_piece_value_rec_lpeak_msize) \
    libc_piece_value_rec_lpeak_msize = libc_piece_value_rec_lcurr_msize;\
    libc_piece_value_rec_curr_msize += sz; \
    if (libc_piece_value_rec_curr_msize>libc_piece_value_rec_peak_msize) \
    libc_piece_value_rec_peak_msize = libc_piece_value_rec_curr_msize;
#define dmp_libc_piece_value_rec_msize_dec(sz) \
      libc_piece_value_rec_curr_msize -= sz;\
      libc_piece_value_rec_lcurr_msize -= sz
struct libc_piece_value_rec *new_libc_piece_value_rec(void)
{ struct libc_piece_value_rec *p = (struct libc_piece_value rec *)
get_mb ptr(9);
#if DMP_MEM_ANY
  dmp_libc_mem_size_inc(40);
  dmp_libc_piece_value rec msize inc(40);
#endif
 return(p);
```

libc mem.c

```
#define dmp_libc_float_rec_msize_inc(sz)
     libc_float_rec_lcurr_msize += sz; \
     if (libc_float_rec_lcurr_msize>libc_float_rec_lpeak_msize) \
     libc_float_rec_lpeak_msize = libc_float_rec_lcurr_msize;\
     libc_float_rec_curr_msize += sz; \
     if (libc_float_rec_curr_msize>libc_float_rec_peak_msize) \
     libc_float_rec_peak_msize = libc_float_rec_curr_msize;
 #define dmp_libc_float_rec_msize_dec(sz) \
       libc_float_rec_curr_msize -= sz;\
       libc_float_rec_lcurr_msize -= sz
 struct libc_float_rec *new_libc_float_rec(void)
 { struct libc_float_rec *p = (struct libc_float_rec *) get_mb_ptr(0);
 #if DMP_MEM ANY
  dmp_libc_mem_size_inc(4);
  dmp_libc_float_rec_msize_inc(4);
#endif
  return(p);
#define dmp_libc_table_val_rec_msize_inc(sz) \
    libc_table_val_rec_lcurr_msize += sz; \
    if (libc_table_val_rec_lcurr_msize>libc_table_val_rec_lpeak_msize) \
    libc_table_val_rec_lpeak_msize = libc_table_val_rec_lcurr_msize;\
    libc_table_val_rec_curr_msize += sz; \
    if (libc_table_val_rec_curr_msize>libc_table_val_rec_peak_msize) \
    libc_table_val_rec_peak_msize = libc_table_val_rec_curr_msize;
#define dmp_libc_table_val_rec_msize_dec(sz) \
      libc_table_val_rec_curr_msize -= sz;\
      libc_table_val_rec_lcurr_msize -= sz
struct libc_table_val_rec *new_libc_table_val rec(void)
{ struct libc_table_val_rec *p = (struct libc_table_val_rec *) get_mb_ptr(5);
#if DMP_MEM_ANY
  dmp libc mem size inc(24);
  dmp_libc_table_val_rec_msize_inc(24);
#endif
  return(p);
#define dmp_libc_timing_rec_msize_inc(sz)
    libc_timing_rec_lcurr_msize += sz; \
    if (libc_timing_rec_lcurr_msize>libc_timing_rec_lpeak_msize) \
    libc_timing_rec_lpeak_msize = libc_timing_rec_lcurr_msize;\
    libc_timing_rec_curr_msize += sz; \
    if (libc_timing_rec_curr_msize>libc_timing_rec_peak_msize) \
    libc_timing_rec_peak_msize = libc_timing_rec_curr_msize;
#define dmp libc_timing_rec msize_dec(sz) \
      libc_timing_rec_curr_msize -= sz;\
      libc_timing_rec_lcurr_msize -= sz
struct libc_timing_rec *new_libc_timing_rec(void)
{ struct libc_timing_rec *p = (struct libc_timing_rec *) get_mb_ptr(22);
#if DMP_MEM_ANY
 dmp_libc_mem_size_inc(120);
  dmp_libc_timing_rec_msize_inc(120);
#endif
 return(p);
```

libc mem.c

```
}
#define dmp libc min pulse width rec msize inc(sz) \
    libc_min_pulse_width_rec_lcurr_msize += sz; \
(libc min pulse width rec lcurr msize>libc min pulse width rec lpeak msize) \
    libc min pulse width rec lpeak msize =
libc_min_pulse_width_rec_lcurr_msize;\
    libc_min_pulse_width rec curr msize += sz; \
(libc_min_pulse_width_rec_curr_msize>libc min_pulse_width_rec_peak_msize) \
    libc min pulse width rec peak msize =
libc min pulse width rec curr msize;
#define dmp_libc_min_pulse_width_rec_msize_dec(sz) \
      libc_min_pulse_width_rec_curr_msize -= sz;\
      libc_min_pulse_width_rec_lcurr_msize -= sz
struct libc_min_pulse_width_rec *new_libc_min_pulse_width_rec(void)
{ struct libc_min_pulse_width_rec *p = (struct libc min pulse width rec *)
get_mb_ptr(3);
#if DMP MEM ANY
  dmp_libc_mem_size_inc(16);
  dmp_libc_min_pulse_width_rec msize inc(16);
#endif
 return(p);
#define dmp libc minimum period rec msize inc(sz)
    libc minimum period rec lcurr msize += sz; \
(libc minimum period_rec lcurr msize>libc minimum period rec lpeak msize) \
    libc minimum period rec lpeak msize =
libc_minimum_period_rec_lcurr_msize;\
    libc_minimum_period_rec_curr_msize += sz; \
(libc_minimum_period_rec_curr_msize>libc_minimum_period_rec_peak_msize) \
    libc_minimum_period_rec_peak_msize = libc_minimum_period rec curr msize;
#define dmp_libc_minimum_period rec msize dec(sz) \
      libc minimum period rec curr msize -= sz;\
      libc minimum period rec lcurr msize -= sz
struct libc_minimum_period_rec *new_libc_minimum_period_rec(void)
{ struct libc_minimum_period rec *p = (struct libc minimum period rec *)
get mb ptr(2);
#if DMP_MEM_ANY
 dmp_libc_mem_size_inc(12);
  dmp_libc_minimum_period_rec_msize_inc(12);
#endif
 return(p);
#define dmp_libc_routing_track_rec_msize_inc(sz)
    libc_routing_track_rec_lcurr_msize += sz; \
(libc_routing_track_rec_lcurr_msize>libc_routing_track_rec_lpeak_msize) \
   libc routing track rec lpeak msize = libc routing track rec lcurr msize;\
   libc routing track rec curr msize += sz; \
    if (libc_routing_track_rec_curr_msize>libc_routing_track_rec_peak_msize)
\
```

```
libc_routing_track_rec_peak_msize = libc_routing_track_rec_curr_msize;
#define dmp_libc_routing_track_rec_msize dec(sz) \
      libc_routing_track_rec_curr_msize -= sz;\
      libc_routing_track_rec_lcurr_msize -= sz
struct libc_routing_track_rec *new_libc_routing_track_rec(void)
{ struct libc_routing_track_rec *p = (struct libc routing track rec *)
get mb ptr(3);
#if DMP MEM ANY
  dmp libc mem size inc(16);
  dmp_libc_routing_track_rec_msize_inc(16);
#endif
  return(p);
#define dmp libc def entry rec msize inc(sz)
    libc_def_entry_rec_lcurr_msize += sz; \
    if (libc_def_entry_rec_lcurr_msize>libc_def_entry rec_lpeak msize) \
    libc_def_entry_rec_lpeak_msize = libc_def entry rec_lcurr_msize;\
    libc_def_entry_rec_curr_msize += sz; \
    if (libc_def_entry_rec_curr_msize>libc_def_entry_rec_peak_msize) \
    libc_def_entry_rec_peak_msize = libc_def_entry_rec_curr_msize;
#define dmp libc def entry rec msize dec(sz) \
      libc def entry rec curr msize -= sz;\
      libc def entry rec lcurr msize -= sz
struct libc_def_entry_rec *new libc def entry_rec(void)
{ struct libc_def_entry_rec *p = (struct libc_def_entry_rec *) get_mb_ptr(2);
#if DMP MEM ANY
  dmp_libc_mem_size inc(12);
  dmp_libc_def_entry_rec msize inc(12);
#endif
  return(p);
#define dmp_libc_define_value_rec_msize_inc(sz)
    libc define value rec lcurr msize += sz; \
    if (libc_define_value_rec_lcurr_msize>libc_define value rec_lpeak_msize)
    libc_define_value_rec_lpeak_msize = libc_define_value_rec_lcurr_msize;\
    libc define value rec curr msize += sz; \
    if (libc_define_value_rec_curr_msize>libc_define_value rec peak msize) \
    libc define_value_rec_peak_msize = libc_define_value_rec_curr_msize;
#define dmp_libc_define_value_rec msize dec(sz) \
      libc_define_value_rec_curr_msize -= sz;\
      libc_define_value_rec_lcurr_msize -= sz
struct libc_define_value_rec *new_libc_define_value_rec(void)
{ struct libc_define_value_rec *p = (struct libc define value rec *)
get_mb_ptr(3);
#if DMP MEM ANY
 dmp libc mem size inc(16);
  dmp_libc_define_value_rec msize inc(16);
#endif
 return(p);
#define dmp_libc_glb_const_rec msize inc(sz)
    libc_glb_const_rec_lcurr msize += sz; \
    if (libc_glb_const_rec_lcurr_msize>libc_glb_const_rec_lpeak_msize) \
```

```
libc_glb_const_rec_lpeak_msize = libc_glb_const_rec_lcurr_msize;\
    libc_glb_const_rec_curr_msize += sz; \
    if (libc_glb_const_rec_curr_msize>libc_glb_const_rec_peak_msize) \
    libc_glb_const_rec_peak_msize = libc_glb_const_rec_curr_msize;
 #define dmp_libc_glb_const_rec_msize_dec(sz) \
      libc_glb_const_rec_curr_msize -= sz;\
      libc glb const_rec_lcurr_msize -= sz
struct libc_glb_const_rec *new_libc_glb_const_rec(void)
 { struct libc_glb_const_rec *p = (struct libc_glb_const_rec *) get_mb_ptr(2);
#if DMP_MEM_ANY
  dmp_libc_mem_size_inc(12);
  dmp_libc_glb_const_rec_msize_inc(12);
  return(p);
#define dmp_libc_def_table_rec_msize_inc(sz)
    libc_def_table_rec_lcurr_msize += sz; \
    if (libc_def_table_rec_lcurr_msize>libc_def_table_rec_lpeak_msize) \
    libc_def_table_rec_lpeak_msize = libc_def_table_rec_lcurr_msize;\
    libc_def_table_rec_curr_msize += sz; \
    if (libc_def_table_rec_curr_msize>libc_def_table_rec_peak_msize) \
    libc_def_table_rec_peak_msize = libc_def_table_rec_curr_msize;
#define dmp libc_def_table_rec msize_dec(sz) \
      libc def_table_rec_curr_msize -= sz;\
      libc_def_table_rec_lcurr_msize -= sz
struct libc_def_table_rec *new_libc_def_table_rec(void)
{ struct libc_def_table_rec *p = (struct libc_def_table_rec *) get_mb_ptr(3);
#if DMP MEM ANY
  dmp_libc_mem_size_inc(16);
  dmp_libc_def_table_rec_msize_inc(16);
#endif
  return(p);
#endif
routine: free_any unit rec()
   void free_any_unit_rec(struct any_unit_rec *ptr)
  if (ptr==NULL) return;
   free mb_ptr((void *) ptr,1);
#if DMP MEM_ANY
   dmp_any_unit_rec_msize_dec(8);
   dmp_libc mem size dec(8);
#endif
routine: nfree_any_unit_rec()
  void nfree_any_unit_rec(struct any_unit_rec **pptr)
{ struct any_unit_rec *ptr= *pptr;
```

```
if (ptr==NULL) return;
    free_mb_ptr((void *) ptr,1);
 #if DMP_MEM_ANY
    dmp_any_unit_rec_msize_dec(8);
    dmp_libc_mem size dec(8);
 #endif
  *pptr = NULL;
routine: free_libc_name_list_rec()
   void free_libc_name_list_rec(struct libc_name_list_rec *ptr)
  struct libc_name_list_rec *next;
  while (ptr!=NULL) {
    free_text_buffer(ptr->name);
    next = ptr->next ;
    free_mb_ptr((void *) ptr,1);
#if DMP_MEM ANY
    dmp_libc_name_list_rec_msize_dec(8);
    dmp_libc_mem_size_dec(8);
#endif
    ptr = next ;
routine: nfree_libc_name_list_rec()
   void nfree_libc_name_list_rec(struct libc_name_list_rec **pptr)
{ struct libc_name_list_rec *ptr= *pptr;
  struct libc_name_list_rec *next;
  while (ptr!=NULL) {
   free_text_buffer(ptr->name);
    next = ptr->next ;
   free_mb_ptr((void *) ptr,1);
#if DMP MEM ANY
   dmp_libc_name_list_rec_msize_dec(8);
   dmp_libc_mem_size_dec(8);
#endif
   ptr = next ;
  *pptr = NULL;
struct libc_name_list_rec * copy_libc_name_list_rec(struct libc_name_list_rec
*sou)
{ struct libc_name_list_rec *tar, *head, *prev;
 if (sou==NULL) return(NULL);
 prev = NULL;
```

```
head = tar = new_libc_name_list_rec();
 while (sou!=NULL) {
   tar->name
                = (char *) copy string(sou->name);
   sou = sou->next;
   if (sou!=NULL)
   { prev = tar;
    tar->next = new libc name list rec();
     tar = tar->next;
 }
   return (head);
}
routine: free_libc_name_list_list()
  void free libc name list list(struct libc name list list *ptr)
 struct libc_name_list_list *next;
 while (ptr!=NULL) {
   free libc_name_list_rec(ptr-> name_list1);
   free_libc_name_list_rec(ptr->name_list2);
    next = ptr->next ;
   free mb ptr((void *) ptr,2);
#if DMP_MEM_ANY
   dmp_libc_name_list_list_msize_dec(12);
   dmp libc mem size dec(12);
#endif
   ptr = next ;
 }
}
routine: nfree libc name list list()
  _____*
void nfree_libc_name_list_list(struct libc_name_list_list **pptr)
{ struct libc name list list *ptr= *pptr;
 struct libc name_list_list *next;
 while (ptr!=NULL) {
   free_libc_name_list_rec(ptr->name_list1);
   free_libc_name_list_rec(ptr-> name_list2);
   next = ptr->next ;
   free mb ptr((void *) ptr,2);
#if DMP MEM ANY
   dmp libc name list list msize dec(12);
   dmp_libc mem_size_dec(12);
#endif
   ptr = next ;
 *pptr = NULL;
```

```
routine: free libc float list rec()
  void free_libc_float_list_rec(struct libc float list rec *ptr)
 struct libc float_list_rec *next;
 while (ptr!=NULL) {
   next = ptr->next ;
   free mb ptr((void *) ptr,1);
#if DMP MEM ANY
   dmp_libc_float_list_rec_msize_dec(8);
   dmp libc mem size dec(8);
#endif
  ptr = next ;
}
routine: nfree_libc_float_list_rec()
  void nfree libc float list rec(struct libc float list rec **pptr)
{ struct libc float list rec *ptr= *pptr;
 struct libc float list rec *next;
 while (ptr!=NULL) {
   next = ptr->next ;
   free_mb_ptr((void *) ptr,1);
#if DMP MEM ANY
   dmp libc float list rec msize dec(8);
   dmp_libc_mem_size_dec(8);
#endif
  ptr = next ;
 *pptr = NULL;
routine: free libc float list list()
  void free_libc_float_list_list(struct libc_float_list_list *ptr)
 struct libc_float_list_list *next;
 while (ptr!=NULL) {
   free_libc_float_list_rec(ptr->v_list);
  next = ptr->next ;
   free_mb_ptr((void *) ptr,1);
#if DMP_MEM_ANY
   dmp libc float list list msize dec(8);
   dmp libc mem size dec(8);
#endif
```

```
ptr = next ;
}
routine: nfree_libc_float_list_list()
  void nfree_libc_float_list_list(struct libc_float list list **pptr)
{ struct libc_float_list_list *ptr= *pptr;
 struct libc float list list *next;
 while (ptr!=NULL) {
   free libc float_list_rec(ptr->v list);
   next = ptr->next ;
   free_mb_ptr((void *) ptr,1);
#if DMP_MEM_ANY
   dmp_libc_float_list_list_msize_dec(8);
   dmp libc mem size dec(8);
#endif
   ptr = next ;
 *pptr = NULL;
routine: free_libc_define_rec()
  ______ */
void free_libc_define_rec(struct libc_define_rec *ptr)
 struct libc_define_rec *next;
 while (ptr!=NULL) {
   free text buffer(ptr->name);
   free text_buffer(ptr->object);
   free text buffer(ptr->type);
   next = ptr->next ;
   free_mb_ptr((void *) ptr,3);
#if DMP MEM ANY
   dmp libc define rec msize dec(16);
   dmp libc mem size dec(16);
#endif
   ptr = next ;
routine: nfree_libc_define_rec()
  void nfree_libc_define_rec(struct libc define rec **pptr)
{ struct libc_define_rec *ptr= *pptr;
 struct libc define rec *next;
```

```
while (ptr!=NULL) {
    free_text_buffer(ptr->name);
    free_text buffer(ptr->object);
    free_text_buffer(ptr->type);
    next = ptr->next ;
    free_mb_ptr((void *) ptr,3);
#if DMP MEM ANY
    dmp_libc_define_rec_msize dec(16);
    dmp_libc mem size dec(16);
#endif
   ptr = next ;
  *pptr = NULL;
routine: free_libc_cell area rec()
   void free_libc_cell_area_rec(struct libc_cell_area_rec *ptr)
  struct libc_cell_area_rec *next;
  while (ptr!=NULL) {
   free_text_buffer(ptr-> area name);
   next = ptr->next ;
   free_mb_ptr((void *) ptr,2);
#if DMP MEM ANY
   dmp_libc_cell_area_rec_msize_dec(12);
   dmp_libc_mem size dec(12);
#endif
   ptr = next ;
routine: nfree_libc_cell_area_rec()
  */
void nfree_libc_cell_area_rec(struct libc_cell_area_rec **pptr)
{ struct libc_cell_area_rec *ptr= *pptr;
 struct libc_cell_area rec *next;
 while (ptr!=NULL) {
   free_text_buffer(ptr->area_name);
    next = ptr->next ;
   free_mb_ptr((void *) ptr,2);
#if DMP MEM ANY
   dmp_libc_cell_area_rec_msize_dec(12);
   dmp_libc_mem size dec(12);
#endif
   ptr = next ;
 *pptr = NULL;
```

```
routine: free_libc_k_factor_rec()
   void free_libc_k_factor_rec(struct libc_k_factor_rec *ptr)
{ int i0;
  struct libc_k_factor_rec *next;
  while (ptr!=NULL) {
   free_text_buffer(ptr->kf name);
    next = ptr->next ;
   free_mb_ptr((void *) ptr,29);
#if DMP MEM ANY
   dmp_libc_k_factor_rec_msize_dec(548);
   dmp_libc_mem_size_dec(548);
#endif
   ptr = next ;
routine: nfree_libc_k_factor_rec()
  void nfree_libc_k_factor_rec(struct libc_k_factor_rec **pptr)
{ struct libc_k_factor_rec *ptr= *pptr;
 int i0;
 struct libc_k_factor rec *next;
 while (ptr!=NULL) {
   free_text_buffer(ptr-> kf_name);
    next = ptr->next ;
   free_mb_ptr((void *) ptr,29);
#if DMP MEM ANY
   dmp_libc_k_factor_rec_msize_dec(548);
   dmp_libc_mem_size_dec(548);
   ptr = next ;
 *pptr = NULL;
routine: free_libc_lib_rec()
  void free_libc_lib_rec(struct libc_lib_rec *ptr)
{ int i0;
 if (ptr==NULL) return;
   free_text_buffer(ptr->lib_name);
   free_text_buffer(ptr->aux_no_pulldown_pin_property);
   free_text_buffer(ptr->bus_naming_style);
   free_text_buffer(ptr-> comment);
   free_any_unit_rec(ptr-> current_unit);
   free_text_buffer(ptr->date);
   free_any_unit_rec(ptr->leakage_power_unit);
   free_text_buffer(ptr->no_pulldown_pin_property);
```

```
free_any_unit_rec(ptr->power_unit);
    free text buffer(ptr->preferred output pad slew rate control);
    free_text_buffer(ptr->preferred_output_pad_voltage);
    free_text_buffer(ptr->preferred_input_pad_voltage);
    free any unit rec(ptr->pulling resistance unit);
    free text buffer(ptr->revision);
    free any unit rec(ptr-> time unit);
   free_text_buffer(ptr->unconnected_pin_property);
   free any unit rec(ptr->voltage unit);
   free libc name list rec(ptr->default connection class);
   free text buffer(ptr->default operating conditions);
    free_text_buffer(ptr->default_wire_load);
   free text buffer(ptr-> default wire load selection);
    free libc k factor rec(ptr->k factor);
    free libc k factor rec(ptr->sc k factor);
    free any unit rec(ptr->capacitive load unit);
    free libc define rec(ptr->define);
    free libc_cell area rec(ptr->define cell area);
    free_float_buffer(ptr->piece_define);
    free_libc_name_list_rec(ptr-> routing_layers);
    free text buffer(ptr-> technology);
    free_libc_lu_table_template_rec(ptr->lut_template);
    free libc lu table template rec(ptr->plut template);
    free_libc_operating_condition_rec(ptr->operating_cond);
   free_libc_power_supply_rec(ptr-> power_supply);
   free libc table val rec(ptr->rise tr table);
   free libc table val rec(ptr->fall tr table);
   free_libc_timing_range_rec(ptr->timing_range);
   free_libc_type_rec(ptr-> type);
   free_libc_wire_load_rec(ptr-> wire_load);
   free_libc_wire_load_selection_rec(ptr->wire_load_selection);
    free_libc_cell_rec(ptr-> cells);
    free libc define value rec(ptr->def val);
    free mb ptr((void *) ptr,28);
#if DMP_MEM_ANY
    dmp_libc_lib_rec_msize_dec(392);
    dmp libc mem size dec(392);
#endif
routine: nfree libc lib rec()
   void nfree libc lib rec(struct libc lib rec **pptr)
{ struct libc_lib_rec *ptr= *pptr;
 int i0;
  if (ptr==NULL) return;
    free text buffer(ptr->lib name);
    free text buffer(ptr->aux no_pulldown pin property);
    free text buffer(ptr-> bus naming style);
    free text_buffer(ptr->comment);
    free_any_unit_rec(ptr->current_unit);
    free_text_buffer(ptr->date);
    free_any_unit_rec(ptr->leakage_power_unit);
    free_text_buffer(ptr-> no_pulldown_pin_property);
    free_any_unit_rec(ptr->power_unit);
```

```
free_text_buffer(ptr->preferred_output_pad_slew_rate_control);
   free_text_buffer(ptr-> preferred_output pad voltage);
   free text buffer(ptr->preferred input pad voltage);
   free_any_unit_rec(ptr->pulling resistance unit);
   free text buffer(ptr-> revision);
   free any unit rec(ptr->time unit);
   free text buffer(ptr->unconnected pin property);
   free any unit rec(ptr->voltage unit);
   free libc name list rec(ptr-> default connection class);
   free text buffer(ptr->default operating conditions);
   free_text_buffer(ptr->default_wire_load);
   free_text_buffer(ptr-> default_wire_load selection);
   free_libc_k_factor_rec(ptr-> k_factor);
   free libc k factor rec(ptr->sc k factor);
   free_any_unit_rec(ptr->capacitive load unit);
   free_libc_define_rec(ptr-> define);
   free_libc_cell_area_rec(ptr->define cell area);
   free_float_buffer(ptr-> piece_define);
   free_libc_name_list_rec(ptr->routing layers);
   free_text_buffer(ptr->technology);
   free_libc_lu_table_template_rec(ptr-> lut_template);
   free_libc_lu_table_template_rec(ptr->plut template);
   free libc operating condition rec(ptr->operating cond);
   free_libc_power_supply_rec(ptr->power_supply);
   free_libc_table_val_rec(ptr->rise_tr_table);
   free libc table val rec(ptr->fall tr table);
   free_libc_timing_range_rec(ptr-> timing range);
   free_libc_type_rec(ptr->type);
   free_libc_wire_load_rec(ptr->wire_load);
   free libc wire load selection rec(ptr->wire load selection);
   free_libc_cell_rec(ptr->cells);
   free_libc_define_value_rec(ptr->def_val);
   free mb ptr((void *) ptr,28);
#if DMP MEM ANY
   dmp_libc_lib_rec_msize_dec(392);
   dmp libc mem size dec(392);
#endif
 *pptr = NULL;
routine: free libc lu table template rec()
  void free_libc_lu_table_template_rec(struct libc_lu_table_template_rec *ptr)
{ int i0;
 struct libc lu table template rec *next;
 while (ptr!=NULL) {
   free_text_buffer(ptr-> tt name);
   free float buffer(ptr->index 1);
   free_float_buffer(ptr-> index_2);
   free_float_buffer(ptr->index 3);
    next = ptr->next ;
   free mb ptr((void *) ptr,7);
#if DMP MEM ANY
   dmp_libc_lu_table_template_rec_msize_dec(32);
```

```
dmp_libc_mem_size_dec(32);
#endif
   ptr = next ;
routine: nfree_libc_lu_table_template rec()
  void nfree_libc_lu_table_template_rec(struct libc lu table template rec
**pptr)
{ struct libc_lu_table_template_rec *ptr= *pptr;
int i0;
 struct libc lu table template rec *next;
 while (ptr!=NULL) {
   free text buffer(ptr->tt name);
   free_float_buffer(ptr->index_1);
   free_float_buffer(ptr-> index_2);
   free_float_buffer(ptr->index_3);
   next = ptr->next ;
   free_mb_ptr((void *) ptr,7);
#if DMP MEM ANY
   dmp_libc_lu_table_template_rec_msize_dec(32);
   dmp libc mem size dec(32);
#endif
   ptr = next ;
 *pptr = NULL;
/* /* /*
  routine: free libc oc power rail rec()
  void free_libc_oc_power_rail_rec(struct libc oc power rail rec *ptr)
 struct libc oc power rail rec *next;
 while (ptr!=NULL) {
   free_text_buffer(ptr-> power supply);
   next = ptr->next ;
   free_mb_ptr((void *) ptr,2);
#if DMP MEM ANY
   dmp_libc_oc_power_rail_rec_msize_dec(12);
   dmp_libc_mem size dec(12);
   ptr = next ;
routine: nfree_libc_oc_power_rail_rec()
  void nfree_libc_oc_power_rail_rec(struct libc_oc_power_rail_rec **pptr)
```

```
{ struct libc_oc_power_rail_rec *ptr= *pptr;
 struct libc oc power rail rec *next;
 while (ptr!=NULL) {
   free text buffer(ptr->power supply);
    next = ptr->next ;
   free mb ptr((void *) ptr,2);
#if DMP MEM ANY
   dmp_libc_oc_power_rail_rec_msize_dec(12);
   dmp_libc_mem_size_dec(12);
#endif
   ptr = next ;
 *pptr = NULL;
routine: free libc operating condition rec()
  void free_libc_operating_condition_rec(struct libc_operating_condition_rec
*ptr)
 struct libc_operating_condition_rec *next;
 while (ptr!=NULL) {
   free_text_buffer(ptr->oc_name);
   free_libc_oc_power_rail_rec(ptr->power_rail);
   next = ptr->next ;
   free_mb_ptr((void *) ptr,6);
#if DMP MEM ANY
   dmp libc operating condition rec msize dec(28);
   dmp_libc_mem_size_dec(28);
#endif
   ptr = next ;
}
routine: nfree_libc_operating_condition_rec()
  void nfree libc operating condition rec(struct libc operating condition rec
**pptr)
{ struct libc_operating_condition_rec *ptr= *pptr;
 struct libc operating condition_rec *next;
 while (ptr!=NULL) {
   free text buffer(ptr-> oc name);
   free libc oc power rail rec(ptr-> power rail);
    next = ptr->next ;
   free mb ptr((void *) ptr,6);
#if DMP_MEM_ANY
   dmp_libc_operating_condition_rec_msize_dec(28);
   dmp_libc_mem_size_dec(28);
```

```
#endif
   ptr = next ;
 *pptr = NULL;
routine: free_libc_power_supply_rec()
  void free_libc_power_supply_rec(struct libc_power_supply_rec *ptr)
 struct libc power supply rec *next;
 while (ptr!=NULL) {
   free_text_buffer(ptr->ps_name);
   free_text_buffer(ptr->default_ps);
   free libc oc power rail rec(ptr-> power rail);
   next = ptr->next ;
   free_mb_ptr((void *) ptr,3);
#if DMP MEM ANY
   dmp_libc_power_supply rec msize dec(16);
   dmp_libc_mem_size_dec(16);
#endif
   ptr = next ;
routine: nfree libc power supply rec()
  *
void nfree_libc_power_supply_rec(struct libc_power_supply rec **pptr)
{ struct libc power supply rec *ptr= *pptr;
 struct libc power supply rec *next;
 while (ptr!=NULL) {
   free text buffer(ptr->ps name);
   free text buffer(ptr->default ps);
   free_libc oc_power rail rec(ptr->power rail);
   next = ptr->next ;
   free_mb_ptr((void *) ptr,3);
#if DMP MEM ANY
   dmp_libc_power_supply_rec_msize_dec(16);
   dmp_libc mem size dec(16);
#endif
   ptr = next ;
 *pptr = NULL;
/*
  routine: free_libc_timing_range_rec()
  *
void free_libc_timing_range_rec(struct libc_timing_range_rec *ptr)
```

```
libc mem.c
 struct libc_timing_range_rec *next;
 while (ptr!=NULL) {
   free text buffer(ptr->tr name);
   next = ptr->next ;
   free_mb_ptr((void *) ptr,3);
#if DMP MEM ANY
   dmp_libc_timing_range_rec_msize_dec(16);
   dmp_libc_mem_size_dec(16);
#endif
   ptr = next ;
}
routine: nfree_libc_timing_range_rec()
  void nfree_libc_timing_range_rec(struct libc_timing_range_rec **pptr)
{ struct libc_timing_range_rec *ptr= *pptr;
 struct libc_timing_range_rec *next;
 while (ptr!=NULL) {
   free_text_buffer(ptr->tr_name);
   next = ptr->next ;
   free_mb_ptr((void *) ptr,3);
#if DMP MEM ANY
   dmp_libc_timing_range rec_msize_dec(16);
   dmp libc mem size dec(16);
#endif
   ptr = next ;
 *pptr = NULL;
routine: free_libc_type_rec()
  void free_libc_type_rec(struct libc_type_rec *ptr)
 struct libc_type_rec *next;
 while (ptr!=NULL) {
   free_text_buffer(ptr-> type_name);
   next = ptr->next ;
   free mb ptr((void *) ptr,5);
#if DMP_MEM_ANY
   dmp_libc_type_rec msize dec(24);
   dmp libc mem size dec(24);
#endif
   ptr = next ;
```

```
routine: nfree libc type rec()
  void nfree libc type rec(struct libc type rec **pptr)
{ struct libc type rec *ptr= *pptr;
 struct libc_type_rec *next;
 while (ptr!=NULL) {
   free_text_buffer(ptr-> type_name);
   next = ptr->next ;
   free mb ptr((void *) ptr,5);
#if DMP MEM ANY
   dmp_libc_type_rec_msize_dec(24);
   dmp_libc_mem_size_dec(24);
#endif
  ptr = next ;
 *pptr = NULL;
routine: free_libc_fanout_length_rec()
  void free_libc_fanout_length_rec(struct libc_fanout_length_rec *ptr)
 struct libc_fanout_length_rec *next;
 while (ptr!=NULL) {
   next = ptr->next ;
  free mb ptr((void *) ptr,8);
#if DMP MEM ANY
  dmp_libc_fanout_length_rec_msize_dec(36);
  dmp libc mem size dec(36);
#endif
  ptr = next ;
}
routine: nfree_libc_fanout_length_rec()
  void nfree_libc_fanout_length_rec(struct libc_fanout_length_rec **pptr)
{ struct libc fanout length rec *ptr= *pptr;
 struct libc fanout length rec *next;
 while (ptr!=NULL) {
   next = ptr->next ;
   free_mb_ptr((void *) ptr,8);
#if DMP MEM ANY
  dmp_libc_fanout_length_rec_msize_dec(36);
  dmp_libc_mem_size_dec(36);
#endif
```

```
ptr = next ;
 *pptr = NULL;
routine: free_libc_wire_load_rec()
  ______ */
void free_libc_wire_load_rec(struct libc_wire_load_rec *ptr)
 struct libc_wire_load_rec *next;
 while (ptr!=NULL) {
   free_text_buffer(ptr->wl_name);
   free_libc_fanout_length_rec(ptr->fanout_length);
   next = ptr->next ;
   free mb ptr((void *) ptr,6);
#if DMP MEM ANY
   dmp libc wire load rec msize dec(28);
   dmp libc mem size dec(28);
#endif
   ptr = next ;
}
routine: nfree_libc_wire_load_rec()
  void nfree_libc_wire_load_rec(struct libc_wire_load_rec **pptr)
{ struct libc wire load rec *ptr= *pptr;
 struct libc wire load rec *next;
 while (ptr!=NULL) {
   free_text_buffer(ptr->wl_name);
   free_libc_fanout length rec(ptr->fanout length);
   next = ptr->next ;
   free mb_ptr((void *) ptr,6);
#if DMP MEM ANY
   dmp_libc_wire_load_rec_msize_dec(28);
   dmp_libc_mem size dec(28);
#endif
  ptr = next ;
 *pptr = NULL;
routine: free_libc_wire_load_from_area_rec()
  void free_libc_wire_load_from_area_rec(struct libc_wire_load from area rec
*ptr)
 struct libc_wire_load_from_area_rec *next;
```

```
while (ptr!=NULL) {
   next = ptr->next ;
   free mb_ptr((void *) ptr,3);
#if DMP MEM ANY
   dmp libc wire load from area rec msize dec(16);
   dmp libc mem size dec(16);
#endif
   ptr = next ;
}
routine: nfree_libc_wire_load_from_area_rec()
  void nfree_libc_wire_load_from_area_rec(struct libc wire load from area rec
{ struct libc_wire_load_from_area_rec *ptr= *pptr;
 struct libc_wire_load_from_area_rec *next;
 while (ptr!=NULL) {
   next = ptr->next ;
   free mb ptr((void *) ptr,3);
#if DMP MEM ANY
   dmp_libc_wire_load_from_area_rec_msize_dec(16);
   dmp_libc mem size dec(16);
#endif
   ptr = next ;
 *pptr = NULL;
routine: free_libc_wire_load_selection_rec()
  ______ */
void free libc wire load selection rec(struct libc wire load selection rec
*ptr)
 struct libc_wire_load_selection_rec *next;
 while (ptr!=NULL) {
   free text buffer(ptr->wls name);
   free_libc_wire_load_from_area_rec(ptr->area_table);
   next = ptr->next ;
   free_mb_ptr((void *) ptr,2);
#if DMP_MEM_ANY
   dmp libc wire load selection rec msize dec(12);
   dmp_libc mem_size_dec(12);
#endif
   ptr = next ;
}
```

```
routine: nfree_libc_wire_load_selection_rec()
  void nfree_libc wire_load selection rec(struct libc wire load selection rec
{ struct libc wire load selection rec *ptr= *pptr;
 struct libc_wire_load_selection_rec *next;
 while (ptr!=NULL) {
   free text buffer(ptr->wls name);
   free_libc_wire_load_from_area_rec(ptr->area_table);
    next = ptr->next ;
   free mb ptr((void *) ptr,2);
#if DMP MEM ANY
   dmp_libc_wire_load_selection_rec_msize_dec(12);
   dmp_libc_mem_size dec(12);
#endif
   ptr = next ;
  *pptr = NULL;
routine: free_libc_cell_rec()
  void free libc cell rec(struct libc_cell_rec *ptr)
 struct libc cell rec *next;
 while (ptr!=NULL) {
   free text buffer(ptr->cell name);
   free text buffer(ptr->cell footprint);
   free libc bool opr rec(ptr->contention condition);
   free_text_buffer(ptr->geometry_print);
   free_text_buffer(ptr->scan_group);
   free text buffer(ptr->single bit degenerate);
   free_text buffer(ptr->vhdl name);
   free_libc_name_list_list(ptr->pin_equal);
   free libc name list list(ptr->pin opposite);
   free libc pin rec(ptr->pins);
   free_libc_internal_power(ptr->internal_power_c);
   free libc leakage power(ptr->leakage power);
   free_libc_ff_latch_rec(ptr->ff_latch);
   free libc memory rec(ptr-> memory);
   free_libc_routing_track_rec(ptr->routing_track);
   free_libc_define_value_rec(ptr-> def_val);
   next = ptr->next ;
   free mb ptr((void *) ptr,23);
#if DMP MEM ANY
   dmp_libc_cell_rec_msize_dec(132);
   dmp_libc_mem_size dec(132);
#endif
   ptr = next ;
```

```
routine: nfree libc cell rec()
  void nfree libc cell rec(struct libc cell rec **pptr)
{ struct libc cell rec *ptr= *pptr;
 struct libc_cell_rec *next;
 while (ptr!=NULL) {
   free_text_buffer(ptr->cell_name);
   free_text_buffer(ptr->cell_footprint);
   free libc bool_opr_rec(ptr->contention condition);
   free text buffer(ptr->geometry print);
   free_text_buffer(ptr->scan group);
   free_text_buffer(ptr->single_bit degenerate);
   free_text_buffer(ptr-> vhdl_name);
   free_libc_name_list_list(ptr->pin_equal);
   free_libc_name_list_list(ptr->pin_opposite);
   free_libc_pin_rec(ptr-> pins);
   free_libc_internal_power(ptr-> internal_power_c);
   free_libc leakage power(ptr->leakage power);
   free libc ff latch rec(ptr->ff latch);
   free_libc_memory_rec(ptr->memory);
   free_libc_routing_track_rec(ptr->routing_track);
   free_libc_define_value_rec(ptr->def_val);
   next = ptr->next ;
   free_mb_ptr((void *) ptr,23);
#if DMP_MEM_ANY
   dmp_libc_cell_rec_msize_dec(132);
   dmp_libc_mem_size_dec(132);
   ptr = next ;
 *pptr = NULL;
routine: free_libc_memory_write_rec()
  void free_libc_memory_write_rec(struct libc_memory_write_rec *ptr)
 if (ptr==NULL) return;
   free text buffer(ptr-> address);
   free_text_buffer(ptr->clock_on);
   free_text_buffer(ptr-> enable);
   free mb ptr((void *) ptr,2);
#if DMP MEM ANY
   dmp_libc_memory_write_rec_msize_dec(12);
   dmp_libc_mem_size_dec(12);
#endif
routine: nfree_libc_memory_write_rec()
```

```
void nfree_libc_memory_write_rec(struct libc_memory_write_rec **pptr)
 { struct libc_memory_write_rec *ptr= *pptr;
  if (ptr==NULL) return;
    free_text_buffer(ptr->address);
    free_text_buffer(ptr-> clock_on);
    free_text_buffer(ptr->enable);
    free_mb_ptr((void *) ptr,2);
#if DMP_MEM_ANY
    dmp_libc_memory_write_rec_msize_dec(12);
    dmp_libc_mem_size_dec(12);
#endif
  *pptr = NULL;
struct libc_memory_write_rec * copy_libc_memory_write_rec(struct
libc_memory_write_rec *sou)
{ struct libc_memory_write_rec *tar, *head, *prev;
  if (sou==NULL) return(NULL);
  prev = NULL;
  head = tar = new_libc_memory_write_rec();
    tar->address = (char *) copy_string(sou->address);
    tar->clock_on = (char *) copy_string(sou->clock_on);
   tar->enable
                 = (char *) copy_string(sou->enable);
   return(head);
routine: free_libc_bool_opr_rec()
   */
void free_libc_bool_opr_rec(struct libc_bool_opr_rec *ptr)
  struct libc_bool opr rec *next;
 while (ptr!=NULL) {
   switch (ptr-> type) {
     case ID_B :
          free_text_buffer(ptr-> u.id_name);
     default :
         break;
   free_libc_bool_opr_rec(ptr->L);
    next = ptr->R ;
   free_mb_ptr((void *) ptr,3);
#if DMP MEM ANY
   dmp_libc_bool_opr_rec_msize_dec(16);
   dmp_libc_mem_size dec(16);
#endif
   ptr = next ;
```

```
routine: nfree libc bool opr rec()
   void nfree_libc_bool_opr_rec(struct libc_bool_opr_rec **pptr)
{ struct libc bool opr rec *ptr= *pptr;
 struct libc bool opr rec *next;
 while (ptr!=NULL) {
   switch (ptr->type) {
     case ID B :
          free_text_buffer(ptr->u.id name);
     default :
         break;
   free_libc_bool_opr_rec(ptr->L);
   next = ptr->R ;
   free_mb_ptr((void *) ptr,3);
#if DMP MEM ANY
   dmp libc bool opr rec msize dec(16);
   dmp libc mem size dec(16);
#endif
 ptr = next ;
}
 *pptr = NULL;
struct libc_bool_opr_rec * copy_libc_bool_opr_rec(struct libc bool opr rec
*sou)
{ struct libc_bool_opr_rec *tar, *head, *prev;
 if (sou==NULL) return(NULL);
 prev = NULL;
 head = tar = new_libc_bool_opr_rec();
 while (sou!=NULL) {
   memcpy((char *)tar,(char *)sou,sizeof(struct libc_bool_opr rec));
   tar->type
                = sou->type;
   switch (sou->type) {
     case ID_B :
          tar->u.id_name = (char *) copy string(sou->u.id name);
         break;
     default :
         tar->u.value = sou->u.value;
         break;
                = (struct libc bool opr rec *)
copy_libc_bool_opr_rec(sou->L);
   sou = sou -> R;
```

```
if (sou!=NULL)
   { prev = tar;
     tar->R = new libc bool opr rec();
     tar = tar->R;
   return(head);
routine: free_libc_pin_rec()
  ______ */
void free_libc_pin_rec(struct libc_pin_rec *ptr)
 struct libc_pin_rec *next;
 while (ptr!=NULL) {
   free_libc_name_list_rec(ptr->pin_name);
   free libc name list rec(ptr->members);
   free_libc_name_list_rec(ptr->connection class);
   free_libc_bool_opr_rec(ptr-> function);
   free libc name list rec(ptr->input map);
   free text buffer(ptr->input signal level);
   free_text buffer(ptr->input voltage);
   free_text_buffer(ptr->internal_node);
   free_text_buffer(ptr->output_signal_level);
   free text buffer(ptr-> output voltage);
   free_libc_bool_opr_rec(ptr-> state_function);
   free libc bool opr rec(ptr->three state);
   free_text_buffer(ptr->vhdl_name);
   free text buffer(ptr->wired connection class);
   free libc bool opr rec(ptr->x function);
   free_text_buffer(ptr->address of memory read);
   free_libc_memory_write_rec(ptr->memory_write);
   free libc timing rec(ptr->timing);
   free libc min pulse width rec(ptr->min pulse width);
   free_libc_minimum period rec(ptr->minimum period);
   free_libc_internal_power(ptr->internal_power);
   free libc define value rec(ptr->def val);
    next = ptr->next ;
   free mb ptr((void *) ptr,27);
#if DMP MEM ANY
   dmp libc pin rec msize dec(276);
   dmp libc mem size dec(276);
#endif
   ptr = next ;
routine: nfree_libc_pin_rec()
  void nfree_libc_pin_rec(struct libc_pin_rec **pptr)
{ struct libc pin rec *ptr= *pptr;
```

```
struct libc pin rec *next;
  while (ptr!=NULL) {
    free libc name list rec(ptr->pin name);
    free libc name list rec(ptr->members);
    free libc name list rec(ptr-> connection class);
    free_libc_bool_opr_rec(ptr->function);
    free_libc_name_list_rec(ptr->input map);
    free_text_buffer(ptr->input_signal_level);
    free_text_buffer(ptr->input_voltage);
    free_text_buffer(ptr-> internal_node);
    free_text_buffer(ptr->output_signal level);
    free text buffer(ptr->output voltage);
    free libc bool opr rec(ptr->state function);
    free_libc_bool_opr_rec(ptr->three_state);
    free_text_buffer(ptr->vhdl_name);
    free text_buffer(ptr->wired connection class);
    free_libc_bool_opr_rec(ptr->x_function);
    free_text_buffer(ptr-> address_of_memory_read);
    free_libc_memory_write_rec(ptr->memory_write);
    free_libc_timing rec(ptr->timing);
    free_libc_min_pulse_width_rec(ptr-> min pulse width);
    free_libc_minimum_period_rec(ptr->minimum_period);
    free_libc_internal_power(ptr->internal_power);
    free_libc_define_value_rec(ptr->def_val);
     next = ptr->next ;
    free_mb_ptr((void *) ptr,27);
#if DMP_MEM_ANY
    dmp libc pin rec msize dec(276);
    dmp_libc_mem_size_dec(276);
#endif
    ptr = next ;
  *pptr = NULL;
struct libc_pin_rec * copy_libc_pin_rec(struct libc pin_rec *sou)
{ struct libc pin rec *tar, *head, *prev;
  if (sou==NULL) return(NULL);
  prev = NULL;
  head = tar = new_libc_pin_rec();
  while (sou!=NULL) {
    tar->pin name = (struct libc name list rec *)
copy_libc name list rec(sou->pin name);
    tar->_current_cell = sou->_current_cell;
    tar->members
                  = (struct libc name list rec *)
copy_libc_name_list_rec(sou->members);
    tar->is bus
                   = sou->is bus;
    tar-> bus type = sou-> bus type;
    tar->capacitance = sou->capacitance;
    tar->clock
                    = sou->clock;
    tar->clock_gate_enable_pin = sou->clock_gate_enable_pin;
    tar->connection_class = (struct libc name list rec *)
copy_libc_name_list_rec(sou->connection_class);
    tar->direction = sou->direction;
```

```
tar->dont_false = sou->dont_false;
    tar->drive current = sou->drive current;
    tar->drive type = sou->drive type;
    tar->emitter_count = sou->emitter_count;
    tar->fall current slop after threshold = sou-
>fall current slop after threshold;
    tar->fall current slop before threshold = sou-
>fall current slop_before_threshold;
    tar->fall_time_after_threshold = sou->fall_time_after_threshold;
    tar->fall_time_before_threshold = sou->fall_time_before_threshold;
    tar->fall_wor_emitter = sou->fall_wor_emitter;
    tar->fall_wor_intercept = sou->fall wor_intercept;
    tar->fanout load = sou->fanout load;
    tar->function
                  = (struct libc bool opr rec *)
copy_libc_bool_opr_rec(sou->function);
    tar->hysteresis = sou->hysteresis;
    tar->input_map = (struct libc_name_list_rec *)
copy_libc_name_list_rec(sou->input_map);
    tar->input_signal_level = (char *) copy_string(sou->input_signal_level);
    tar->input_voltage = (char *) copy_string(sou->input_voltage);
    tar->internal_node = (char *) copy_string(sou->internal node);
    tar->inverted output = sou->inverted output;
    tar->is_pad
                   = sou->is_pad;
    tar->max fanout = sou->max fanout;
    tar->max_transition = sou->max transition;
    tar->max_capacitance = sou->max_capacitance;
    tar->min fanout = sou->min fanout;
   tar->min_transition = sou->min_transition;
   tar->min capacitance = sou->min capacitance;
   tar->multicell_pad_pin = sou->multicell pad pin;
    tar->multiple drivers legal = sou->multiple drivers legal;
    tar->nextstate_type = sou->nextstate_type;
    tar->output_signal_level = (char *) copy_string(sou-
>output signal level);
    tar->output_voltage = (char *) copy_string(sou->output voltage);
    tar->pin func type = sou->pin func type;
    tar->pin power = sou->pin power;
    tar->prefer tied = sou->prefer tied;
    tar->primary_output = sou->primary output;
    tar->pulling_current = sou->pulling_current;
    tar->pulling resistance = sou->pulling resistance;
    tar->reference capacitance = sou->reference capacitance;
    tar->rise_current_slop_after_threshold = sou-
>rise_current_slop_after_threshold;
    tar->rise current slop before threshold = sou-
>rise current slop_before_threshold;
    tar->rise_time_after_threshold = sou->rise_time_after_threshold;
    tar->rise_time_before_threshold = sou->rise_time_before_threshold;
    tar->rise_wor_emitter = sou->rise_wor_emitter;
    tar->rise wor intercept = sou->rise wor intercept;
    tar->slew control = sou->slew control;
    tar->state_function = (struct libc_bool_opr_rec *)
copy_libc_bool_opr_rec(sou->state_function);
    tar->three_state = (struct libc_bool_opr_rec *)
copy_libc_bool_opr_rec(sou->three_state);
    tar->vhdl_name = (char *) copy_string(sou->vhdl_name);
    tar->wire_capacitance = sou->wire_capacitance;
```

```
tar->wired_connection_class = (char *) copy_string(sou-
 >wired_connection class);
     tar->x_function = (struct libc_bool_opr_rec *)
 copy_libc_bool_opr_rec(sou->x_function);
     tar->address_of_memory_read = (char *) copy_string(sou-
 >address of_memory_read);
     tar->memory_write = (struct libc_memory_write_rec *)
 copy_libc_memory_write_rec(sou->memory_write);
     tar->timing
                   = (struct libc_timing_rec *) copy_libc_timing_rec(sou-
 >timing);
    tar->min_pulse_width = (struct libc_min_pulse_width_rec *)
copy_libc_min_pulse_width_rec(sou->min_pulse_width);
    tar->minimum_period = (struct libc_minimum_period_rec *)
copy_libc_minimum_period_rec(sou->minimum_period);
    tar->internal_power = (struct libc_internal_power *)
copy_libc_internal_power(sou->internal_power);
    tar->def val
                 = (struct libc_define_value_rec *)
copy_libc_define_value_rec(sou->def_val);
    tar->pin_type
                  = sou->pin type;
    sou = sou->next ;
    if (sou!=NULL)
    { prev = tar;
      tar->next = new_libc_pin_rec();
      tar = tar->next;
  }
    return (head);
routine: free_libc_ff_latch_rec()
   void free_libc_ff_latch_rec(struct libc_ff_latch_rec *ptr)
  struct libc_ff_latch_rec *next;
  while (ptr!=NULL) {
    free_text_buffer(ptr->Q_name);
    free_text_buffer(ptr->QN_name);
    free_libc_bool_opr_rec(ptr->clear);
    free_libc_bool_opr_rec(ptr->preset);
    free_libc_bool_opr_rec(ptr-> clock_on);
    free_libc_bool_opr_rec(ptr-> next_state);
    free_libc_bool_opr_rec(ptr->on_also);
    free_libc_bool_opr_rec(ptr->enable);
    free_libc_bool_opr_rec(ptr->data_in);
    free_libc_bool_opr_rec(ptr->force_00);
    free_libc_bool_opr_rec(ptr->force_01);
    free_libc_bool_opr_rec(ptr->force_10);
    free_libc_bool_opr_rec(ptr->force_11);
    next = ptr->next ;
    free_mb_ptr((void *) ptr,15);
#if DMP MEM ANY
   dmp_libc_ff_latch_rec_msize_dec(64);
   dmp_libc_mem_size dec(64);
```

```
#endif
    ptr = next ;
}
routine: nfree_libc_ff_latch_rec()
   void nfree_libc_ff_latch_rec(struct libc_ff_latch_rec **pptr)
{ struct libc_ff_latch_rec *ptr= *pptr;
  struct libc ff latch rec *next;
  while (ptr!=NULL) {
    free_text_buffer(ptr->Q_name);
    free_text_buffer(ptr-> QN_name);
    free_libc_bool_opr_rec(ptr->clear);
    free_libc_bool_opr_rec(ptr->preset);
    free_libc_bool_opr_rec(ptr->clock_on);
    free_libc_bool_opr_rec(ptr-> next state);
    free_libc_bool_opr_rec(ptr->on_also);
    free_libc_bool opr rec(ptr-> enable);
    free_libc_bool_opr_rec(ptr->data_in);
   free_libc_bool_opr_rec(ptr->force 00);
   free_libc_bool_opr_rec(ptr->force_01);
   free_libc_bool_opr_rec(ptr->force_10);
   free_libc_bool_opr_rec(ptr->force_11);
   next = ptr->next ;
   free_mb_ptr((void *) ptr,15);
#if DMP MEM_ANY
   dmp_libc_ff_latch_rec_msize_dec(64);
   dmp_libc_mem_size dec(64);
#endif
   ptr = next ;
  *pptr = NULL;
routine: free_libc_internal_power()
  */
void free_libc_internal_power(struct libc_internal_power *ptr)
 struct libc_internal_power *next;
 while (ptr!=NULL) {
   free_libc_name_list_rec(ptr->inputs);
   free_libc_name_list_rec(ptr->outputs);
   free_libc_name_list_rec(ptr->equal_or_opposite_output);
   free_text_buffer(ptr->power_level);
   free_libc_name_list_rec(ptr->related_pin);
   free_libc_bool_opr_rec(ptr-> when);
   free_libc_table_val_rec(ptr-> rise_power);
   free_libc_table_val_rec(ptr->fall_power);
   free_libc_table_val_rec(ptr->power);
```

```
next = ptr->next ;
    free mb ptr((void *) ptr,9);
#if DMP MEM ANY
    dmp_libc_internal power msize dec(40);
    dmp_libc mem size dec(40);
#endif
    ptr = next ;
}
routine: nfree_libc_internal_power()
   *
void nfree_libc_internal_power(struct libc_internal_power **pptr)
{ struct libc_internal_power *ptr= *pptr;
  struct libc_internal power *next;
  while (ptr!=NULL) {
    free_libc_name_list_rec(ptr-> inputs);
    free_libc_name_list_rec(ptr->outputs);
    free_libc_name_list_rec(ptr->equal_or opposite output);
    free_text_buffer(ptr->power level);
    free_libc_name_list_rec(ptr->related pin);
    free_libc_bool_opr_rec(ptr-> when);
    free_libc_table_val_rec(ptr->rise_power);
    free_libc_table_val_rec(ptr->fall_power);
    free_libc_table_val_rec(ptr->power);
    next = ptr->next ;
    free_mb_ptr((void *) ptr,9);
#if DMP_MEM ANY
    dmp_libc_internal_power_msize_dec(40);
   dmp libc mem size dec(40);
#endif
   ptr = next ;
  *pptr = NULL;
struct libc_internal_power * copy_libc_internal_power(struct
libc internal_power *sou)
{ struct libc_internal_power *tar, *head, *prev;
  if (sou==NULL) return(NULL);
 prev = NULL;
 head = tar = new_libc_internal_power();
 while (sou!=NULL) {
   tar->inputs
                  = (struct libc name list rec *)
copy_libc_name_list_rec(sou->inputs);
   tar->outputs = (struct libc_name_list_rec *)
copy_libc_name_list_rec(sou->outputs);
   tar->equal_or_opposite_output = (struct libc name list rec *)
copy_libc_name_list_rec(sou->equal or opposite output);
   tar->power_level = (char *) copy_string(sou->power level);
   tar->related_pin = (struct libc name list rec *)
copy_libc_name_list_rec(sou->related pin);
```

```
= (struct libc bool opr rec *)
copy_libc_bool_opr_rec(sou->when);
   tar->rise power = (struct libc table val rec *)
copy libc table val rec(sou->rise power);
   tar->fall_power = (struct libc_table_val_rec *)
copy_libc_table_val_rec(sou->fall_power);
   tar->power
             = (struct libc table val rec *)
copy_libc_table_val_rec(sou->power);
   sou = sou->next;
   if (sou!=NULL)
   { prev = tar;
     tar->next = new libc internal power();
     tar = tar->next;
   return(head);
}
routine: free_libc_leakage_power()
  */
void free_libc_leakage_power(struct libc_leakage_power *ptr)
 struct libc_leakage_power *next;
 while (ptr!=NULL) {
   free_libc_bool_opr_rec(ptr-> when);
    next = ptr->next ;
   free mb ptr((void *) ptr,2);
#if DMP MEM ANY
   dmp_libc_leakage_power_msize_dec(12);
   dmp_libc_mem_size_dec(12);
#endif
   ptr = next ;
routine: nfree_libc_leakage_power()
  void nfree_libc_leakage_power(struct libc_leakage_power **pptr)
{ struct libc_leakage_power *ptr= *pptr;
 struct libc_leakage_power *next;
 while (ptr!=NULL) {
   free_libc_bool_opr_rec(ptr->when);
   next = ptr->next ;
   free mb ptr((void *) ptr,2);
#if DMP MEM ANY
   dmp libc leakage power msize dec(12);
   dmp libc mem size dec(12);
#endif
   ptr = next ;
```

```
*pptr = NULL;
struct libc leakage power * copy libc leakage power(struct libc_leakage_power
*sou)
{ struct libc leakage power *tar, *head, *prev;
 if (sou==NULL) return(NULL);
 prev = NULL;
 head = tar = new_libc_leakage_power();
 while (sou!=NULL) {
            = (struct libc_bool_opr_rec *)
   tar->when
copy libc bool opr rec(sou->when);
   tar->value = sou->value;
   sou = sou->next;
   if (sou!=NULL)
   { prev = tar;
    tar->next = new_libc_leakage_power();
    tar = tar->next;
   return (head);
}
routine: free_libc_memory_rec()
  void free libc memory rec(struct libc memory rec *ptr)
 if (ptr==NULL) return;
   free mb ptr((void *) ptr,2);
#if DMP MEM ANY
   dmp_libc_memory_rec_msize_dec(12);
   dmp libc mem size dec(12);
#endif
routine: nfree_libc_memory_rec()
  */
void nfree_libc_memory_rec(struct libc_memory_rec **pptr)
{ struct libc memory rec *ptr= *pptr;
 if (ptr==NULL) return;
   free mb ptr((void *) ptr,2);
#if DMP MEM ANY
   dmp_libc_memory_rec_msize_dec(12);
   dmp libc mem size dec(12);
#endif
 *pptr = NULL;
```

```
routine: free libc piece value_rec()
  ______ */
void free libc piece value_rec(struct libc_piece_value_rec *ptr)
 struct libc_piece_value_rec *next;
 while (ptr!=NULL) {
   next = ptr->next ;
   free mb ptr((void *) ptr,9);
#if DMP MEM ANY
   dmp libc piece value rec msize dec(40);
   dmp libc mem size dec(40);
#endif
   ptr = next ;
}
routine: nfree_libc_piece_value_rec()
   _____ */
void nfree libc piece value rec(struct libc piece value rec **pptr)
{ struct libc piece value_rec *ptr= *pptr;
 struct libc_piece_value_rec *next;
 while (ptr!=NULL) {
    next = ptr->next ;
   free mb ptr((void *) ptr,9);
#if DMP MEM ANY
   dmp libc piece value rec msize dec(40);
   dmp_libc_mem_size_dec(40);
#endif
   ptr = next ;
  *pptr = NULL;
struct libc_piece_value_rec * copy_libc_piece_value_rec(struct
libc piece value rec *sou)
{ struct libc_piece_value_rec *tar, *head, *prev;
  if (sou==NULL) return(NULL);
 prev = NULL;
 head = tar = new libc piece value rec();
 while (sou!=NULL) {
   tar->piece
                 = sou->piece;
   tar->fall_delay_intercept = sou->fall_delay_intercept;
   tar->fall_nonpaired_twin = sou->fall_nonpaired_twin;
   tar->fall pin resistance = sou->fall_pin_resistance;
   tar->fall wire resistance = sou->fall wire resistance;
   tar->rise_delay_intercept = sou->rise_delay_intercept;
   tar->rise_nonpaired_twin = sou->rise_nonpaired_twin;
   tar->rise pin resistance = sou->rise pin resistance;
   tar->rise_wire_resistance = sou->rise_wire_resistance;
   sou = sou->next ;
```

```
if (sou!=NULL)
   { prev = tar;
    tar->next = new_libc_piece_value_rec();
    tar = tar->next;
  return (head);
routine: free_libc_float_rec()
  *
void free libc float rec(struct libc float rec *ptr)
 if (ptr==NULL) return;
   free mb ptr((void *) ptr,0);
#if DMP MEM ANY
   dmp libc float rec msize dec(4);
   dmp_libc_mem_size_dec(4);
#endif
}
routine: nfree_libc_float_rec()
  void nfree libc float rec(struct libc float_rec **pptr)
{ struct libc_float_rec *ptr= *pptr;
 if (ptr==NULL) return;
   free mb ptr((void *) ptr,0);
#if DMP MEM ANY
   dmp libc float rec msize dec(4);
   dmp libc mem size dec(4);
#endif
 *pptr = NULL;
struct libc_float_rec * copy_libc_float_rec(struct libc_float_rec *sou)
{ struct libc_float_rec *tar, *head, *prev;
 if (sou==NULL) return(NULL);
 prev = NULL;
 head = tar = new libc float rec();
 memcpy((char *)tar, (char *)sou, sizeof(struct libc_float_rec));
 return (head);
routine: free libc table val rec()
  void free_libc_table_val_rec(struct libc_table_val_rec *ptr)
{ int i0;
```

```
if (ptr==NULL) return;
   free float buffer(ptr->index1);
   free float buffer(ptr->index2);
   free float buffer(ptr->index3);
   free float buffer(ptr-> values);
   free mb ptr((void *) ptr,5);
#if DMP MEM ANY
   dmp libc table val rec msize dec(24);
   dmp_libc_mem_size_dec(24);
#endif
routine: nfree libc table val rec()
  */
void nfree libc table val rec(struct libc table val rec **pptr)
{ struct libc table val rec *ptr= *pptr;
int i0;
 if (ptr==NULL) return;
   free_float_buffer(ptr-> index1);
   free_float_buffer(ptr->index2);
   free float buffer(ptr->index3);
   free float buffer(ptr-> values);
   free mb ptr((void *) ptr,5);
#if DMP MEM ANY
   dmp libc table val rec msize dec(24);
   dmp_libc_mem_size_dec(24);
#endif
  *pptr = NULL;
struct libc_table_val_rec * copy_libc_table_val_rec(struct libc table val rec
{ struct libc table val rec *tar, *head, *prev;
 int i0;
 if (sou==NULL) return(NULL);
 prev = NULL;
 head = tar = new libc table val rec();
   tar-> tbl = sou-> tbl;
   tar->index1
                  = get float buffer(sizeof float buffer(sou->index1));
   for (i0=0;i0<sizeof float buffer(sou->index1);i0++)
    tar->index1 [i0] = sou->index1[i0];
                  = get_float_buffer(sizeof_float_buffer(sou->index2));
   tar->index2
   for (i0=0;i0<sizeof_float_buffer(sou->index2);i0++)
    tar->index2 [i0] = sou->index2[i0];
   tar->index3
                  = get float buffer(sizeof float buffer(sou->index3));
   for (i0=0;i0<sizeof_float_buffer(sou->index3);i0++)
    tar->index3 [i0] = sou->index3[i0];
   tar->scalar val = sou->scalar val;
                  = get_float_buffer(sizeof_float_buffer(sou->values));
   tar->values
   for (i0=0;i0<sizeof float buffer(sou->values);i0++)
    tar->values [i0] = sou->values[i0];
   return(head);
}
```

```
routine: free_libc_timing_rec()
  _____*
void free libc timing rec(struct libc timing rec *ptr)
 struct libc timing rec *next;
 while (ptr!=NULL) {
   free text buffer(ptr-> t_name);
   free libc bool opr rec(ptr->related bus_pins);
   free libc name list rec(ptr->related pin);
   free_libc_bool_opr_rec(ptr->when);
   free libc_bool_opr_rec(ptr->when_start);
   free libc bool_opr_rec(ptr-> when_end);
   free_libc_piece_value_rec(ptr->piecewise);
   free libc table val rec(ptr->cell rise);
   free libc table val rec(ptr-> cell fall);
   free libc table val rec(ptr->rise propagation);
   free_libc_table_val_rec(ptr-> fall_propagation);
   free libc table_val_rec(ptr->rise_transition);
   free_libc_table_val_rec(ptr->fall_transition);
   free_libc_table_val_rec(ptr->rise_constraint);
   free_libc_table_val_rec(ptr->fall_constraint);
    next = ptr->next ;
   free mb ptr((void *) ptr,22);
#if DMP MEM ANY
   dmp_libc_timing_rec_msize_dec(120);
   dmp libc mem size_dec(120);
#endif
   ptr = next ;
 }
routine: nfree libc timing rec()
  _____ */
void nfree libc timing rec(struct libc timing rec **pptr)
{ struct libc timing rec *ptr= *pptr;
 struct libc_timing_rec *next;
 while (ptr!=NULL) {
   free text buffer(ptr-> t_name);
   free_libc_bool_opr_rec(ptr-> related_bus_pins);
   free_libc_name_list_rec(ptr->related_pin);
   free libc bool opr_rec(ptr-> when);
   free_libc_bool_opr_rec(ptr->when_start);
   free_libc_bool_opr_rec(ptr->when_end);
   free_libc_piece_value_rec(ptr->piecewise);
   free_libc_table_val_rec(ptr-> cell_rise);
   free_libc_table_val_rec(ptr->cell_fall);
   free libc table val rec(ptr->rise propagation);
   free libc table val_rec(ptr-> fall_propagation);
   free libc table val rec(ptr-> rise transition);
   free libc table val rec(ptr->fall transition);
```

```
free libc table val rec(ptr->rise_constraint);
    free_libc_table_val_rec(ptr-> fall_constraint);
    next = ptr->next ;
    free mb ptr((void *) ptr,22);
#if DMP MEM ANY
    dmp libc_timing_rec_msize_dec(120);
    dmp_libc_mem_size dec(120);
#endif
    ptr = next ;
  *pptr = NULL;
struct libc_timing_rec * copy_libc_timing_rec(struct libc_timing_rec *sou)
{ struct libc_timing_rec *tar, *head, *prev;
  if (sou==NULL) return(NULL);
  prev = NULL;
  head = tar = new libc timing rec();
  while (sou!=NULL) {
                    = (char *) copy_string(sou->t_name);
    tar->t_name
    tar->_current_pin = sou->_current_pin;
    tar->edge_rate_sensitivity_f0 = sou->edge_rate_sensitivity_f0;
    tar->edge_rate_sensitivity_f1 = sou->edge_rate_sensitivity_f1;
    tar->edge rate sensitivity_r0 = sou->edge_rate_sensitivity_r0;
    tar->edge rate sensitivity_r1 = sou->edge_rate_sensitivity_r1;
    tar->fall resistance = sou->fall_resistance;
    tar->rise resistance = sou->rise_resistance;
    tar->intrinsic_fall = sou->intrinsic_fall;
    tar->intrinsic_rise = sou->intrinsic rise;
    tar->related_bus_pins = (struct libc_bool_opr_rec *)
copy libc bool opr rec(sou->related_bus_pins);
    tar->related_output_pin = sou->related_output_pin;
    tar->related_pin = (struct libc name list rec *)
copy libc name_list_rec(sou->related_pin);
    tar->slope fall = sou->slope_fall;
    tar->slope rise = sou->slope rise;
    tar->timing_type = sou->timing_type;
    tar->timing_sense = sou->timing_sense;
                    = (struct libc_bool_opr_rec *)
    tar->when
copy libc bool opr rec(sou->when);
    tar->when_start = (struct libc bool opr rec *)
copy_libc_bool_opr_rec(sou->when_start);
    tar->when_end = (struct libc_bool_opr_rec *)
copy_libc_bool_opr_rec(sou->when_end);
    tar->piecewise = (struct libc_piece_value_rec *)
copy libc piece value rec(sou->piecewise);
    tar->cell_rise = (struct libc_table_val_rec *)
copy_libc_table_val_rec(sou->cell_rise);
    tar->cell_fall = (struct libc_table_val_rec *)
copy_libc_table_val_rec(sou->cell_fall);
    tar->rise propagation = (struct libc table val_rec *)
copy libc table val rec(sou->rise propagation);
    tar->fall_propagation = (struct libc_table val_rec *)
copy_libc_table_val_rec(sou->fall_propagation);
    tar->rise transition = (struct libc_table_val_rec *)
copy libc table val rec(sou->rise transition);
```

```
tar->fall_transition = (struct libc_table_val_rec *)
copy libc table val rec(sou->fall transition);
   tar->rise_constraint = (struct libc_table_val_rec *)
copy libc table val rec(sou->rise_constraint);
   tar->fall constraint = (struct libc table val rec *)
copy libc table val rec(sou->fall_constraint);
   sou = sou->next;
   if (sou!=NULL)
   { prev = tar;
     tar->next = new_libc_timing_rec();
     tar = tar->next;
   return(head);
}
routine: free libc_min_pulse_width_rec()
  void free_libc_min_pulse_width_rec(struct libc_min_pulse_width_rec *ptr)
 struct libc min pulse width_rec *next;
 while (ptr!=NULL) {
   free_libc_bool_opr_rec(ptr->when);
    next = ptr->next ;
   free_mb_ptr((void *) ptr,3);
#if DMP MEM ANY
   dmp libc min pulse width rec msize dec(16);
   dmp_libc_mem_size dec(16);
#endif
   ptr = next ;
routine: nfree_libc_min_pulse_width_rec()
  _____*/
void nfree libc min pulse width_rec(struct libc_min_pulse_width_rec **pptr)
{ struct libc min pulse_width_rec *ptr= *pptr;
 struct libc_min_pulse_width_rec *next;
 while (ptr!=NULL) {
   free_libc_bool_opr_rec(ptr->when);
    next = ptr->next ;
   free mb ptr((void *) ptr,3);
#if DMP MEM ANY
   dmp_libc_min_pulse_width_rec_msize_dec(16);
   dmp libc mem size dec(16);
#endif
   ptr = next ;
  *pptr = NULL;
```

libc_mem.c

```
}
struct libc min pulse width rec * copy libc min pulse width_rec(struct
libc min pulse width rec *sou)
{ struct libc_min_pulse_width_rec *tar, *head, *prev;
 if (sou==NULL) return(NULL);
 prev = NULL;
 head = tar = new_libc_min_pulse_width_rec();
 while (sou!=NULL) {
   tar->constraint_high = sou->constraint_high;
   tar->constraint_low = sou->constraint_low;
   tar->when
                = (struct libc_bool_opr_rec *)
copy_libc_bool_opr_rec(sou->when);
   sou = sou->next;
   if (sou!=NULL)
   { prev = tar;
     tar->next = new_libc_min_pulse_width_rec();
     tar = tar->next;
   return (head);
routine: free_libc_minimum_period_rec()
  ______ */
void free_libc_minimum_period_rec(struct libc_minimum_period_rec *ptr)
 struct libc_minimum_period_rec *next;
 while (ptr!=NULL) {
   free libc bool opr rec(ptr-> when);
   next = ptr->next ;
   free mb ptr((void *) ptr,2);
#if DMP_MEM_ANY
   dmp libc_minimum_period_rec_msize_dec(12);
   dmp libc mem_size_dec(12);
#endif
   ptr = next ;
routine: nfree_libc_minimum period rec()
  void nfree_libc_minimum_period_rec(struct libc_minimum_period_rec **pptr)
{ struct libc_minimum_period_rec *ptr= *pptr;
 struct libc_minimum_period_rec *next;
 while (ptr!=NULL) {
   free libc bool opr_rec(ptr->when);
    next = ptr->next ;
```

```
free_mb_ptr((void *) ptr,2);
#if DMP MEM ANY
   dmp libc minimum period rec msize dec(12);
   dmp libc mem size dec(12);
#endif
   ptr = next ;
  *pptr = NULL;
struct libc_minimum_period_rec * copy_libc_minimum_period_rec(struct
libc minimum period rec *sou)
{ struct libc_minimum_period_rec *tar, *head, *prev;
 if (sou==NULL) return(NULL);
 prev = NULL;
 head = tar = new libc minimum period rec();
 while (sou!=NULL) {
   tar->constraint = sou->constraint;
   tar->when
                = (struct libc bool opr rec *)
copy_libc_bool_opr_rec(sou->when);
   sou = sou->next;
   if (sou!=NULL)
   { prev = tar;
     tar->next = new_libc_minimum_period rec();
     tar = tar->next;
 }
   return(head);
routine: free libc routing track rec()
  ------*/
void free_libc_routing_track_rec(struct libc routing track rec *ptr)
 struct libc routing track rec *next;
 while (ptr!=NULL) {
   free_text_buffer(ptr->layer_name);
   next = ptr->next ;
   free mb ptr((void *) ptr,3);
#if DMP MEM ANY
   dmp_libc_routing_track_rec_msize_dec(16);
   dmp libc mem size dec(16);
#endif
   ptr = next ;
}
routine: nfree_libc_routing_track_rec()
  *
void nfree_libc_routing_track_rec(struct libc_routing_track_rec **pptr)
```

```
{ struct libc routing track rec *ptr= *pptr;
 struct libc routing track rec *next;
 while (ptr!=NULL) {
   free text_buffer(ptr->layer_name);
    next = ptr->next ;
   free_mb_ptr((void *) ptr,3);
#if DMP MEM ANY
   dmp_libc_routing_track_rec_msize_dec(16);
   dmp libc mem size dec(16);
#endif
   ptr = next ;
 *pptr = NULL;
routine: free libc def_entry_rec()
  */
void free_libc_def_entry_rec(struct libc_def_entry_rec *ptr)
 struct libc_def_entry_rec *next;
 while (ptr!=NULL) {
   free_text_buffer(ptr-> def_name);
   next = ptr->next ;
   free mb ptr((void *) ptr,2);
#if DMP MEM ANY
   dmp_libc_def_entry_rec_msize_dec(12);
   dmp libc mem size dec(12);
#endif
   ptr = next ;
routine: nfree libc_def_entry_rec()
  */
void nfree_libc_def_entry_rec(struct libc_def_entry_rec **pptr)
{ struct libc def entry rec *ptr= *pptr;
 struct libc_def_entry_rec *next;
 while (ptr!=NULL) {
   free_text_buffer(ptr-> def_name);
    next = ptr->next ;
   free_mb_ptr((void *) ptr,2);
#if DMP_MEM_ANY
   dmp libc def entry rec_msize_dec(12);
   dmp libc mem size dec(12);
#endif
   ptr = next ;
 *pptr = NULL;
```

libc mem.c

```
}
routine: free_libc_define_value_rec()
  void free libc define value rec(struct libc_define_value_rec *ptr)
 struct libc_define_value_rec *next;
 while (ptr!=NULL) {
   free text buffer(ptr->str_val);
    next = ptr->next ;
   free mb ptr((void *) ptr,3);
#if DMP MEM ANY
   dmp_libc_define_value_rec_msize_dec(16);
   dmp libc mem size dec(16);
#endif
   ptr = next ;
}
routine: nfree_libc_define_value_rec()
  _____ */
void nfree_libc_define_value_rec(struct libc_define_value_rec **pptr)
{ struct libc_define_value_rec *ptr= *pptr;
 struct libc define value rec *next;
 while (ptr!=NULL) {
   free text buffer(ptr->str val);
    next = ptr->next ;
   free mb ptr((void *) ptr,3);
#if DMP_MEM_ANY
   dmp libc define value rec msize dec(16);
   dmp libc mem size dec(16);
#endif
   ptr = next ;
 *pptr = NULL;
struct libc_define_value_rec * copy_libc_define_value_rec(struct
libc define_value_rec *sou)
{ struct libc define value rec *tar, *head, *prev;
 if (sou==NULL) return(NULL);
 prev = NULL;
 head = tar = new_libc_define_value_rec();
 while (sou!=NULL) {
   tar-> def
                = sou-> def;
   tar->float val = sou->float val;
   tar->str val
               = (char *) copy_string(sou->str_val);
   sou = sou->next ;
   if (sou!=NULL)
```

```
{ prev = tar;
    tar->next = new_libc_define_value_rec();
    tar = tar->next;
  return(head);
routine: free_libc_glb_const_rec()
  void free libc glb const rec(struct libc glb_const_rec *ptr)
 struct libc_glb_const_rec *next;
 while (ptr!=NULL) {
   free text buffer(ptr->gc_name);
   next = ptr->next ;
   free_mb_ptr((void *) ptr,2);
#if DMP MEM_ANY
   dmp_libc_glb_const_rec_msize_dec(12);
   dmp libc mem_size_dec(12);
#endif
  ptr = next ;
}
routine: nfree libc glb const_rec()
  void nfree_libc_glb_const_rec(struct libc_glb_const_rec **pptr)
{ struct libc_glb_const_rec *ptr= *pptr;
 struct libc_glb_const_rec *next;
 while (ptr!=NULL) {
   free_text_buffer(ptr-> gc_name);
   next = ptr->next ;
   free mb ptr((void *) ptr,2);
#if DMP_MEM_ANY
   dmp_libc_glb_const_rec_msize_dec(12);
   dmp_libc_mem_size_dec(12);
   ptr = next ;
 *pptr = NULL;
routine: free libc def table rec()
  _____ */
void free libc def_table_rec(struct libc_def_table_rec *ptr)
{ int i0;
```

```
if (ptr==NULL) return;
   if (ptr->gc entry!=NULL) {
   for (i0=0;i0<sizeof_ptr_buffer(ptr->gc_entry);i0++)
      free_libc_glb_const_rec(ptr->gc_entry[i0]);
   free ptr buffer(ptr->gc entry);
   if (ptr->lib!=NULL) {
   for (i0=0;i0<sizeof_ptr_buffer(ptr->lib);i0++)
      free_libc_def_entry_rec(ptr->lib[i0]);
   free_ptr_buffer(ptr->lib);
   if (ptr-> cell!=NULL) {
   for (i0=0;i0<sizeof_ptr_buffer(ptr-> cell);i0++)
      free libc def entry rec(ptr-> cell[i0]);
   free ptr buffer(ptr-> cell);
   if (ptr->pin!=NULL) {
   for (i0=0;i0<sizeof ptr buffer(ptr->pin);i0++)
      free_libc_def_entry_rec(ptr->pin[i0]);
   free_ptr_buffer(ptr->pin);
   free_mb_ptr((void *) ptr,3);
#if DMP MEM ANY
   dmp libc def table rec msize dec(16);
   dmp_libc_mem_size_dec(16);
#endif
/* ------
  routine: nfree libc def table rec()
  */
void nfree_libc_def_table_rec(struct libc_def_table_rec **pptr)
{ struct libc_def_table_rec *ptr= *pptr;
int i0;
 if (ptr==NULL) return;
   if (ptr->gc_entry!=NULL) {
   for (i0=0;i0<sizeof_ptr_buffer(ptr->gc_entry);i0++)
      free_libc_glb const rec(ptr->gc entry[i0]);
   free_ptr_buffer(ptr->gc_entry);
   if (ptr-> lib!=NULL) {
   for (i0=0;i0<sizeof_ptr_buffer(ptr-> lib);i0++)
      free_libc_def_entry_rec(ptr-> lib[i0]);
   free_ptr_buffer(ptr-> lib);
   if (ptr-> cell!=NULL) {
   for (i0=0;i0<sizeof_ptr_buffer(ptr-> cell);i0++)
      free libc def entry rec(ptr-> cell[i0]);
   free_ptr_buffer(ptr-> cell);
   if (ptr-> pin!=NULL) {
   for (i0=0;i0<sizeof ptr buffer(ptr-> pin);i0++)
      free_libc_def_entry_rec(ptr-> pin[i0]);
   free_ptr_buffer(ptr-> pin);
   free_mb_ptr((void *) ptr,3);
```

```
#if DMP_MEM_ANY
    dmp_libc_def_table_rec_msize_dec(16);
    dmp_libc_mem_size_dec(16);
#endif
  *pptr = NULL;
}
```

libc_mem.c

```
#ifndef _H_libc_mem_DEF
#define _H_libc_mem DEF 1
#include <stdio.h>
#include "dmp util.h"
#include "libcds.h"
#if DMP MEM ANY
struct any_unit_rec *new_any_unit_rec(void);
struct libc_name_list_rec *new_libc_name_list_rec(void);
struct libc_name_list_list *new_libc_name_list_list(void);
struct libc_float_list_rec *new_libc_float_list_rec(void);
struct libc_float_list_list *new_libc_float_list list(void);
struct libc define rec *new libc define rec(void);
struct libc_cell_area_rec *new_libc_cell_area_rec(void);
struct libc k_factor_rec *new_libc k factor rec(void);
struct libc_lib_rec *new_libc_lib rec(void);
struct libc_lu_table_template_rec *new_libc lu table template rec(void);
struct libc_oc_power_rail_rec *new_libc_oc_power_rail_rec(void);
struct libc_operating_condition_rec *new libc operating condition rec(void);
struct libc power supply rec *new_libc_power_supply_rec(void);
struct libc_timing_range_rec *new_libc_timing range rec(void);
struct libc_type_rec *new libc type rec(void);
struct libc fanout length rec *new libc fanout length rec(void);
struct libc_wire_load_rec *new_libc_wire_load_rec(void);
struct libc_wire_load_from_area_rec *new libc wire load from area rec(void);
struct libc_wire_load_selection_rec *new_libc_wire_load_selection_rec(void);
struct libc_cell_rec *new_libc_cell_rec(void);
struct libc_memory_write_rec *new_libc_memory_write rec(void);
struct libc_bool_opr_rec *new_libc_bool_opr_rec(void);
struct libc_pin_rec *new_libc_pin_rec(void);
struct libc_ff_latch_rec *new_libc_ff_latch_rec(void);
struct libc_internal_power *new_libc_internal_power(void);
struct libc_leakage_power *new_libc_leakage_power(void);
struct libc memory rec *new libc memory rec(void);
struct libc_piece_value_rec *new_libc_piece value rec(void);
struct libc_float_rec *new_libc_float_rec(void);
struct libc table val rec *new libc table val rec(void);
struct libc_timing_rec *new_libc timing rec(void);
struct libc_min_pulse_width_rec *new_libc min pulse width rec(void);
struct libc_minimum period rec *new libc minimum period rec(void);
struct libc_routing_track_rec *new_libc_routing track rec(void);
struct libc_def_entry_rec *new_libc_def_entry_rec(void);
struct libc_define_value_rec *new_libc_define_value_rec(void);
struct libc_glb_const_rec *new_libc glb const rec(void);
struct libc_def_table_rec *new_libc_def_table_rec(void);
#else
#define new_any_unit_rec() (struct any_unit_rec *) get_mb_ptr(1)
#define new_libc_name_list_rec() (struct libc_name list rec *) get mb ptr(1)
#define new_libc_name_list_list() (struct libc_name_list_list *)
get mb ptr(2)
#define new_libc_float_list_rec() (struct libc_float_list_rec *)
get mb ptr(1)
#define new_libc_float_list_list() (struct libc_float_list_list *)
get mb ptr(1)
#define new_libc_define rec() (struct libc define rec *) get mb ptr(3)
#define new_libc_cell_area_rec() (struct libc_cell_area rec *) get mb ptr(2)
#define new_libc_k_factor_rec() (struct libc_k_factor_rec *) get mb ptr(29)
#define new_libc_lib_rec() (struct libc_lib_rec *) get_mb_ptr(28)
```

```
#define new_libc_lu_table_template_rec() (struct libc_lu_table_template_rec
*) get mb ptr(7)
#define new_libc_oc_power_rail_rec() (struct libc_oc_power_rail_rec *)
get mb ptr(2)
#define new libc operating condition rec() (struct
libc operating condition rec *) get_mb_ptr(6)
#define new_libc_power_supply_rec() (struct libc_power_supply_rec *)
get mb ptr(3)
#define new_libc_timing_range_rec() (struct libc_timing range_rec *)
get mb ptr(3)
#define new libc type rec() (struct libc type rec *) get_mb_ptr(5)
#define new libc fanout_length_rec() (struct libc_fanout_length_rec *)
get mb ptr(8)
#define new_libc_wire_load_rec() (struct libc_wire_load_rec *) get_mb_ptr(6)
#define new_libc_wire_load_from_area_rec() (struct
libc wire load from area rec *) get_mb_ptr(3)
#define new libc wire load selection rec() (struct
libc wire load selection rec *) get mb ptr(2)
#define new libc_cell_rec() (struct libc_cell_rec *) get_mb_ptr(23)
#define new_libc_memory_write_rec() (struct libc_memory_write_rec *)
get mb ptr(2)
#define new_libc_bool_opr_rec() (struct libc_bool_opr_rec *) get_mb_ptr(3)
#define new_libc_pin_rec() (struct libc_pin_rec *) get_mb ptr(27)
#define new_libc_ff_latch_rec() (struct libc_ff_latch_rec *) get_mb_ptr(15)
#define new libc internal_power() (struct libc_internal_power *)
get mb ptr(9)
#define new_libc_leakage_power() (struct libc_leakage_power *) get_mb_ptr(2)
#define new_libc_memory_rec() (struct libc_memory_rec *) get_mb_ptr(2)
#define new_libc_piece_value_rec() (struct libc_piece_value_rec *)
qet mb ptr(9)
#define new_libc_float_rec() (struct libc_float_rec *) get_mb_ptr(0)
#define new_libc_table_val_rec() (struct libc_table_val_rec *) get_mb_ptr(5)
#define new libc timing rec() (struct libc_timing rec *) get_mb ptr(22)
#define new_libc_min_pulse_width_rec() (struct libc_min_pulse_width_rec *)
get mb ptr(3)
#define new_libc_minimum_period_rec() (struct libc_minimum_period_rec *)
get mb ptr(2)
#define new libc routing track rec() (struct libc_routing_track_rec *)
get mb ptr(3)
#define new libc def entry rec() (struct libc def_entry_rec *) get_mb_ptr(2)
#define new_libc_define_value_rec() (struct libc_define_value_rec *)
get mb ptr(3)
#define new_libc_glb_const_rec() (struct libc glb const_rec *) get_mb_ptr(2)
#define new_libc_def_table_rec() (struct libc_def_table_rec *) get_mb_ptr(3)
#endif
void free any unit rec(struct any unit rec *);
void nfree any unit_rec(struct any_unit_rec **);
void free libc name list_rec(struct libc_name_list_rec *);
void nfree libc name list rec(struct libc name_list_rec **);
struct libc_name_list_rec * copy_libc_name_list_rec(struct libc_name_list_rec
*);
void free libc name list list(struct libc name_list_list *);
void nfree_libc_name_list_list(struct libc_name_list_list **);
void free libc float list rec(struct libc_float_list_rec *);
void nfree libc float list rec(struct libc_float_list_rec **);
void free_libc_float_list_list(struct libc_float_list_list *);
void nfree libc_float_list_list(struct libc_float_list_list **);
```

```
void free_libc_define_rec(struct libc_define_rec *);
void nfree_libc_define_rec(struct libc_define_rec **);
void free libc cell area_rec(struct libc_cell_area_rec *);
void nfree_libc_cell_area_rec(struct libc_cell_area_rec **);
void free libc k_factor rec(struct libc_k_factor_rec *);
void nfree libc k factor rec(struct libc_k_factor_rec **);
void free_libc_lib_rec(struct libc_lib_rec *);
void nfree_libc_lib_rec(struct libc_lib_rec **);
void free libc_lu_table_template_rec(struct libc_lu_table_template_rec *);
void nfree libc_lu_table_template_rec(struct libc lu_table_template_rec **);
void free libc oc power_rail_rec(struct libc oc_power_rail_rec *);
void nfree libc oc power rail rec(struct libc oc power rail_rec **);
void free_libc_operating_condition_rec(struct libc_operating_condition_rec
void nfree libc operating condition_rec(struct libc_operating_condition_rec
void free libc_power_supply_rec(struct libc_power_supply_rec *);
void nfree libc power_supply_rec(struct libc_power_supply_rec **);
void free libc_timing_range_rec(struct libc_timing_range_rec *);
void nfree_libc_timing_range_rec(struct libc_timing_range_rec **);
void free libc_type_rec(struct libc_type_rec *);
void nfree_libc_type_rec(struct libc_type_rec **);
void free_libc_fanout_length_rec(struct libc_fanout_length_rec *);
void nfree libc_fanout_length rec(struct libc_fanout length rec **);
void free libc_wire_load_rec(struct libc_wire_load_rec *);
void nfree libc_wire_load_rec(struct libc_wire_load_rec **);
void free libc_wire_load_from_area_rec(struct libc_wire_load_from_area_rec
void nfree libc wire load from area rec(struct libc_wire_load_from_area_rec
void free libc_wire_load_selection_rec(struct libc_wire_load_selection_rec
void nfree libc wire load selection rec(struct libc_wire_load_selection_rec
**);
void free_libc_cell_rec(struct libc_cell_rec *);
void nfree libc_cell_rec(struct libc_cell_rec **);
void free_libc_memory_write_rec(struct libc_memory_write_rec *);
void nfree libc memory write rec(struct libc_memory_write_rec **);
struct libc_memory_write_rec * copy_libc_memory_write_rec(struct
libc memory write_rec *);
void free_libc_bool_opr_rec(struct libc_bool opr rec *);
void nfree_libc_bool_opr_rec(struct libc bool opr rec **);
struct libc_bool_opr_rec * copy_libc_bool_opr_rec(struct libc_bool_opr_rec
void free libc_pin_rec(struct libc_pin_rec *);
void nfree_libc_pin_rec(struct libc_pin_rec **);
struct libc_pin_rec * copy_libc_pin_rec(struct libc_pin_rec *);
void free_libc_ff_latch_rec(struct libc_ff_latch_rec *);
void nfree_libc_ff_latch_rec(struct libc_ff_latch rec **);
void free libc internal power(struct libc_internal_power *);
void nfree_libc_internal_power(struct libc_internal power **);
struct libc internal power * copy_libc_internal_power(struct
libc internal_power *);
void free_libc_leakage_power(struct libc_leakage_power *);
void nfree_libc_leakage_power(struct libc_leakage_power **);
struct libc leakage power * copy_libc_leakage_power(struct libc_leakage_power
*);
```

```
void free_libc_memory_rec(struct libc_memory_rec *);
void nfree_libc_memory_rec(struct libc_memory_rec **);
void free_libc_piece_value_rec(struct libc_piece_value_rec *);
void nfree_libc_piece_value_rec(struct libc_piece_value_rec **);
 struct libc_piece_value_rec * copy_libc_piece_value rec(struct
libc_piece value rec *);
void free_libc_float_rec(struct libc_float_rec *);
void nfree_libc_float_rec(struct libc_float_rec **);
struct libc_float_rec * copy_libc_float_rec(struct libc_float_rec *);
void free_libc_table_val_rec(struct libc_table_val_rec *);
void nfree_libc_table_val_rec(struct libc_table_val_rec **);
struct libc_table_val_rec * copy_libc_table_val_rec(struct libc_table_val_rec
*);
void free libc_timing_rec(struct libc_timing_rec *);
void nfree_libc_timing_rec(struct libc_timing_rec **);
struct libc_timing_rec * copy_libc_timing_rec(struct libc_timing_rec *);
void free_libc_min_pulse_width_rec(struct libc_min_pulse_width_rec *);
void nfree_libc_min_pulse_width_rec(struct libc_min_pulse_width_rec **);
struct libc_min_pulse_width_rec * copy_libc_min_pulse_width_rec(struct
libc_min_pulse_width_rec *);
void free_libc_minimum_period_rec(struct libc_minimum_period_rec *);
void nfree_libc_minimum_period_rec(struct libc_minimum_period_rec **);
struct libc_minimum_period_rec * copy_libc_minimum_period_rec(struct
libc_minimum_period rec *);
void free_libc_routing_track_rec(struct libc_routing_track_rec *);
void nfree_libc_routing_track_rec(struct libc_routing_track_rec **);
void free_libc_def_entry_rec(struct libc_def_entry_rec *);
void nfree_libc_def_entry_rec(struct libc_def_entry_rec **);
void free_libc_define_value_rec(struct libc_define_value_rec *);
void nfree_libc_define_value_rec(struct libc_define_value_rec **);
struct libc_define_value_rec * copy_libc_define_value_rec(struct
libc_define_value_rec *);
void free_libc_glb_const_rec(struct libc_glb_const_rec *);
void nfree_libc_glb_const_rec(struct libc_glb_const_rec **);
void free_libc_def_table_rec(struct libc_def_table_rec *);
void nfree_libc_def_table_rec(struct libc_def_table_rec **);
#endif
```

```
#include "libc_def.h"
public
libc bool opr rec *libc opr handle(
     libc bool type E op type,
     int value)
{ libc bool opr rec * bor;
 bor = new_libc_bool_opr_rec();
 bor->type = op_type;
 if (op type == ID B)
   bor->u.id name = (text buffer *) value;
 else
   bor->u.value = value;
 return(bor);
public
void libc_opr_id_print(
     FILE *outp,
     char *name,
     libc cell rec *cell,
     int idx)
{ libc pin rec *pin;
 libc ff latch rec *ffp;
 pin = libc_cell_find_pin_by_name(cell,name,-99999);
 if (pin == NULL) {
                   /* (idx >= 0) and (this ID B is not a bundle or
   /* ---- for index of ff_latch (IQ, IQN) */
   if (idx > -10000) {
#if 1
     fprintf(outp,cell->_tlib->bus_naming_style,name,idx);
#else
     for (ffp=cell->ff latch;ffp;ffp=ffp->next) {
      if (strcmp(cell->ff_latch->Q_name, name) == 0 | |
        strcmp(cell->ff_latch->QN_name, name) == 0) {
        fprintf(outp,cell-> tlib->bus naming style,name,idx);
      return;
     fprintf(outp, "%s", name);
#endif
   else
     fprintf(outp, "%s", name);
 }
```

```
else {
    if (pin->is_bus)
      fprintf(outp,cell->_tlib->bus_naming_style,name,idx);
    else if (pin->members != NULL) {
      libc_name_list rec *np;
      int i;
      for (np=pin->members,i=0;np!=NULL;np=np->next,i++) {
      if (i == idx)
       break;
      assert(np != NULL);
      fprintf(outp, "%s", np->name);
    else
      fprintf(outp, "%s", name);
/* ----- */
public
void libc opr print (
      FILE *outp,
      libc_bool_opr_rec *opr,
      libc_cell_rec *cell,
      int idx)
{ libc_pin_rec *pin;
  if (opr == NULL)
   return;
  switch (opr->type) {
   case XOR B :
   case OR B :
   case AND_B :
      if (opr->L->type >= opr->type)
      libc opr print(outp,opr->L,cell,idx);
     else {
        fprintf(outp, "(");
      libc_opr_print(outp,opr->L,cell,idx);
        fprintf(outp,")");
     fprintf(outp, "%s", (opr->type == AND_B)? " " : (opr->type == OR_B)? " |
      if (opr->R->type >= opr->type)
      libc opr print(outp,opr->R,cell,idx);
     else {
       fprintf(outp, "(");
      libc_opr_print(outp,opr->R,cell,idx);
       fprintf(outp,")");
     break;
   case NOT_B :
     fprintf(outp,"!");
     if (opr->R->type >= NOT B)
     libc_opr_print(outp,opr->R,cell,idx);
     else {
```

```
fprintf(outp,"(");
     libc_opr_print(outp,opr->R,cell,idx);
        fprintf(outp,")");
     break;
   case ID B :
     libc_opr_id_print(outp,opr->u.id_name,cell,idx);
     break;
   case INDEX B :
     libc_opr_print(outp,opr->L,cell,opr->R->u.value);
     break;
   case ZERO B :
   case ONE B :
   case CONST B :
     fprintf(outp, "%d", opr->u.value);
}
public
void libc_opr_power_when_print(
     FILE *outp,
     libc_bool_opr_rec *opr,
     libc_cell_rec *cell,
     int idx)
{ libc_pin_rec *pin;
 if (opr == NULL)
   return;
 switch (opr->type) {
   case XOR B :
   case OR B :
   case AND B :
     if (opr->L->type >= opr->type)
     libc opr power when print(outp,opr->L,cell,idx);
     else {
        fprintf(outp,"(");
     libc_opr_power_when_print(outp,opr->L,cell,idx);
        fprintf(outp,")");
     fprintf(outp, "%s", (opr->type == AND_B)? "*" : (opr->type == OR B)? "+"
     if (opr->R->type >= opr->type)
     libc_opr_power when print(outp,opr->R,cell,idx);
     else {
       fprintf(outp,"(");
     libc_opr_power_when_print(outp,opr->R,cell,idx);
       fprintf(outp,")");
     break;
   case NOT B :
     if (opr->R->type == ID B) {
       libc_opr_id_print(outp,opr->R->u.id name,cell,idx);
       fprintf(outp, "0");
     }
```

```
else {
        fprintf(outp,"!");
        if (opr->R->type >= NOT B)
        libc_opr_power_when_print(outp,opr->R,cell,idx);
        else {
          fprintf(outp,"(");
        libc_opr_power_when_print(outp,opr->R,cell,idx);
          fprintf(outp,")");
      break;
    case ID_B :
      libc_opr_id_print(outp,opr->u.id_name,cell,idx);
      fprintf(outp,"1");
      break;
    case INDEX B :
     libc_opr_power_when_print(outp,opr->L,cell,opr->R->u.value);
      break;
    case ZERO B :
    case ONE B :
    case CONST B :
      fprintf(outp, "%d", opr->u.value);
     break;
}
/* ================ */
```

```
______ */
#include "libc def.h"
char *libc_time_delay_str(
       delay_model_E de)
   switch (de) {
     case GENERIC_ECL : return("generic_ecl");
case GENERIC_CMOS : return("generic_cmos");
case TABLE_LOOKUP : return("table_lookup");
case CMOS2 : return("table_lookup");
                           return("cmos2");
     case CMOS2 :
     case PIECEWISE_CMOS : return("piecewise_cmos");
     deafult :
     assert(0);
  }
/* ----- */
public
void libc_time_delay_model(
     delay model E de)
{ char msg[256];
  if (tech lib->delay model == UNKNOW M) {
   if (de != GENERIC_CMOS && de != TABLE LOOKUP) {
     sprintf(msg, "This statement belongs to %s delay model, \n
only generic cmos and table lookup delay model are supported.",
     libc_time_delay_str(de));
     libcerror(msg);
   tech_lib->delay_model = de;
  else if (tech_lib->delay_model == de)
   return ;
  else {
   sprintf(msg,"\"%s\" statement in \"%s\" delay model.",
     libc_time_delay_str(de), libc_time_delay_str(tech_lib->delay_model));
   libcerror(msg);
   if (tech lib->delay model != GENERIC CMOS &&
       tech lib->delay model != TABLE LOOKUP)
     tech_lib->delay_model = de;
/* ========================== */
public
void libc_time init(
```

```
text_buffer *timing_name)
  lib_timing = new libc timing rec();
  lib_timing->t name = timing name;
  lib_timing->_current pin = lib pin;
  /* ---- set default value */
  lib_timing->intrinsic_fall = tech_lib->default_intrinsic_fall;
  lib_timing->intrinsic_rise = tech_lib->default_intrinsic_rise;
  if (lib_pin->direction == INOUT_E) {
    lib_timing->fall resistance
                               = tech_lib->default inout pin_fall res;
    lib_timing->rise_resistance
                                 = tech_lib->default_inout pin rise res;
  else {
    lib_timing->fall_resistance
                                 = tech_lib->default_output_pin_fall_res;
    lib_timing->rise resistance
                                  = tech_lib->default_output_pin_rise res;
}
   */
public
void libc_time_finish(void)
{ char msg[128], *type name;
  int t_type;
 switch (lib_timing->timing_type) {
   case COMBINATIONAL_T : t_type = 1; type_name = "combination";
     break;
   case RISING_EDGE T :
                          t_type = 1; type_name = "rising edge";
     break;
   case FALLING EDGE T :
                          t_type = 1; type_name = "falling edge";
     break;
   case PRESET T :
                           t_type = 3; type_name = "preset";
     break;
   case CLEAR T :
                           t_type = 3; type_name = "clear";
   case THREE_STATE_DISABLE_T :t_type = 1;
                                            type_name =
"three_state_disable"; break;
   case THREE STATE_ENABLE_T :
                                t_type = 1; type_name =
"three_state_enable";
                    break;
   case HOLD RISING T :
                          t_type = 4; type_name = "hold_rising";
     break;
   case HOLD_FALLING_T :
                          t_type = 4; type_name = "hold falling";
     break;
   case SETUP_RISING T :
                          t_type = 4; type_name = "setup_rising";
     break;
   case SETUP_FALLING_T :
                          t_type = 4; type_name = "setup_falling";
   case RECOVERY_RISING_T : t_type = 4; type_name = "recovery_rising";
   case RECOVERY_FALLING_T : t_type = 4; type_name = "recovery_falling";
     break;
```

```
case REMOVAL_RISING_T : t_type = 4; type_name = "removal_rising";
     case REMOVAL_FALLING_T : t_type = 4; type_name = "removal_falling";
      break;
    case SKEW_RISING_T :
                              t_type = 2; type_name = "skew_rising";
      break;
    case SKEW FALLING T :
                             t_type = 2; type_name = "skew_falling";
      break;
    case NON_SEQ HOLD RISING T :
                                    t_type = 2; type_name =
 "non_seq_hold_rising"; break;
    case NON_SEQ_HOLD_FALLING_T :
                                   t_tppe = 2;
                                                 type name =
 "non_seq hold falling"; break;
    case NON_SEQ_SETUP_RISING_T : t_type = 2;
                                                type name =
 "non_seq_setup_rising"; break;
    case NON_SEQ_SETUP_FALLING_T : t_type = 2; type_name =
 "non seq setup_falling";
                              break;
    case NOCHANGE HIGH HIGH T :
                                     t_type = 2; type name =
 "nochange_high_high";
                       break;
    case NOCHANGE HIGH LOW T :
                                     t_type = 2; type name =
"nochange high low";
                        break;
    case NOCHANGE_LOW_HIGH T :
                                     t_type = 2; type_name =
"nochange low high";
                        break;
    case NOCHANGE_LOW_LOW_T : t_type = 2; type_name = "nochange_low_low";
      break:
    default :
      libcerror("timing_type in timing group is not specified.");
      return;
  }
  if (tech_lib->delay_model != TABLE_LOOKUP)
    return;
  if (t_type == 1) {
    if (lib_timing->cell_rise == NULL && lib_timing->rise propagation ==
      sprintf(msg, "cell_rise or rise_propagation table is required for
timing_type %s.",type_name);
      libcerror(msg);
    else if (lib_timing->cell_rise != NULL && lib_timing->rise_propagation !=
      sprintf(msg, "only one table is allowed of cell_rise or rise_propagation
tables for timing_type %s.",type_name);
      libcerror(msg);
    if (lib timing->cell_fall == NULL && lib_timing->fall_propagation ==
NULL) {
      sprintf(msg, "cell_fall or fall_propagation table is required for
timing_type %s.",type name);
      libcerror(msg);
    else if (lib_timing->cell_fall == NULL && lib_timing->fall_propagation ==
NULL) {
      sprintf(msg, "only one table is allowed of cell_fall or fall propagation
tables for timing_type %s.",type_name);
```

```
libcerror(msg);
    else if (lib_timing->cell_rise != NULL && lib timing->cell fall == NULL)
      sprintf(msg, "cell_fall table is required for timing_type
%s.", type name);
      libcerror(msq);
    else if (lib_timing->rise_propagation != NULL && lib_timing-
>fall_propagation == NULL) {
      sprintf(msg, "fall_propagation table is required for timing type
%s.", type name);
      libcerror(msg);
    }
    if (lib_timing->rise_transition == NULL) {
      sprintf(msg, "rise_transition table is required for timing_type
%s.", type name);
      libcerror(msg);
    if (lib_timing->fall_transition == NULL) {
      sprintf(msg, "fall_transition table is required for timing_type
%s.",type_name);
      libcerror(msg);
  }
  else if (t_type == 2) {
    if (lib_timing->rise_constraint == NULL) {
      sprintf(msg, "rise_constraint table is required for timing type
%s.",type_name);
      libcerror(msg);
    if (lib_timing->fall_constraint == NULL) {
      sprintf(msg, "fall_constraint table is required for timing_type
%s.", type name);
      libcerror(msq);
  else if (t_type == 3) {
    if (lib_timing->cell_rise == NULL && lib_timing->rise_propagation == NULL
        lib_timing->cell_fall == NULL && lib_timing->fall_propagation ==
NULL) {
      sprintf(msg, "delay or propagation table is required for timing_type
%s.",type_name);
      libcerror(msg);
    if (lib_timing->rise_transition == NULL && lib_timing->fall_transition ==
NULL) {
      sprintf(msg,"transition table is required for timing type
%s.",type_name);
      libcerror (msg);
  else if (t type == 4) {
    if (lib_timing->rise_constraint == NULL && lib_timing->fall constraint ==
NULL) {
```

```
sprintf(msg, "constraint table is required for timing type
%s.", type name);
      libcerror(msq);
}
   public
void libc_time_piecewise(
      int piece,
      int field,
      float value)
{ libc_piece_value_rec *new, *p, *prev=NULL;
  /* ---- search first */
  for (p=lib_timing->piecewise;p!=NULL;prev=p,p=p->next) {
    if (p->piece == piece)
      goto insert_value;
    if (p->piece > piece)
      break;
  /* ---- insert a new entry (fanout from small->large) */
  new = new_libc_piece_value_rec();
  new->piece
                              = piece;
  /* ---- set default value */
  new->fall_delay_intercept
                              = tech_lib->default fall delay intercept;
  new->rise_delay intercept
                              = tech_lib->default rise delay intercept;
  new->fall_nonpaired twin
                              = tech_lib->default fall nonpaired twin;
  new->rise nonpaired twin
                              = tech_lib->default_rise_nonpaired twin;
                              = tech_lib->default_fall_pin_resistance;
  new->fall pin resistance
  new->rise pin resistance
                              = tech_lib->default_rise_pin_resistance;
                              = tech_lib->default_fall_wire_resistance;
  new->fall_wire_resistance
  new->rise_wire_resistance
                              = tech_lib->default_rise_wire_resistance;
  if (prev == NULL)
    lib_timing->piecewise = new;
    prev->next = new;
  new->next = p;
  p = new;
insert_value :
 switch (field) {
    case 1 :
                 p->fall delay intercept = value;
                                                      break;
    case 2 :
                 p->fall nonpaired twin = value;
                                                           break;
    case 3 :
                 p->fall pin resistance = value;
                                                           break;
    case 4 :
                 p->fall_wire_resistance = value;
                                                     break;
   case 5 :
                 p->rise_delay_intercept = value;
                                                      break;
   case 6 :
                 p->rise nonpaired twin = value;
                                                           break;
   case 7 :
                 p->rise pin resistance = value;
                                                           break;
   case 8 :
                 p->rise_wire_resistance = value;
                                                     break:
   default :
                 assert(0);
}
```

```
public
float_buffer *libc_time_copy_float_buf(
     float buffer *fbuf)
{ int size;
  float buffer *result;
  if (fbuf == NULL)
   return (NULL);
 size = sizeof_float buffer(fbuf);
 result = get_float_buffer(size);
 memcpy(result,fbuf,size*sizeof(float));
 return(result);
/* =========== */
/* ---- for both timing and power */
public
int libc_time_find_template(
     text buffer *name,
     libc_lu_table_template_rec *head,
     libc table val rec *tbl)
{ libc_lu_table_template_rec *p;
 int i,code = 0;
                      /* not found */
 if (strcmp(name, "scalar") == 0) {
   tbl->_tbl = NULL;
                            /* scalar table */
   code = 1;
                            /* found */
 }
 else {
   for (p=head;p!=NULL;p=p->next) {
     if (strcmp(p->tt name, name) == 0) {
       tbl->_tbl = p;
       code = 1;
                      /* found */
       /* ---- copy default index */
       if (p->index_1 != NULL) {
         tbl->index1 = get_float_buffer(sizeof_float_buffer(p->index 1));
         for (i=0;i<sizeof_float_buffer(p->index 1);i++)
           tbl->index1[i] = p->index_1[i];
       if (p->index_2 != NULL) {
         tbl->index2 = get_float_buffer(sizeof_float_buffer(p->index 2));
         for (i=0;i<sizeof_float_buffer(p->index 2);i++)
           tbl->index2[i] = p->index 2[i];
       if (p->index 3 != NULL) {
         tbl->index3 = get_float_buffer(sizeof_float_buffer(p->index_3));
         for (i=0;i<sizeof_float buffer(p->index 3);i++)
           tbl->index3[i] = p->index 3[i];
       break;
     }
   }
```

libc t

```
free_text_buffer(name);
  return(code);
/* ------ */
public
void libc time values handle (
     libc_table_val_rec *timing_tbl,
     libc_float_list_list *f_lists)
{ int size1, size2, size3;
  libcerror("only one value is allowed in scalar table.");
   timing tbl->scalar val = f lists->v list->fvalue;
    free_libc_float_list_list(f_lists);
 else {
   size1 = (timing_tbl->index1)? sizeof_float_buffer(timing tbl->index1) :
   size2 = (timing_tbl->index2)? sizeof_float_buffer(timing_tbl->index2) :
0;
   size3 = (timing_tbl->index3)? sizeof_float_buffer(timing_tbl->index3) :
   timing_tbl->values =
libc_util_float_lists3buffer(size1, size2, size3, f_lists);
}
public
void libc_time_handle(
     int table type)
{ libc_table_val rec **tbl;
 switch (table_type) {
   case 0 :
     free_libc_table_val_rec(lib timing tbl);
     lib_timing_tbl = NULL;
     return;
   case 1 :
                tbl = &(lib_timing->cell_rise);
                                                      break;
   case 2 :
                tbl = &(lib_timing->cell_fall);
                                                      break;
   case 3 :
                tbl = &(lib_timing->rise_propagation);
                                                      break;
                tbl = &(lib_timing->fall_propagation);
   case 4 :
                                                      break;
   case 5 :
                tbl = &(lib_timing->rise transition);
                                                      break;
                tbl = &(lib timing->fall transition);
   case 6 :
                                                      break:
                tbl = &(lib_timing->rise_constraint);
   case 7 :
                                                      break;
                tbl = &(lib_timing->fall constraint);
                                                      break;
 free libc table val rec(*tbl);
 (*tbl) = lib timing tbl;
 lib_timing_tbl = NULL;
}
```

libc_t

```
----- */
public
void libc_time_power_handle(
     int table type)
{ libc_table val_rec **tbl;
 switch (table_type) {
   case 0 :
               tbl = &(lib_int_power->fall_power);
                                                    break;
   case 1 :
               tbl = &(lib_int_power->rise power);
                                                    break;
   case 2 :
              tbl = &(lib_int_power->power);
                                                         break;
 free_libc_table_val_rec(*tbl);
  (*tbl) = lib timing tbl;
 lib_timing_tbl = NULL;
  public
void libc_time_minimum_period(
     int is high,
     float value)
{ libc_min_pulse_width_rec *mpp, *nmpp;
 if (lib_pin->min_pulse_width == NULL) {
   nmpp = new_libc_min_pulse_width_rec();
   nmpp->when = NULL;
   lib pin->min pulse width = nmpp;
 else {
   for(mpp=lib_pin->min_pulse_width;mpp->next;mpp=mpp->next);
   if (mpp->when == NULL)
     nmpp = mpp;
   else {
     nmpp = new_libc_min_pulse_width rec();
     mpp->next = nmpp;
 if (is high)
   nmpp->constraint_high = value;
 else
   nmpp->constraint low = value;
  */
```

```
_____ */
#include "libc_def.h"
/* =========== */
public
void libc_util_name_list1(
    YYSTYPE *r,
    YYSTYPE *r1)
{ libc_name_list_rec *np;
 np = new_libc_name_list_rec();
 np->name = r1->string;
 r->name_list.head = r->name_list._tail = np;
/* ----- */
void libc_util_name_list2(
    YYSTYPE *r,
    YYSTYPE *r1,
    YYSTYPE *r3)
{ libc_name_list_rec *np;
 np = new_libc_name_list_rec();
 np->name = r3->string;
 r->name_list.head = r1->name_list.head;
 r1->name_list. tail->next = np;
 r->name_list._tail = np;
public
void libc_util_float_list1(
    YYSTYPE *r,
    YYSTYPE *r1)
{ libc_float_list_rec *vp;
 vp = new_libc_float_list_rec();
 vp->fvalue = r1->real val;
 r->float_list.head = r->float_list._tail = vp;
/* ----- */
public
void libc util float list2(
    YYSTYPE *r,
    YYSTYPE *r1.
    YYSTYPE *r3)
```

```
{ libc_float_list_rec *vp;
  vp = new_libc float list rec();
  vp->fvalue = r3->real_val;
  r->float_list.head = r1->float_list.head;
  r1->float_list._tail->next = vp;
  r->float_list._tail = vp;
public
void libc_util_float_list_list1(
     YYSTYPE *r,
     YYSTYPE *r1)
{ libc_float list list *vp;
  vp = new_libc_float_list list();
  vp->v_list = r1->float_list.head;
  r->float_list_list.head = r->float_list_list._tail = vp;
}
  */
public
void libc_util_float_list_list2(
     YYSTYPE *r,
     YYSTYPE *r1,
     YYSTYPE *r3)
{ libc_float_list_list *vp;
 vp = new_libc_float list list();
 vp->v_list = r3->float list.head;
 r->float_list_list.head = r1->float_list_list.head;
 r1->float_list_list._tail->next = vp;
 r->float_list_list. tail = vp;
/* =========== */
any_unit_rec *libc_util_unit(
     float unit)
{ any_unit_rec *up;
 up = new_any_unit_rec();
 up->unit = unit;
 return(up);
public
float_buffer *libc_util float list2buffer(
     libc_float_list_rec *f list)
{ int i, size, is inc=1, error=0;
 libc_float_list_rec *p;
```

```
float_buffer *fbuf;
  float v1, v2;
  if (f_list == NULL)
    return(NULL);
  for(p=f_list,size=0;p!=NULL;p=p->next,size++);
  fbuf = get_float buffer(size);
  if (size >= 2) {
    if (f_list->fvalue >= f_list->next->fvalue)
      is_inc = 1;
    else
      is_inc = 0;
  for (p=f_list,i=0;p!=NULL;p=p->next,i++) {
    fbuf[i] = p->fvalue;
    if (i > 0) {
      if (is inc) {
      if (fbuf[i-1] <= fbuf[i]) {</pre>
        v1 = fbuf[i-1];
        v2 = fbuf[i];
        error = 1;
      else {
      if (fbuf[i-1] >= fbuf[i]) {
        v1 = fbuf[i-1];
        v2 = fbuf[i];
        error = 1;
    }
  free_libc_float_list_rec(f_list);
  if (error) {
    char msg[256];
    sprintf(msg, "Value %G shall %s than value %G in
index",v1,is_inc?"larger":"smaller",v2);
    libc error (msg);
  return(fbuf);
/* ------*/
public
float_buffer **libc_util_float_lists2buffer(
      libc_float_list list *f lists)
{ int i,j,size1,size2;
  libc_float_list_list *q;
  libc_float list rec *p;
  float buffer **fbuf;
  for (q=f_lists,sizel=0;q!=NULL;q=q->next,sizel++);
  fbuf = (float_buffer **) get_ptr_buffer(size1);
  for (p=f_lists->v_list,size2=0;p!=NULL;p=p->next,size2++);
```

```
if (size1 == 1) {
                             /* 1D array */
    fbuf[0] = (float_buffer *) get_float buffer(size2);
    for (p=f_lists->v_list, j=0; p!=NULL; p=p->next, j++)
      fbuf[0][j] = p->fvalue;
  else {
                        /* 2D array */
    for (q=f_lists, i=0; q!=NULL; q=q->next, i++) {
      fbuf[i] = (float_buffer *) get_float_buffer(size2);
      for (p=q->v_list,j=0;p!=NULL;p=p->next,j++)
        fbuf[i][j] = p->fvalue;
  return(fbuf);
public
float_buffer *libc_util float lists3buffer(
      int size1,
      int size2,
     int size3,
     libc_float_list_list *f lists)
{ int i,j,k;
 libc_float list list *q;
 libc_float_list rec *p;
 float_buffer *fbuf;
 q = f_lists;
 p = q->v_list;
 if (size2 == 0) {
                            /* 1D array */
   fbuf = (float_buffer *) get float buffer(size1);
   for (i=0;q!=NULL;q=q->next) {
     for (p=q->v_list;p!=NULL;p=p->next,i++)
       fbuf[i] = p->fvalue;
 fbuf = (float_buffer *) get_float_buffer(size1 * size2);
   for (i=0;i<size1;i++) {
     for (j=0;j<size2;j++) {
       fbuf[i*size2+j] = (p!= NULL)? p->fvalue : 0.0;
     p = p->next;
     if (p == NULL && q != NULL) {
       q = q->next;
       p = (q != NULL)? q->v_list : NULL;
 else {
                       /* 3D array */
   fbuf = (float_buffer *) get_float_buffer(size1 * size2 * size3);
   for (i=0;i<size1;i++) {
     for (j=0; j < size2; j++) {
       for (k=0;k<size3;k++) {
```

```
fbuf[(i*size2+j)*size3+k] = (p!= NULL)? p->fvalue : 0.0;
       p = p->next;
       if (p == NULL && q != NULL) {
         q = q->next;
         p = (q != NULL)? q->v list : NULL;
    }
  free_libc_float_list_list(f_lists);
  return(fbuf);
public
libc_bool_opr_rec *libc_util_bool_opr(
     libc_bool_type E op type,
     int value)
{ libc_bool_opr_rec * bor;
  bor = new_libc_bool_opr_rec();
 bor->type = op_type;
  if (op_type == ID_B)
   bor->u.id_name = (text buffer *) value;
   bor->u.value = value;
 return(bor);
void libc_util_wire_load fanout(
     int fanout,
     float length,
     float ave cap,
     float std dev,
     int no_of_net)
{ libc_fanout_length_rec *new, *p, *prev;
 new = new_libc_fanout_length rec();
              = fanout;
 new->fanout
 new->length
                 = length;
 new->area
                = lib_wire_load->area;
 new->capacitance = lib_wire load->capacitance;
 new->resistance = lib_wire_load->resistance;
 new->ave_cap
                = ave_cap;
 new->std dev
               = std_dev;
 new->no_of_net = no_of_net;
 /* ---- sort by fanout (small->large) */
 for (p=lib_wire_load->fanout_length,prev=NULL;p!=NULL;prev=p,p=p->next) {
   if (p->fanout >= new->fanout)
     break;
 }
```

```
new->next = p;
  if (prev == NULL)
    lib_wire_load->fanout_length = new;
   prev->next = new;
  ----- */
public
void libc_util_wire_load_table(
      int fanout,
      int field, /* 1: length, 2: capacitance, 3: resistance, 4: area */
     float value)
{ libc_fanout_length_rec *new, *p, *prev=NULL;
  /* ---- search first */
  for (p=lib_wire_load->fanout_length;p!=NULL;prev=p,p=p->next) {
    if (p->fanout == fanout)
     goto insert value;
   if (p->fanout > fanout)
     break;
  /* ---- insert a new entry (fanout from small->large) */
  new = new_libc_fanout_length_rec();
 /* default value */
 new->capacitance = lib_wire_load->capacitance;
                                                  /* default value
  if (prev == NULL)
   lib_wire_load->fanout_length = new;
 else
  prev->next = new;
 new->next = p;
 p = new;
insert value :
 switch (field) {
   case 1 : p->length = value;
   case 2: p->capacitance = value;
case 3: p->resistance = value;
case 4: p->area = value;
break;
default: assert(0);
                                         break;
                                         break;
                                      break;
/* ================ */
public
void libc_util_wl_select(
     float min_area,
     float max area,
     text buffer *name)
{ libc_wire_load rec *p;
 libc_wire_load_from_area_rec *wap,*prev,*new;
 char msg[100];
```

```
for (p=tech_lib->wire_load;p!=NULL;p=p->next) {
  if (strcmp(p->wl_name,name) == 0) {
   free_text_buffer(name);
   new = new_libc_wire_load_from_area_rec();
   new->min_area = min_area;
   new->max_area = max area;
   new->_load_model = p;
   /* ---- sort by area (and each group shall not overlap) */
   for (wap=lib_wire_load_sel->area_table,prev=NULL;
      wap!=NULL;
      prev=wap, wap=wap->next) {
     if (wap->min_area >= min_area)
     break;
   new->next = wap;
   if (prev == NULL)
   lib_wire_load_sel->area_table = new;
     prev->next = new;
   return;
sprintf(msg, "wire load model(%s) not found.", name);
libcerror(msg);
free_text_buffer(name);
```

```
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#define YYSTYPE int
#endif
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